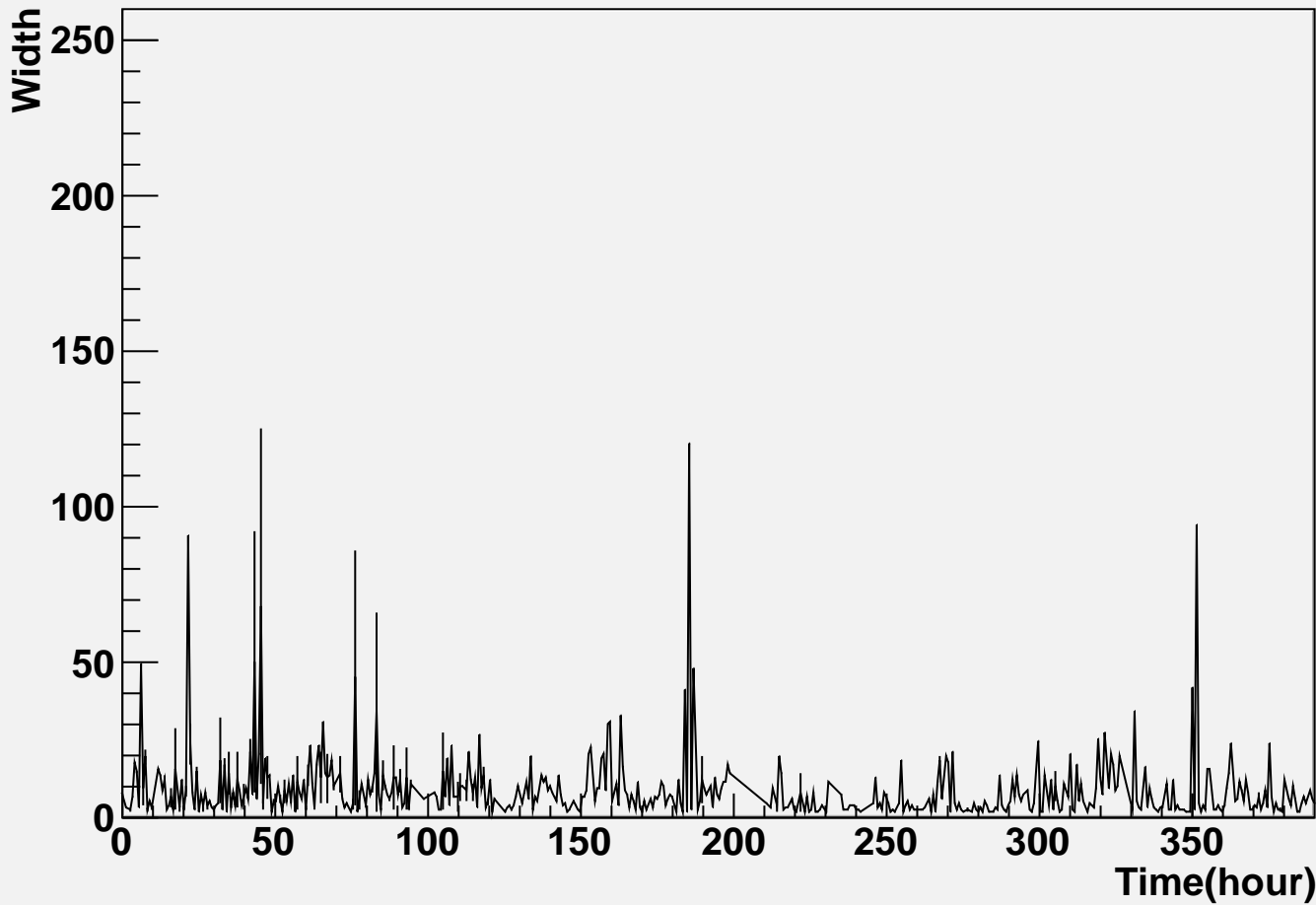
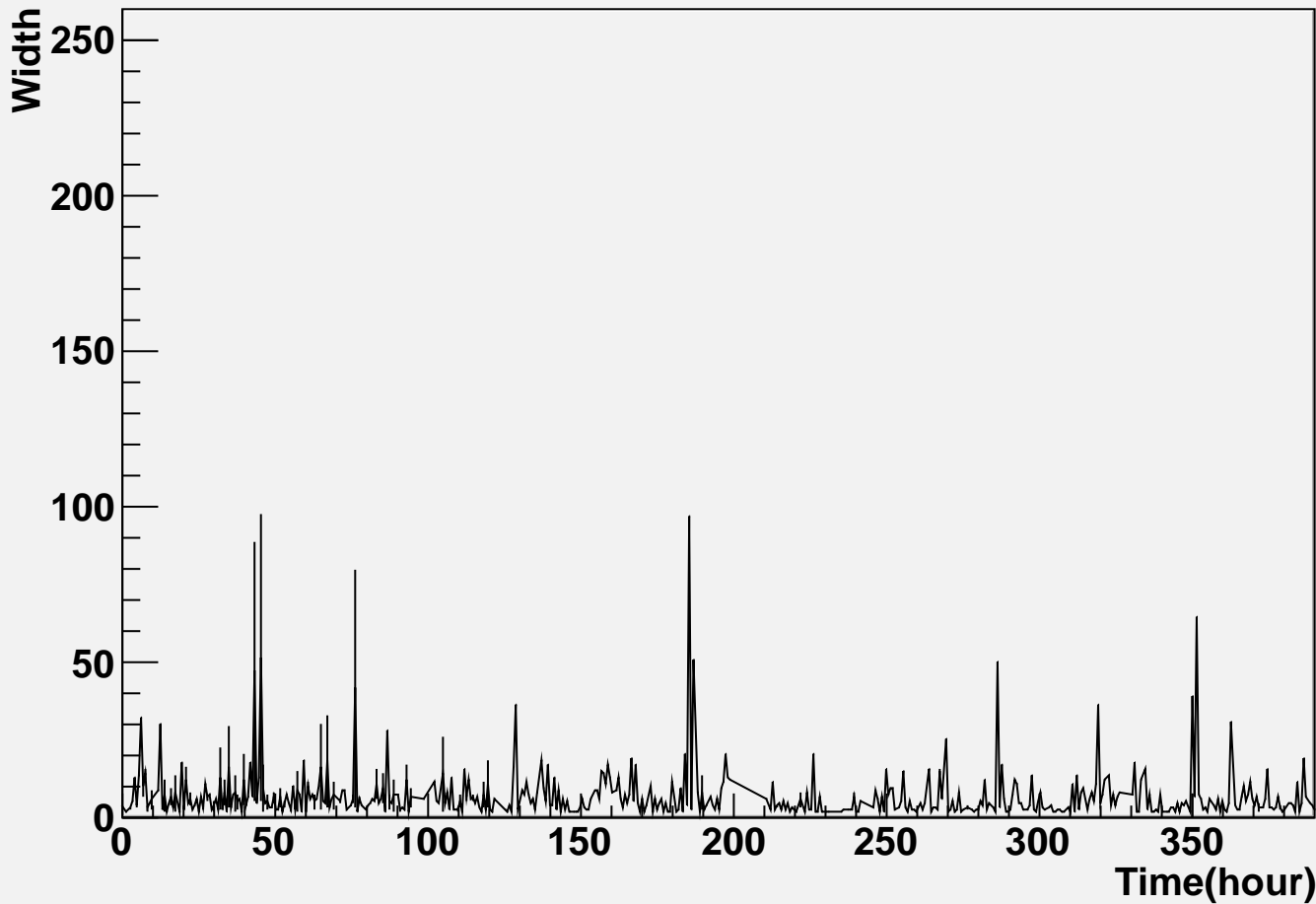


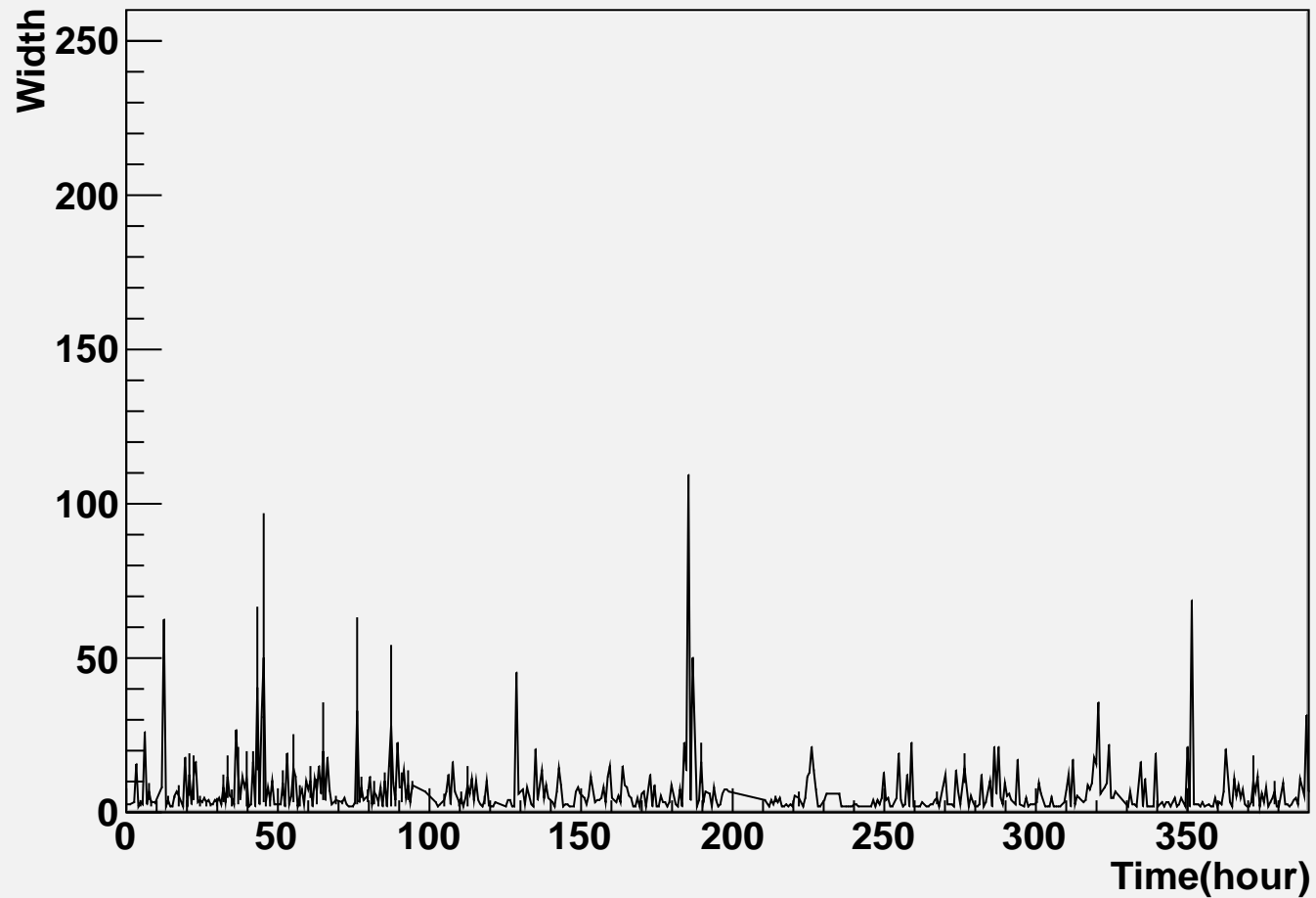
width-S0-SFET2A-Ch0



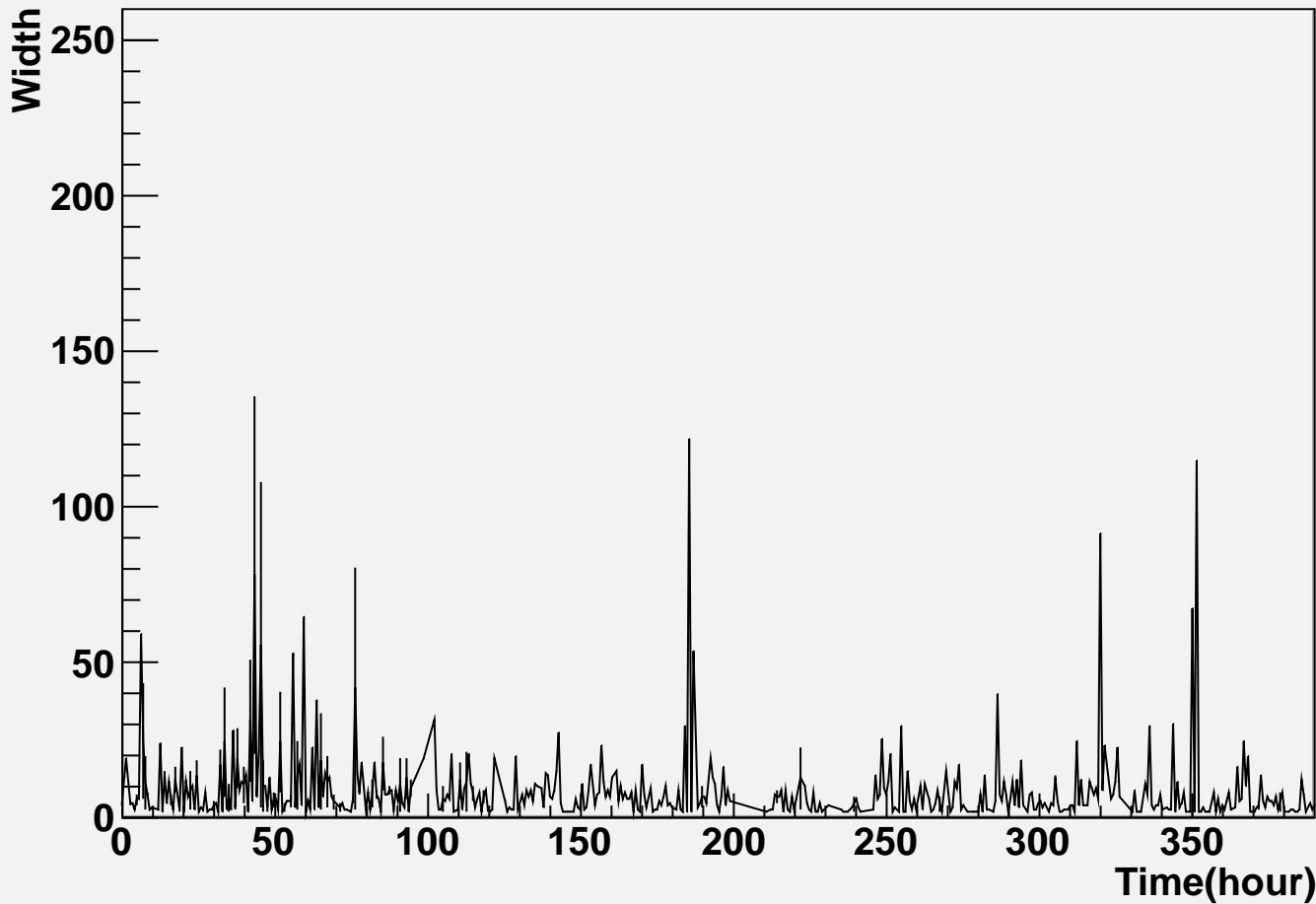
width-S0-SFET2A-Ch1



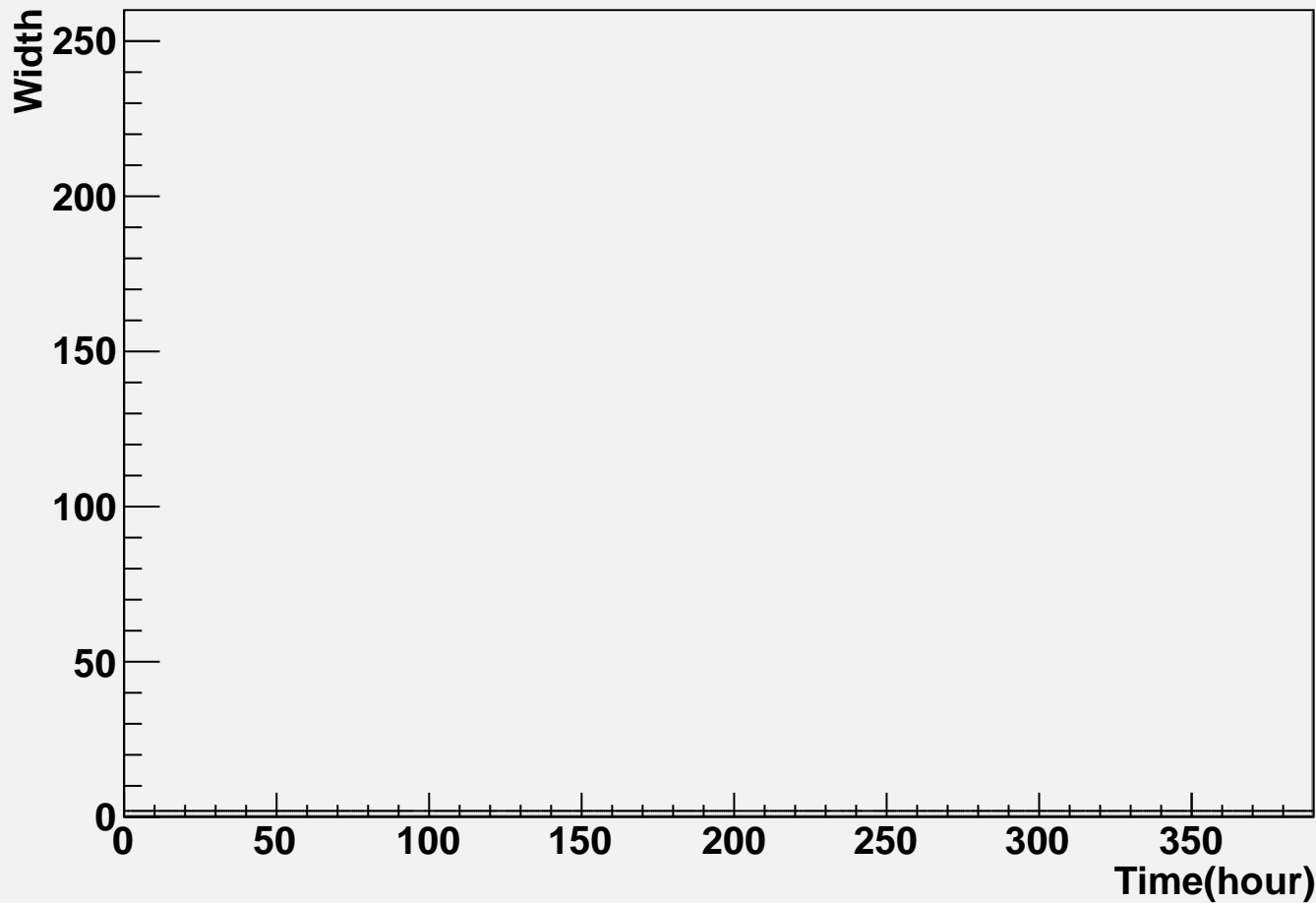
width-S0-SFET2A-Ch2



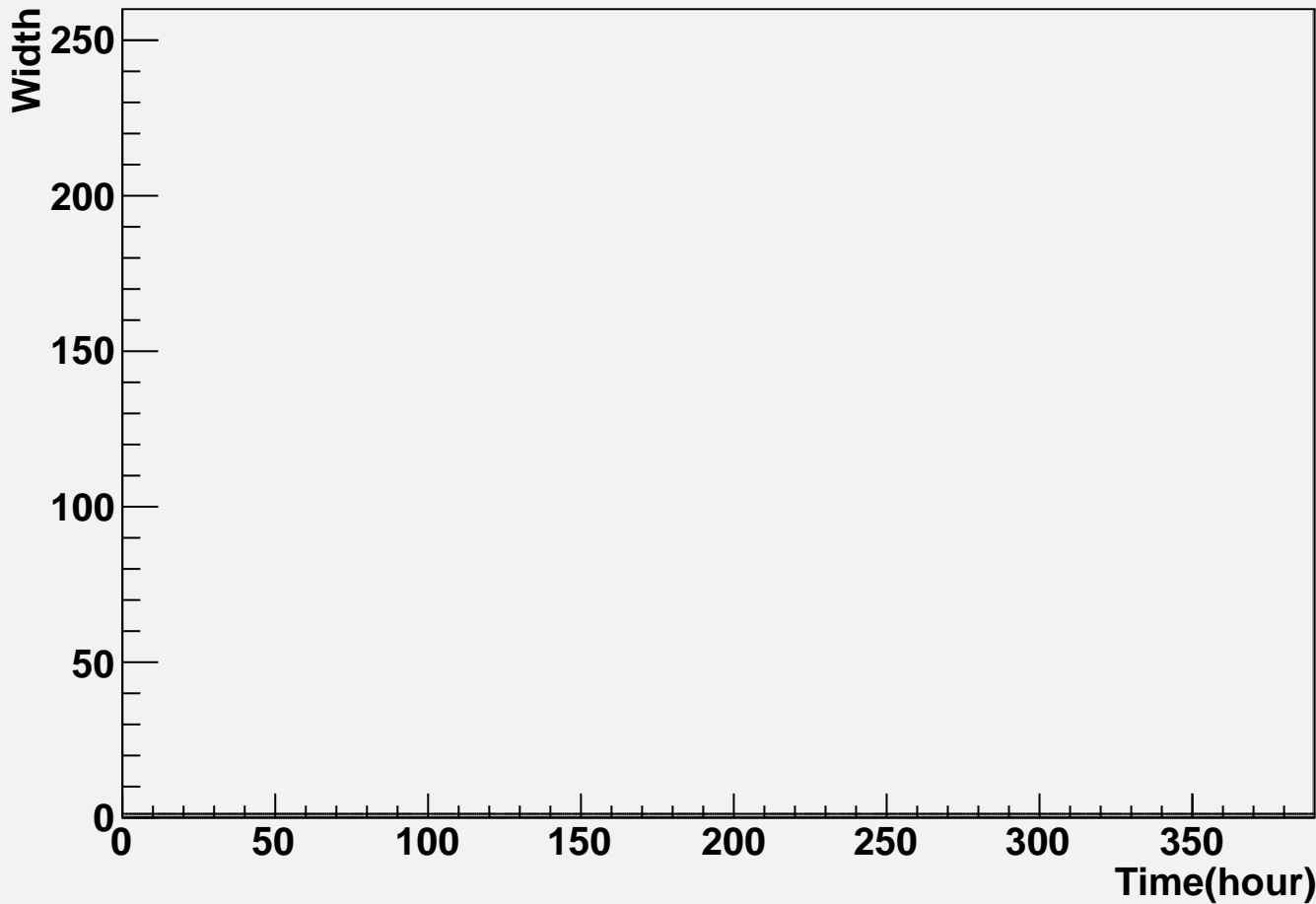
width-S0-SFET2A-Ch3



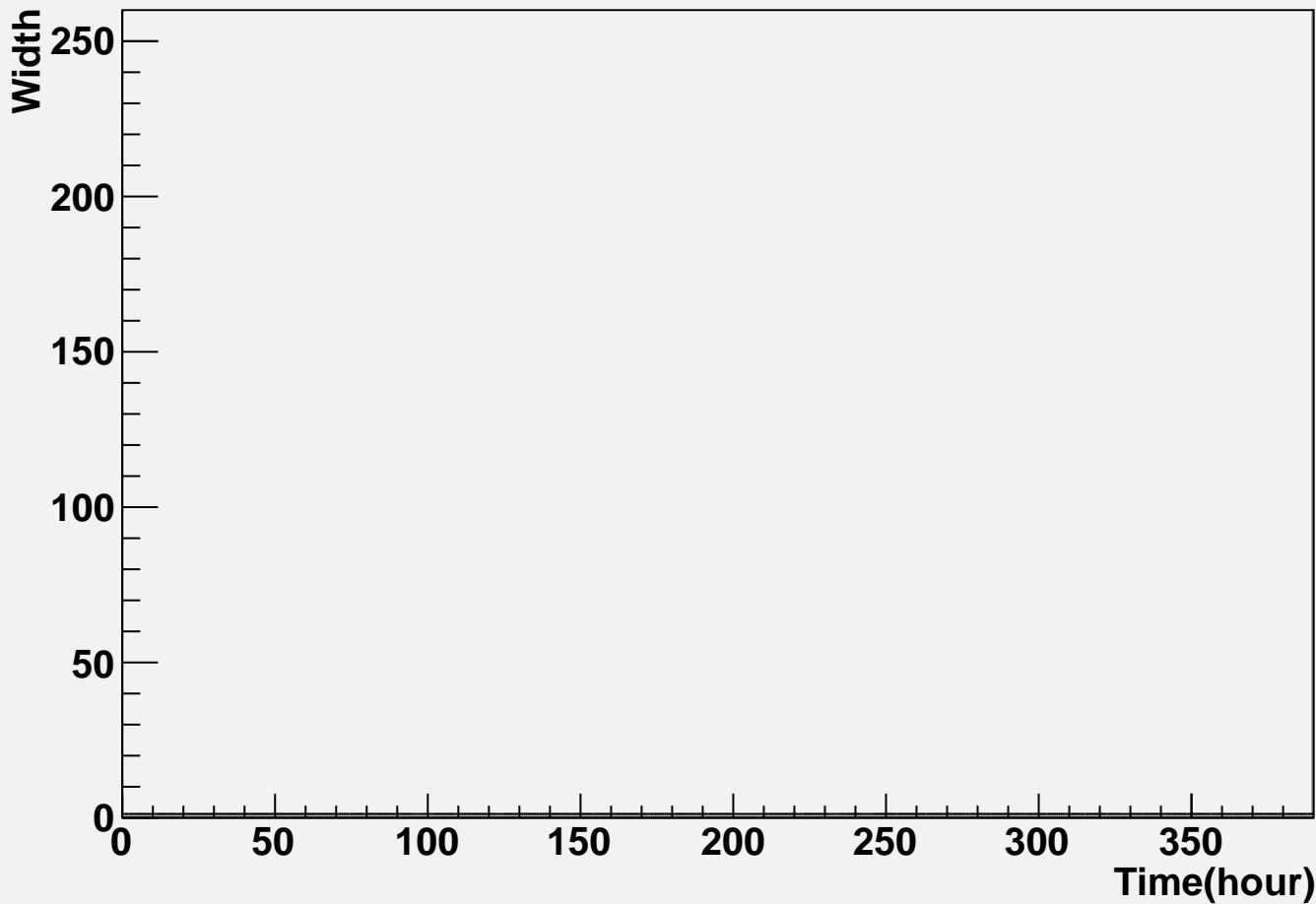
width-S0-SFET2A-Ch4



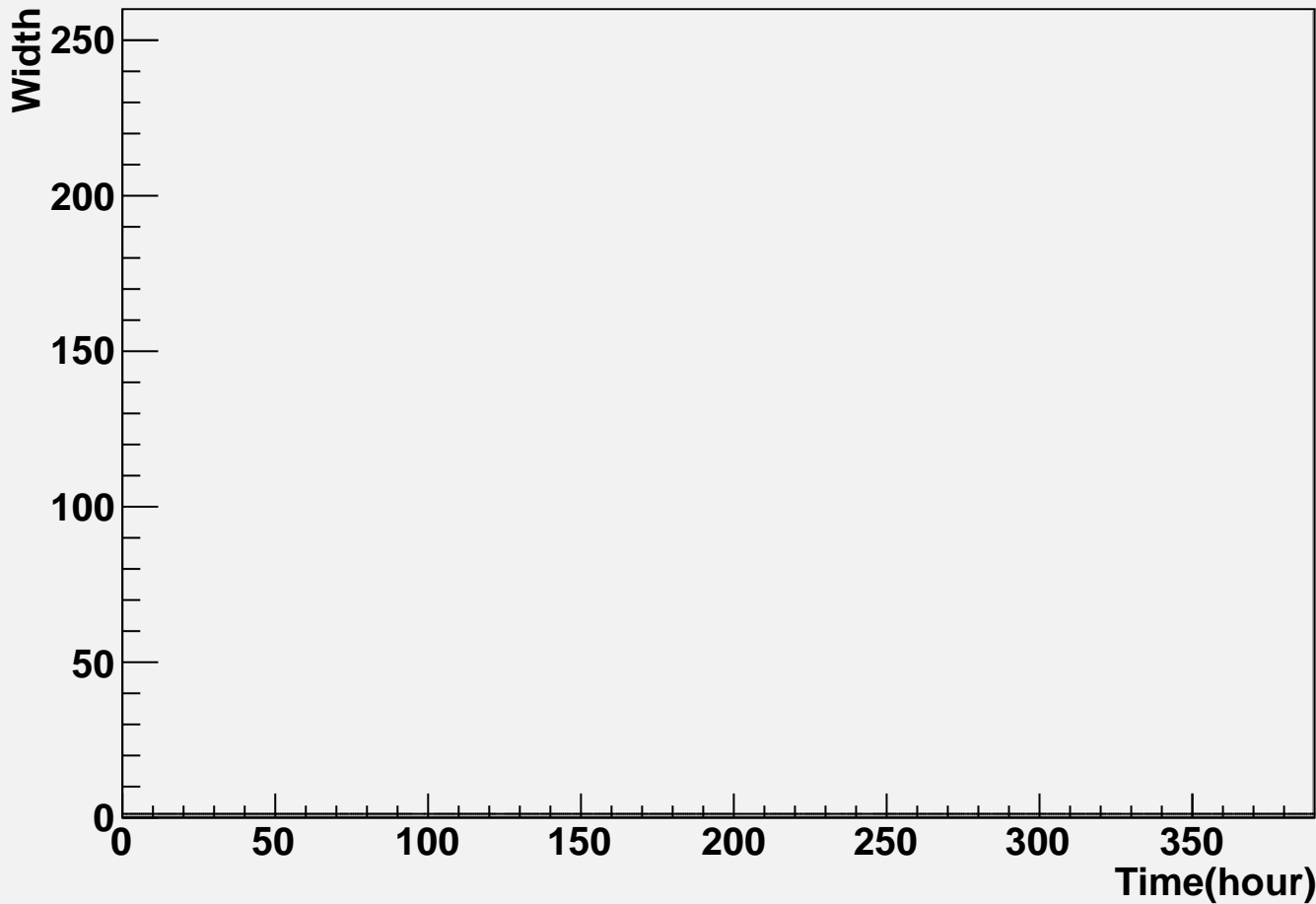
width-S0-SFET2A-Ch5



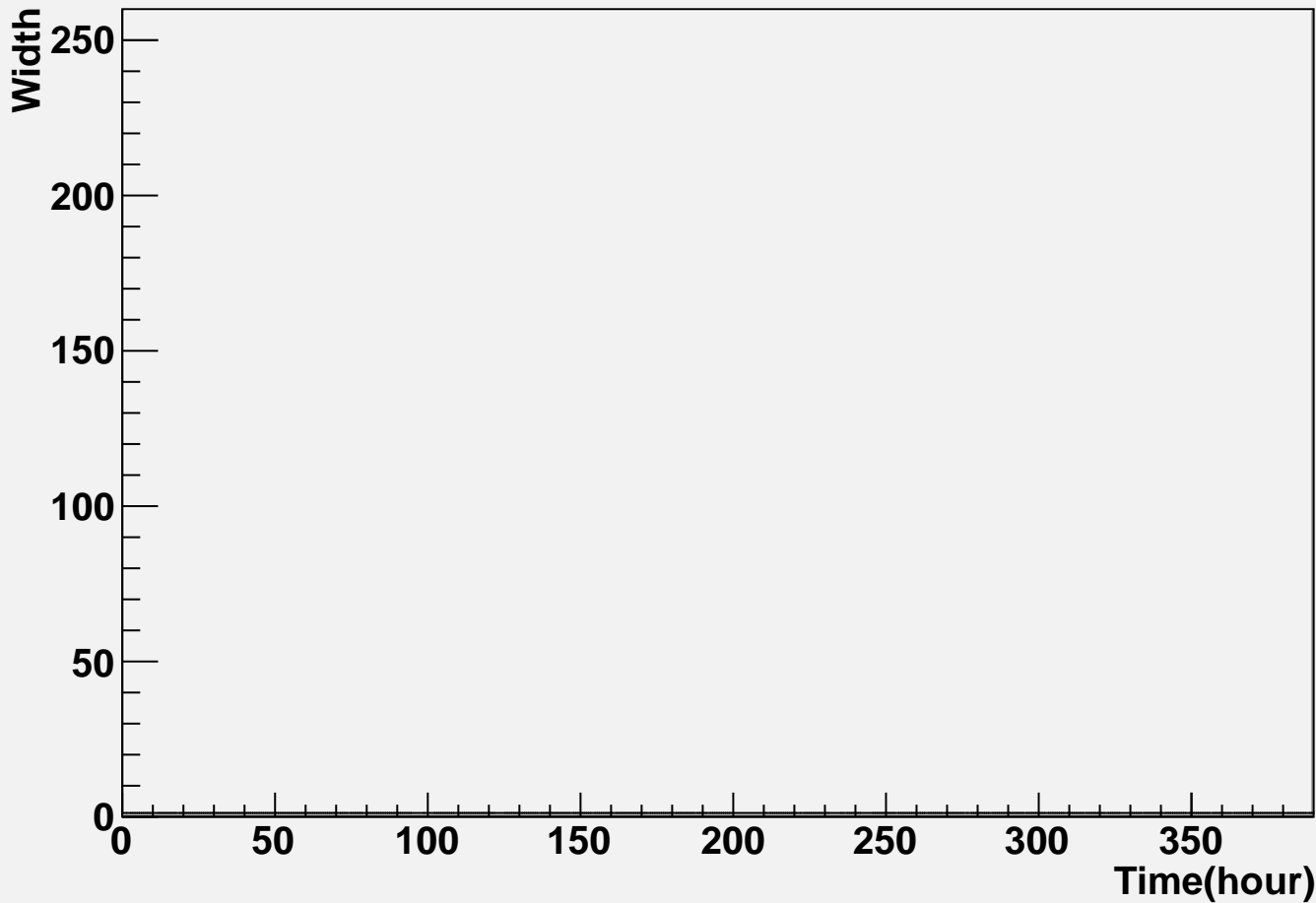
width-S0-SFET2A-Ch6



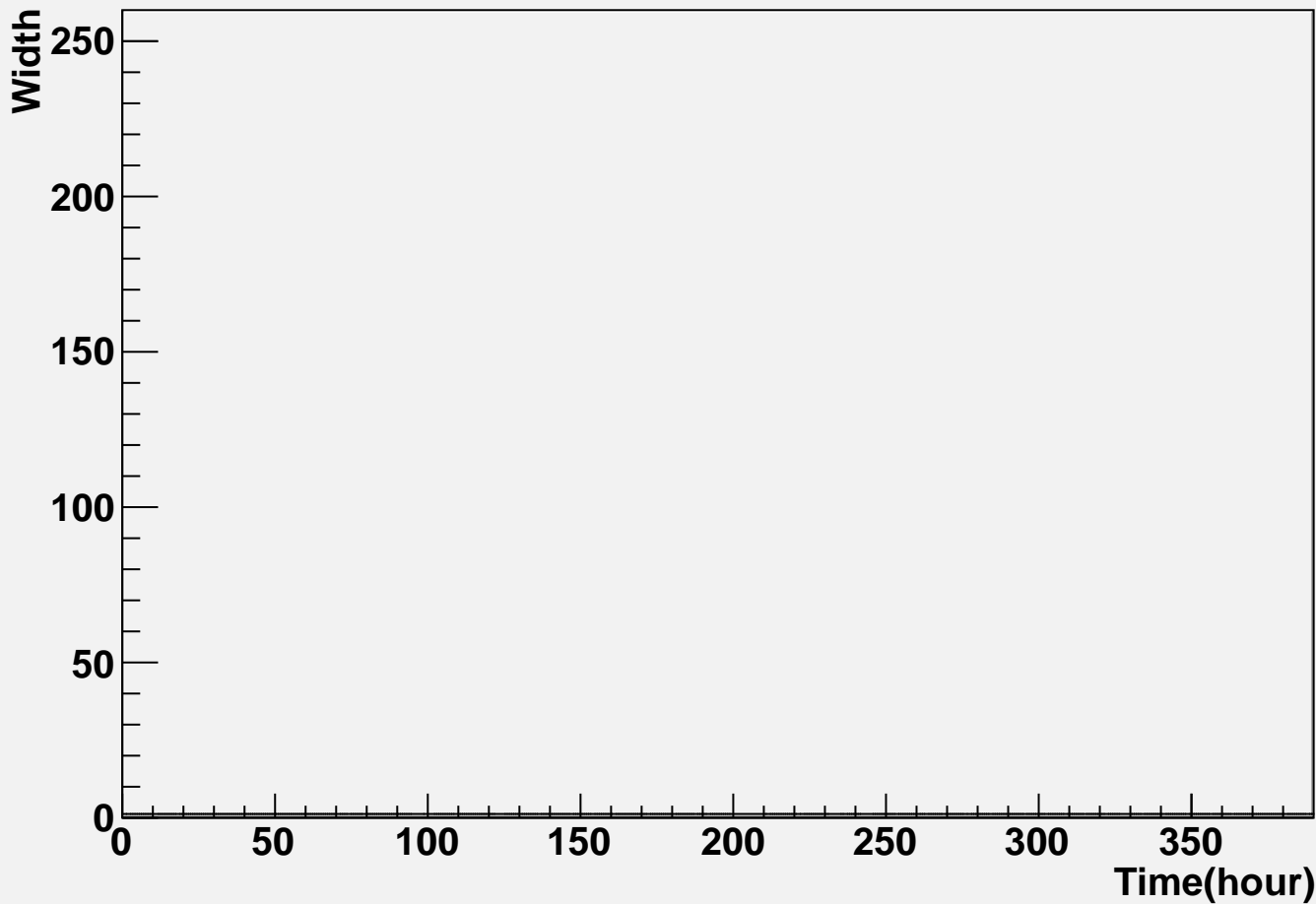
width-S0-SFET2A-Ch7



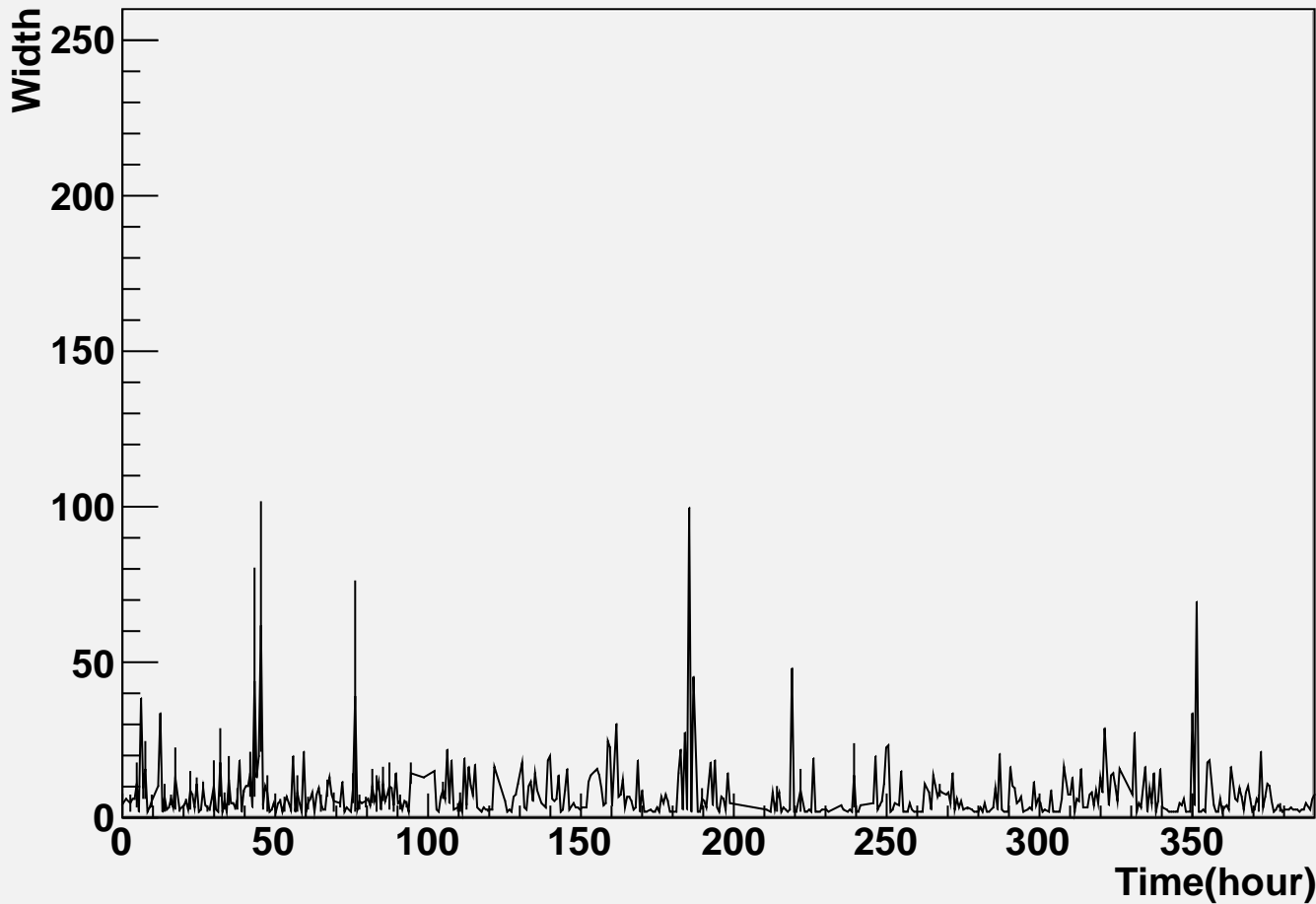
width-S0-SFET2A-Ch8



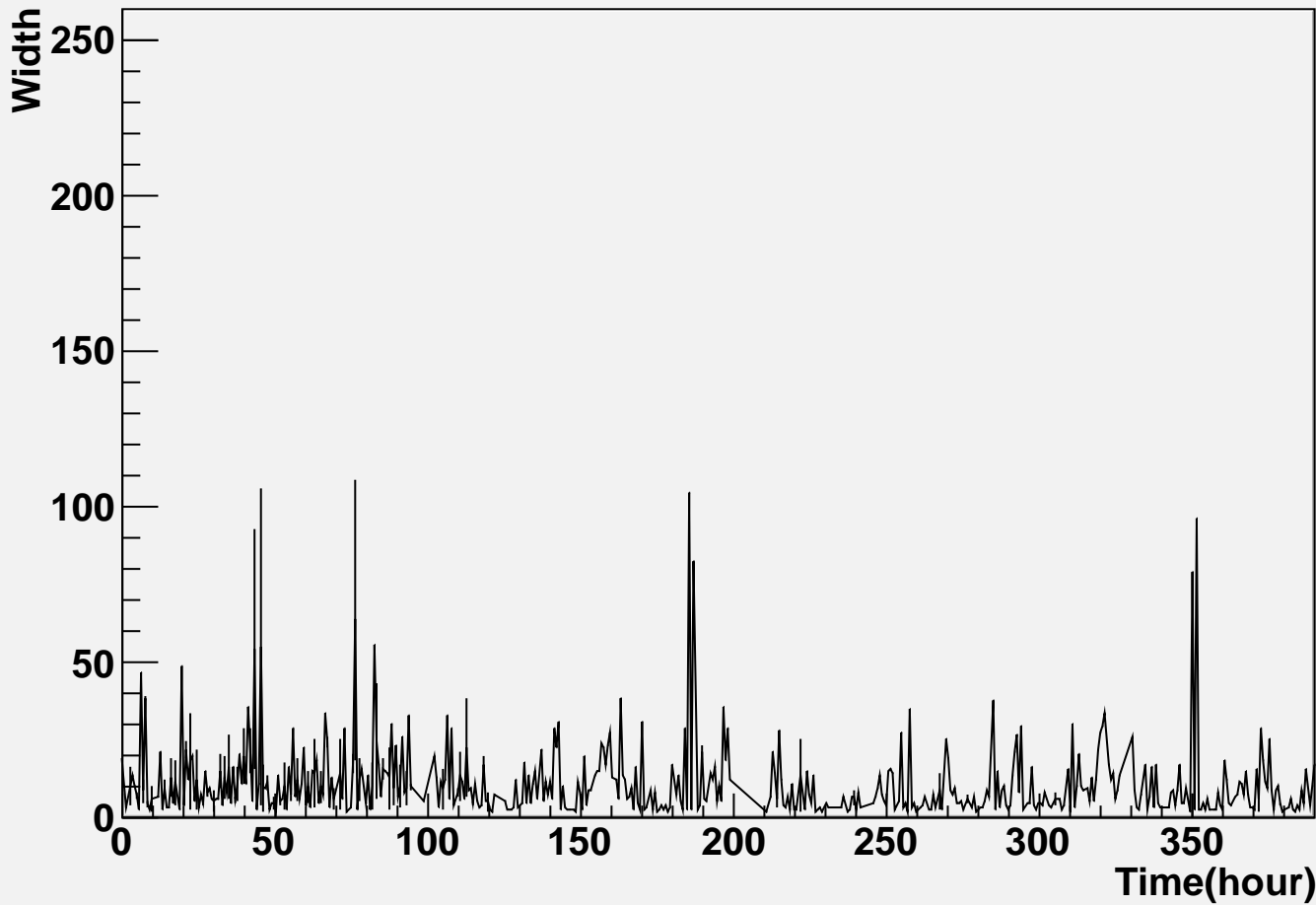
width-S0-SFET2A-Ch9



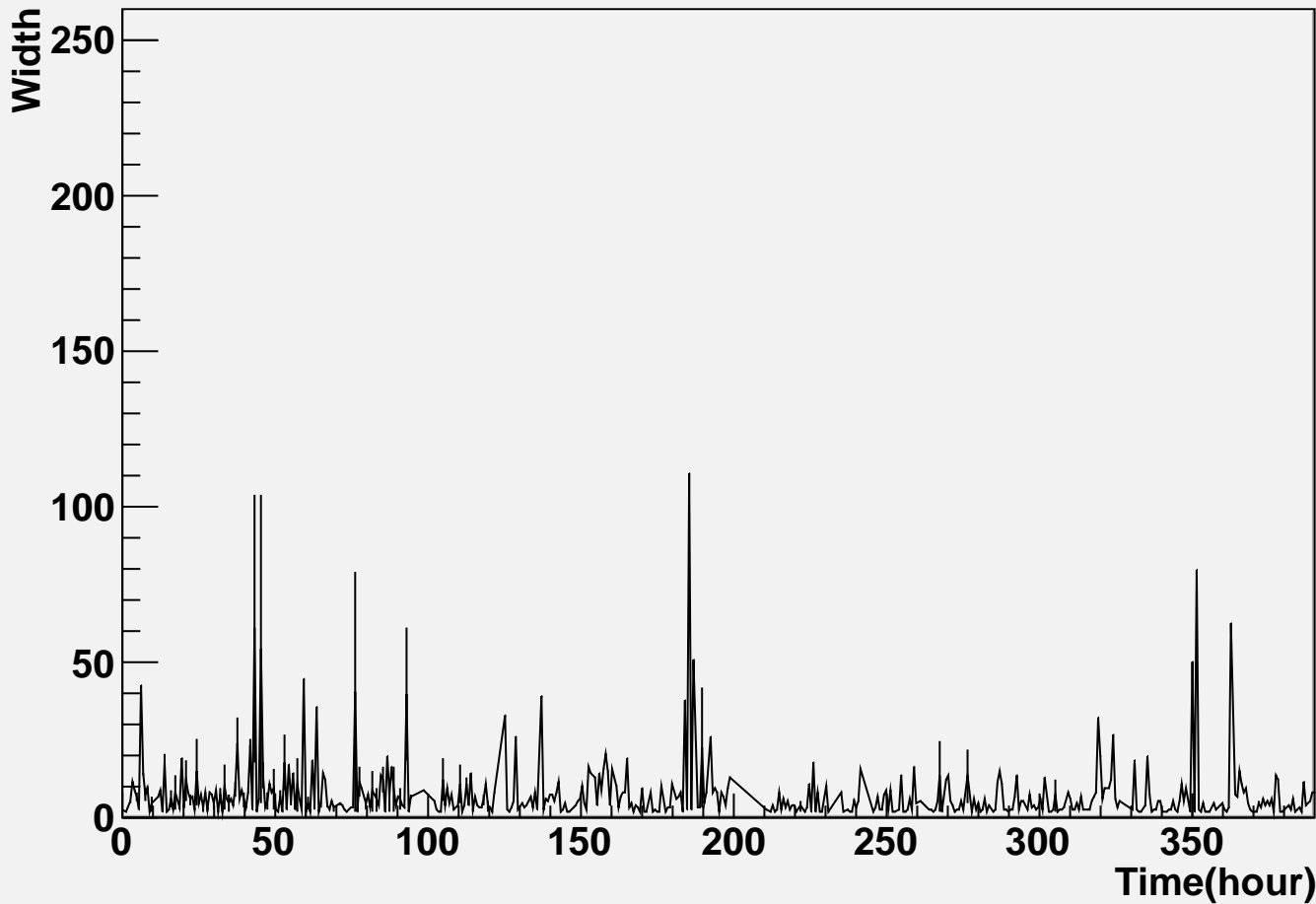
width-S0-SFET2B-Ch0



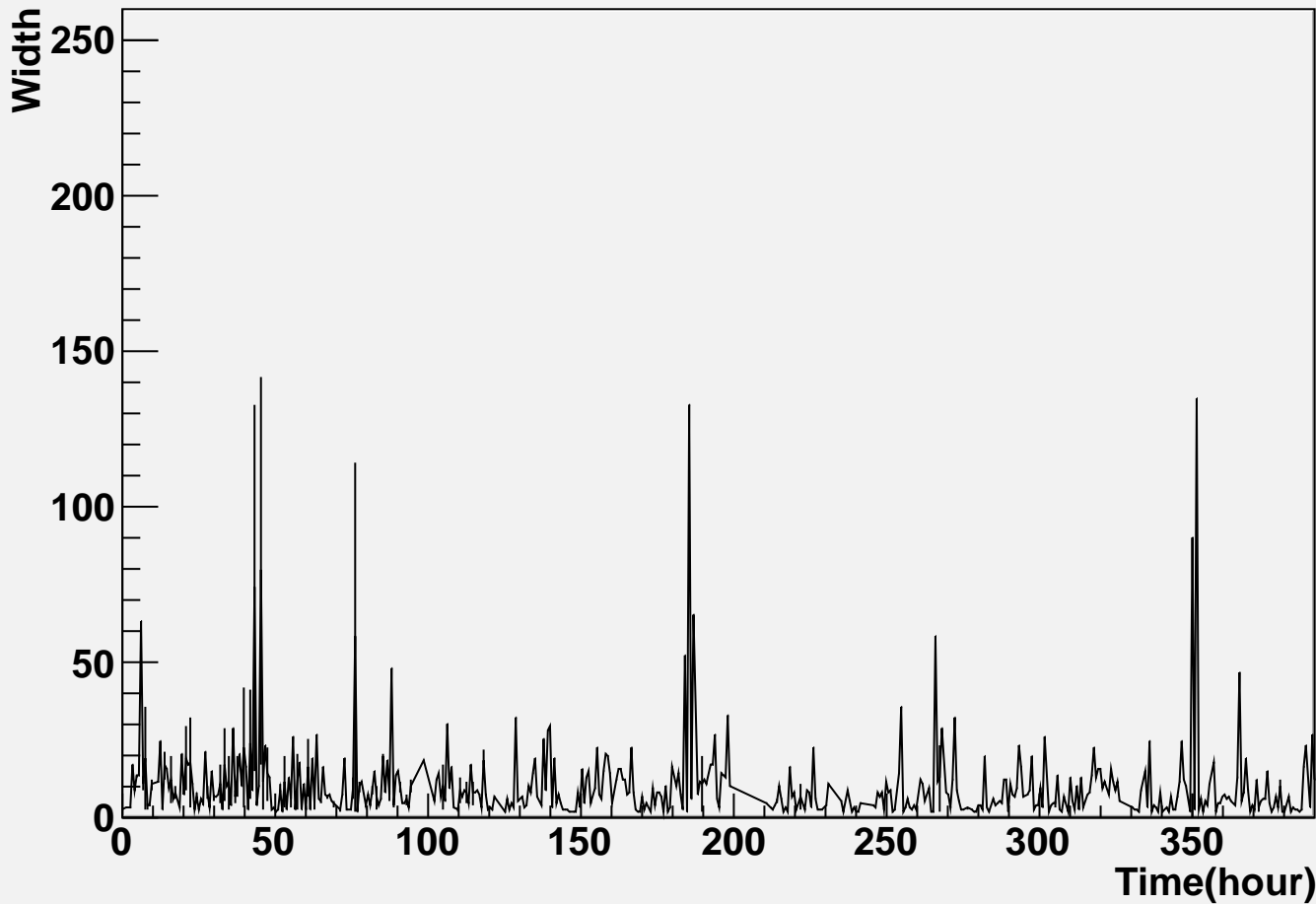
width-S0-SFET2B-Ch1



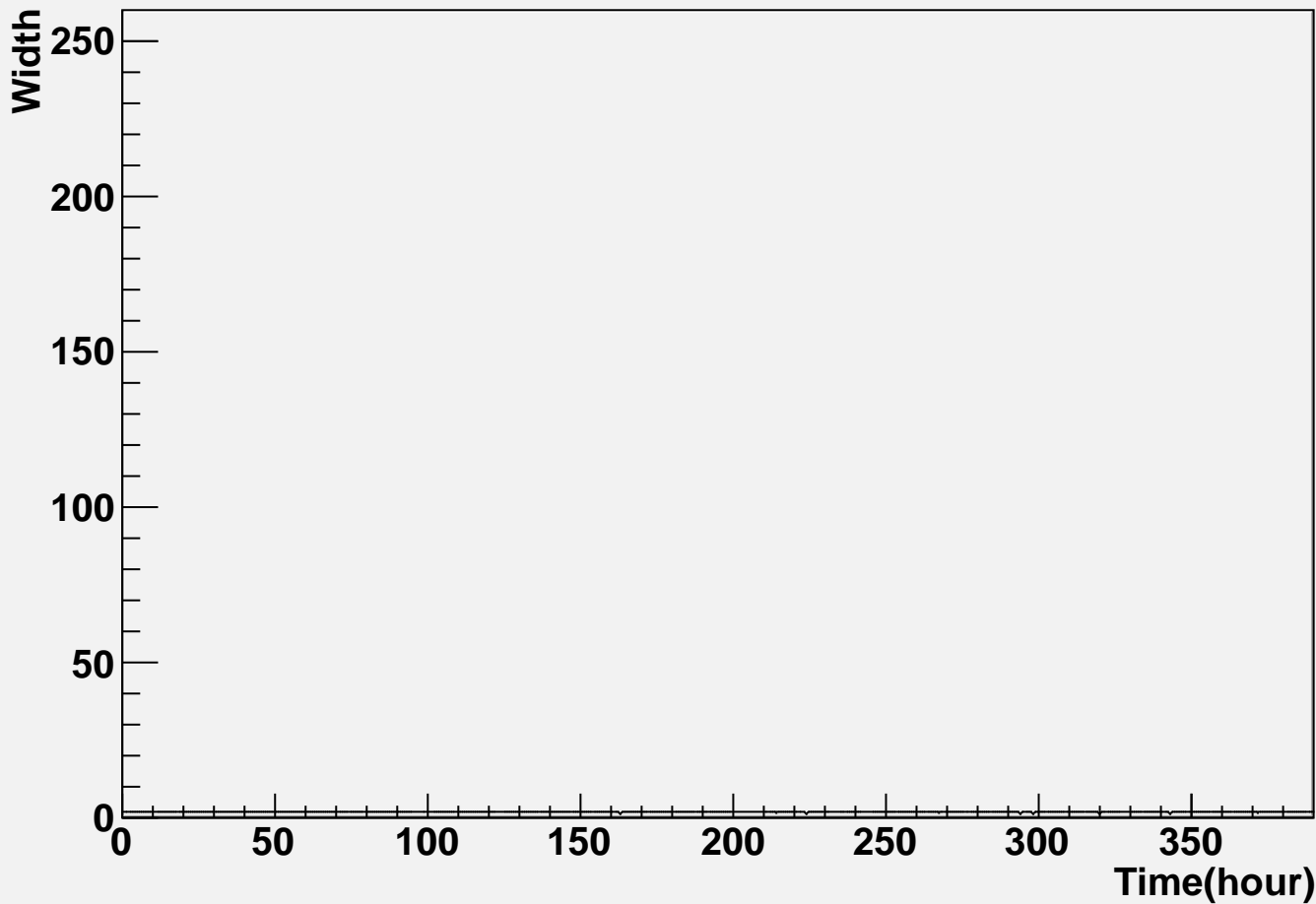
width-S0-SFET2B-Ch2



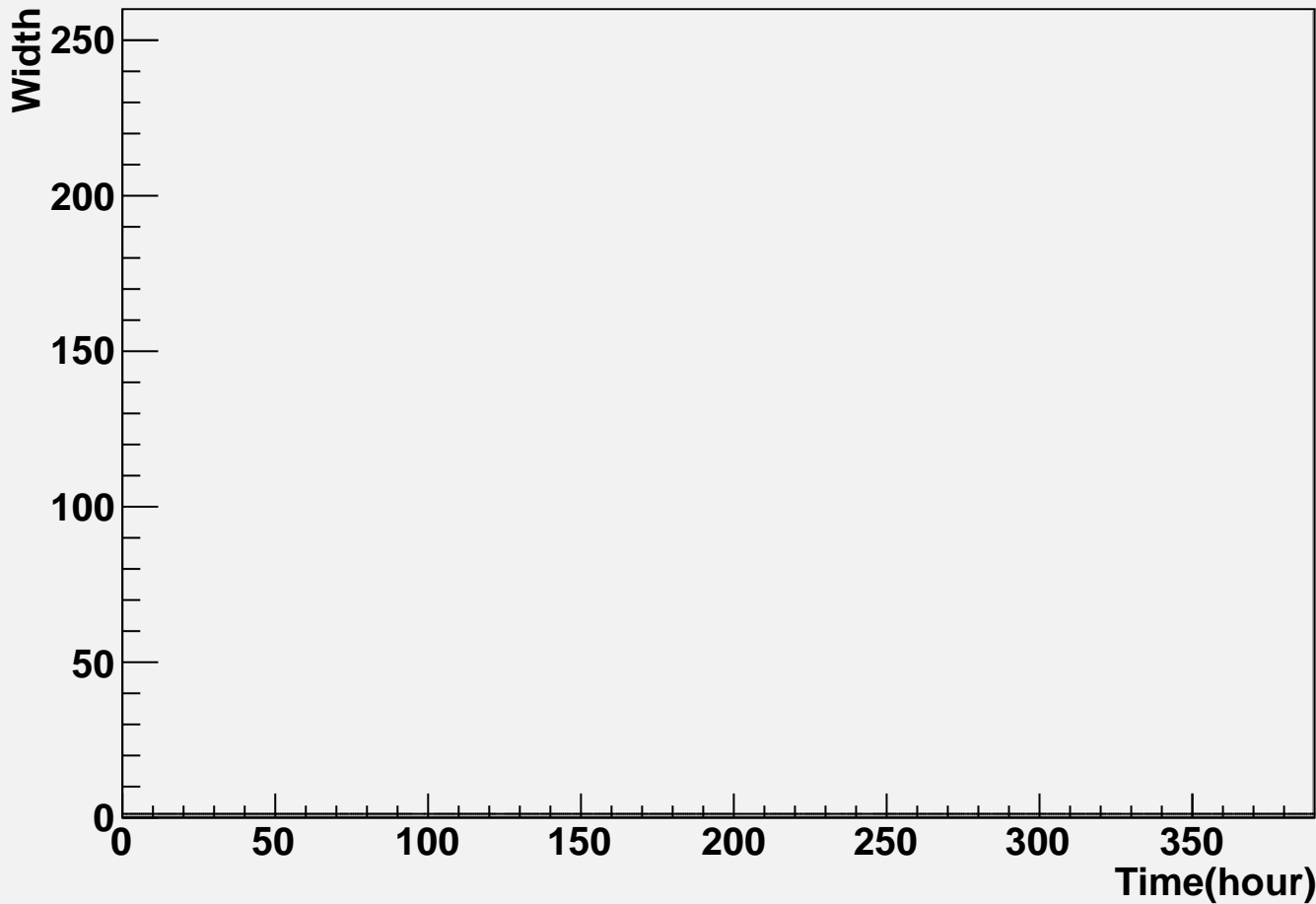
width-S0-SFET2B-Ch3



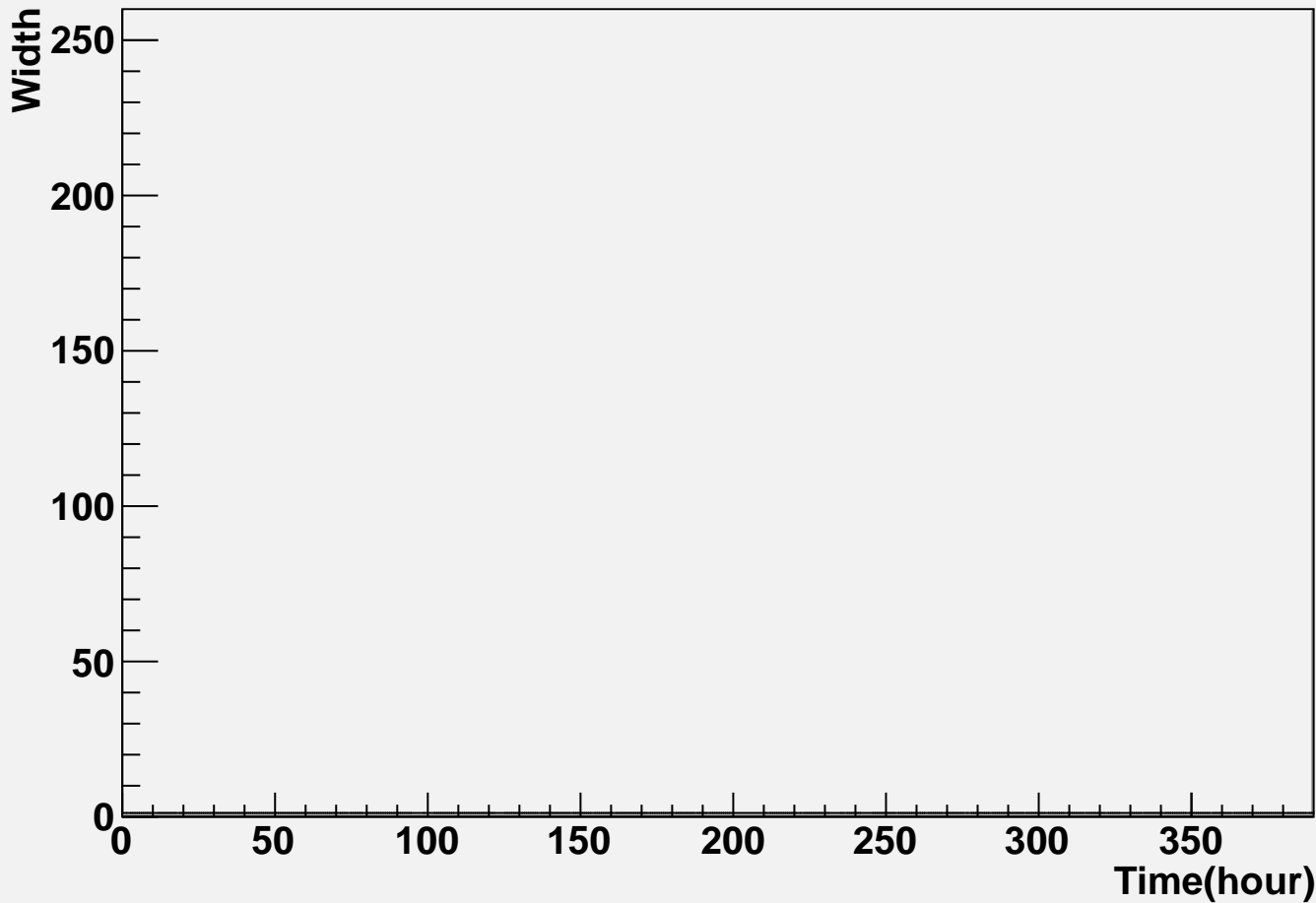
width-S0-SFET2B-Ch4



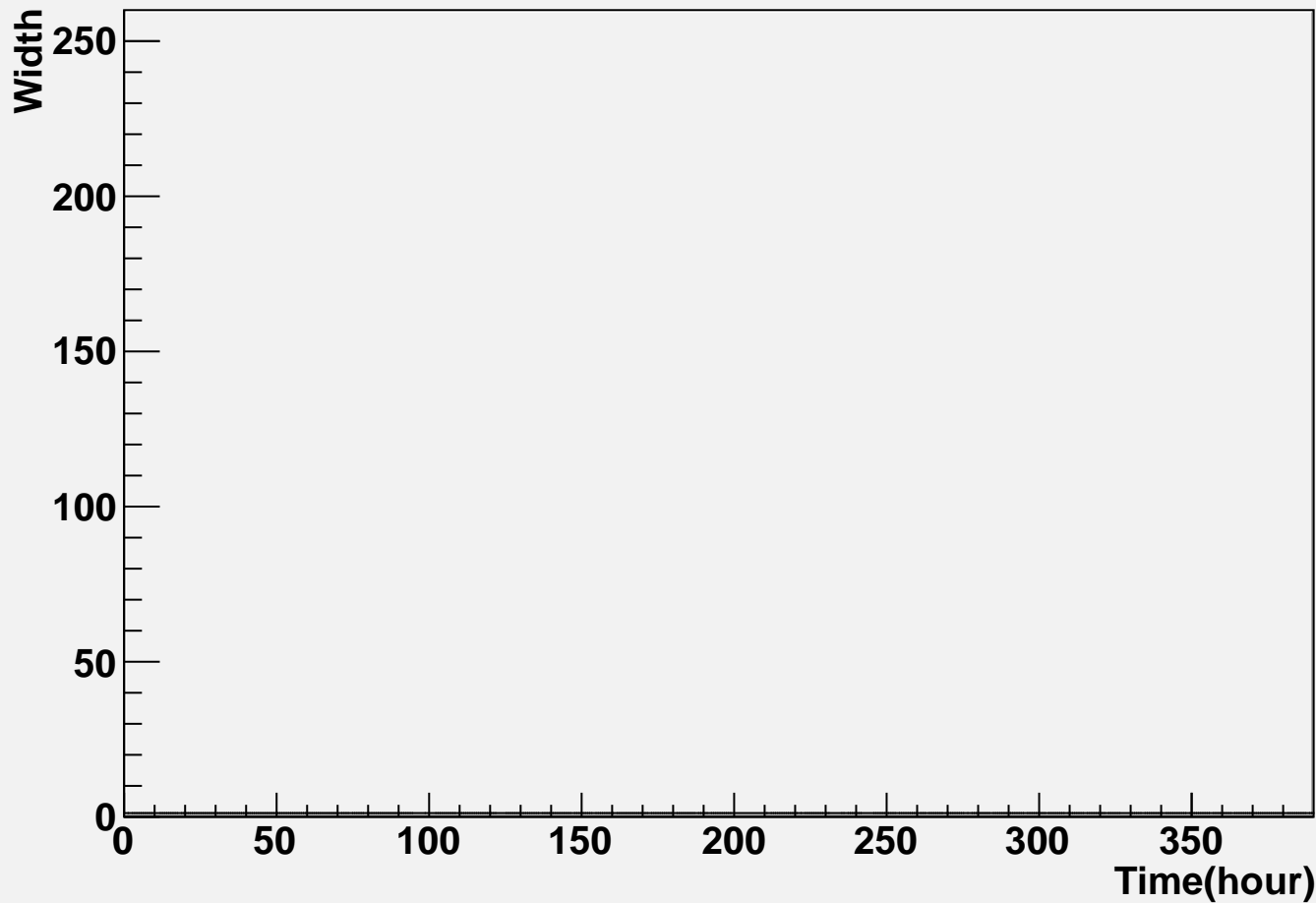
width-S0-SFET2B-Ch5



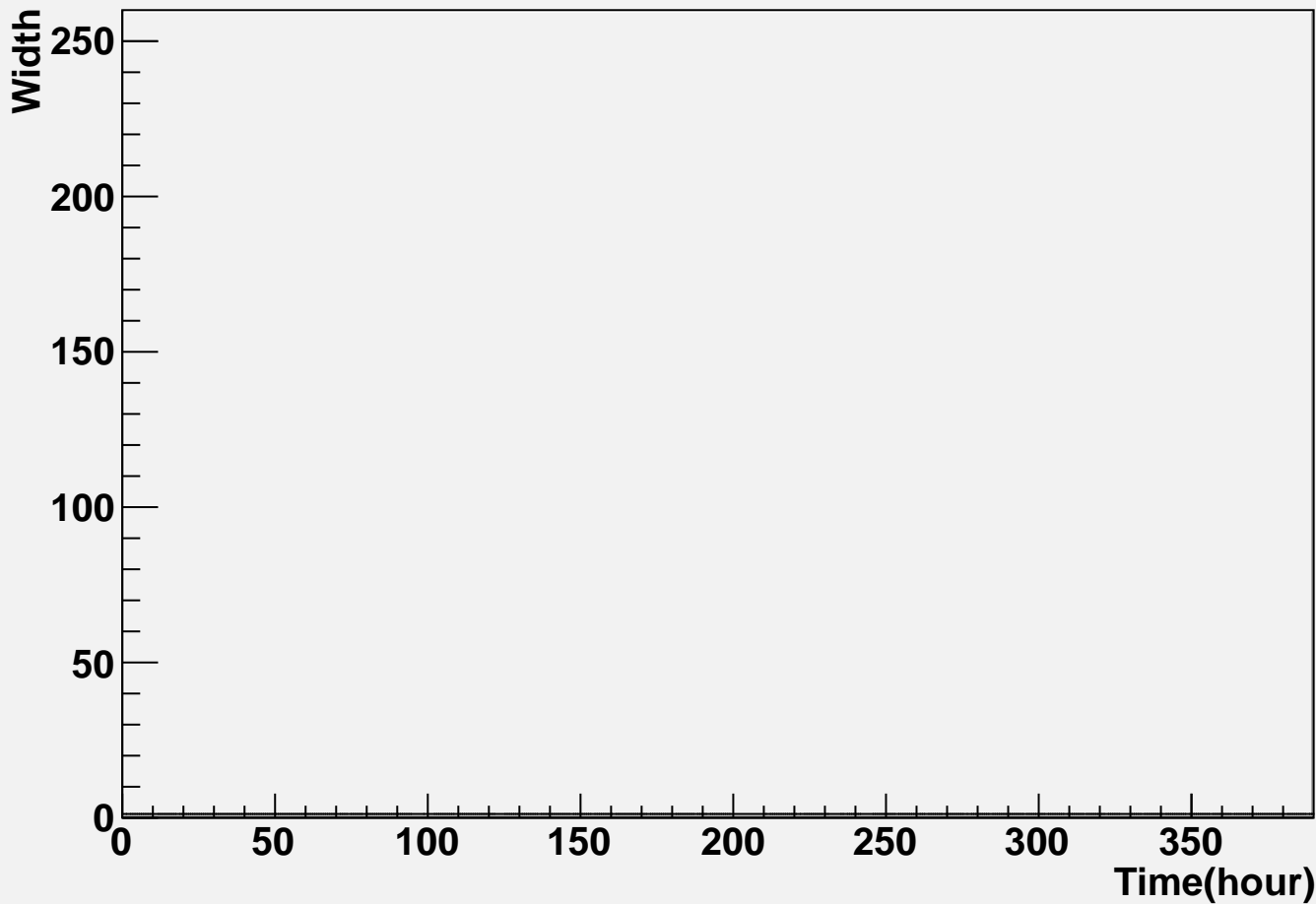
width-S0-SFET2B-Ch6



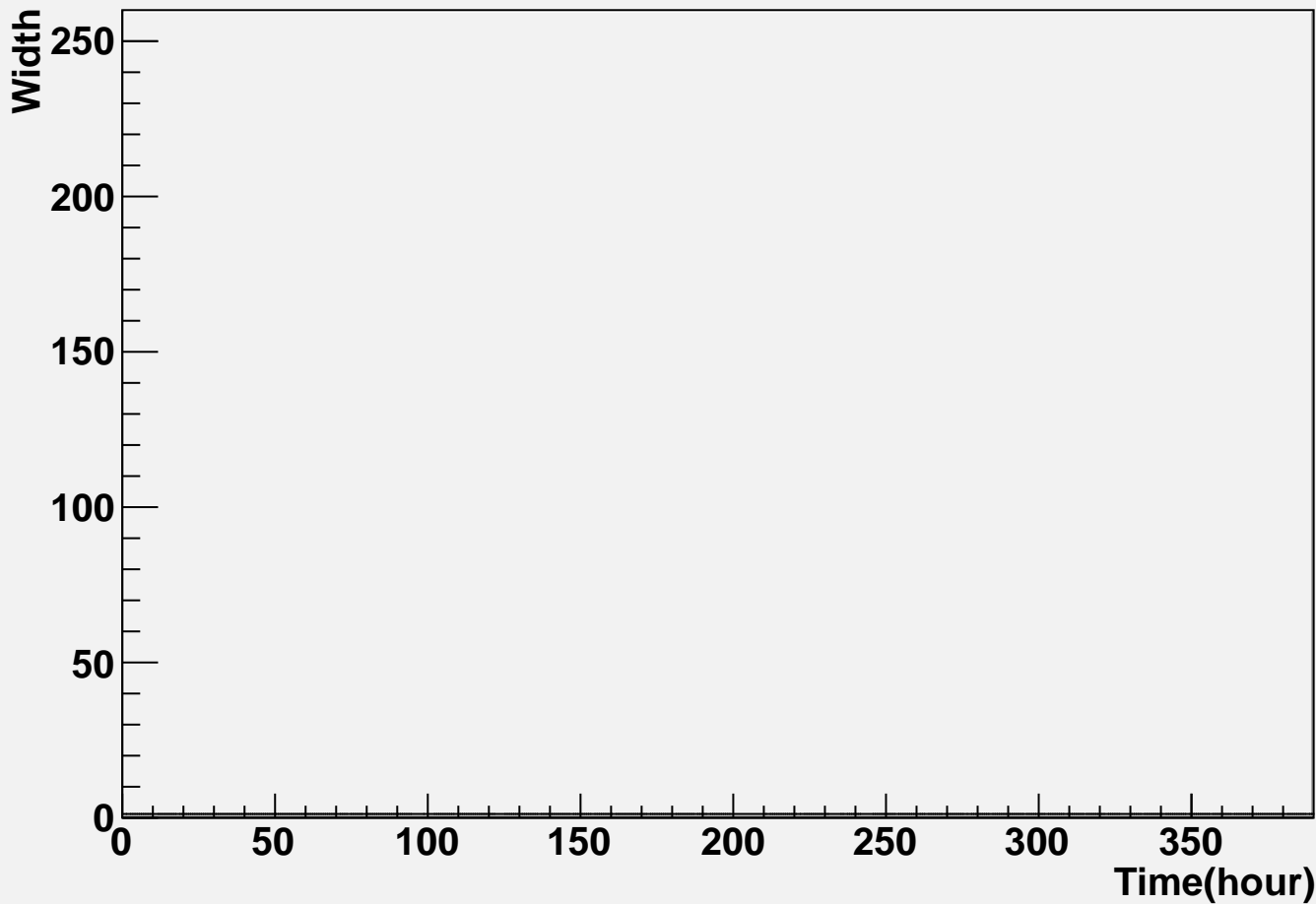
width-S0-SFET2B-Ch7



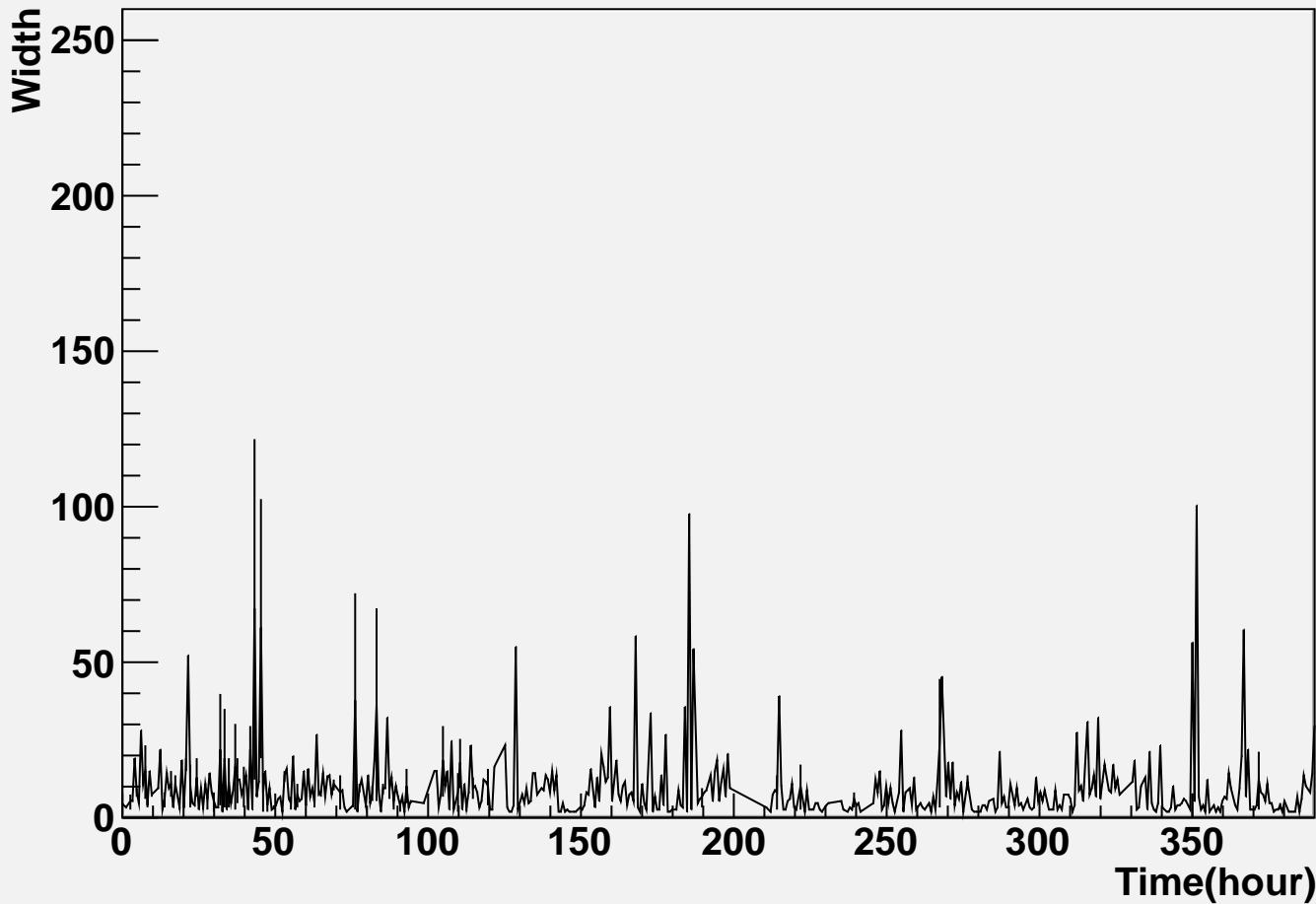
width-S0-SFET2B-Ch8



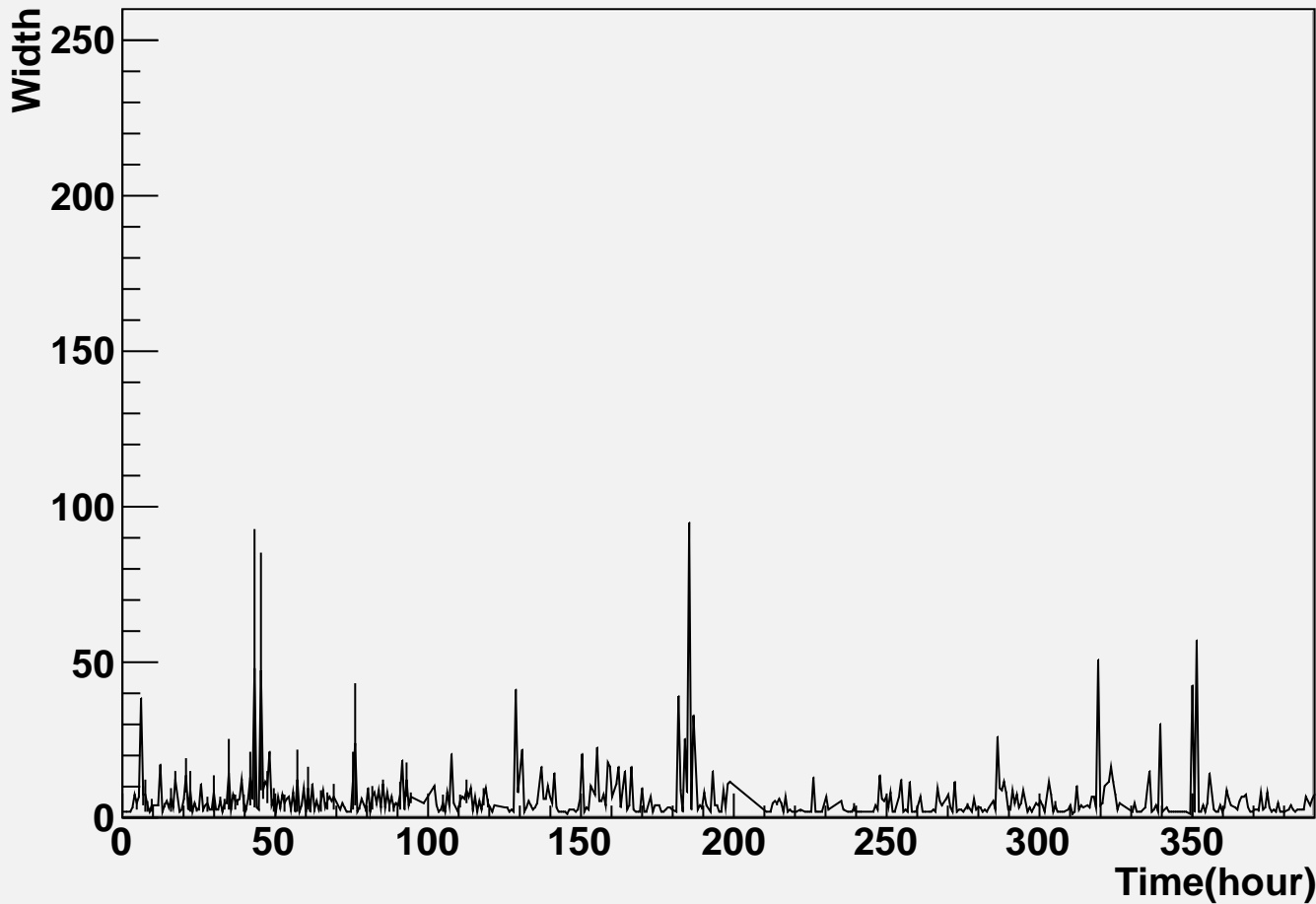
width-S0-SFET2B-Ch9



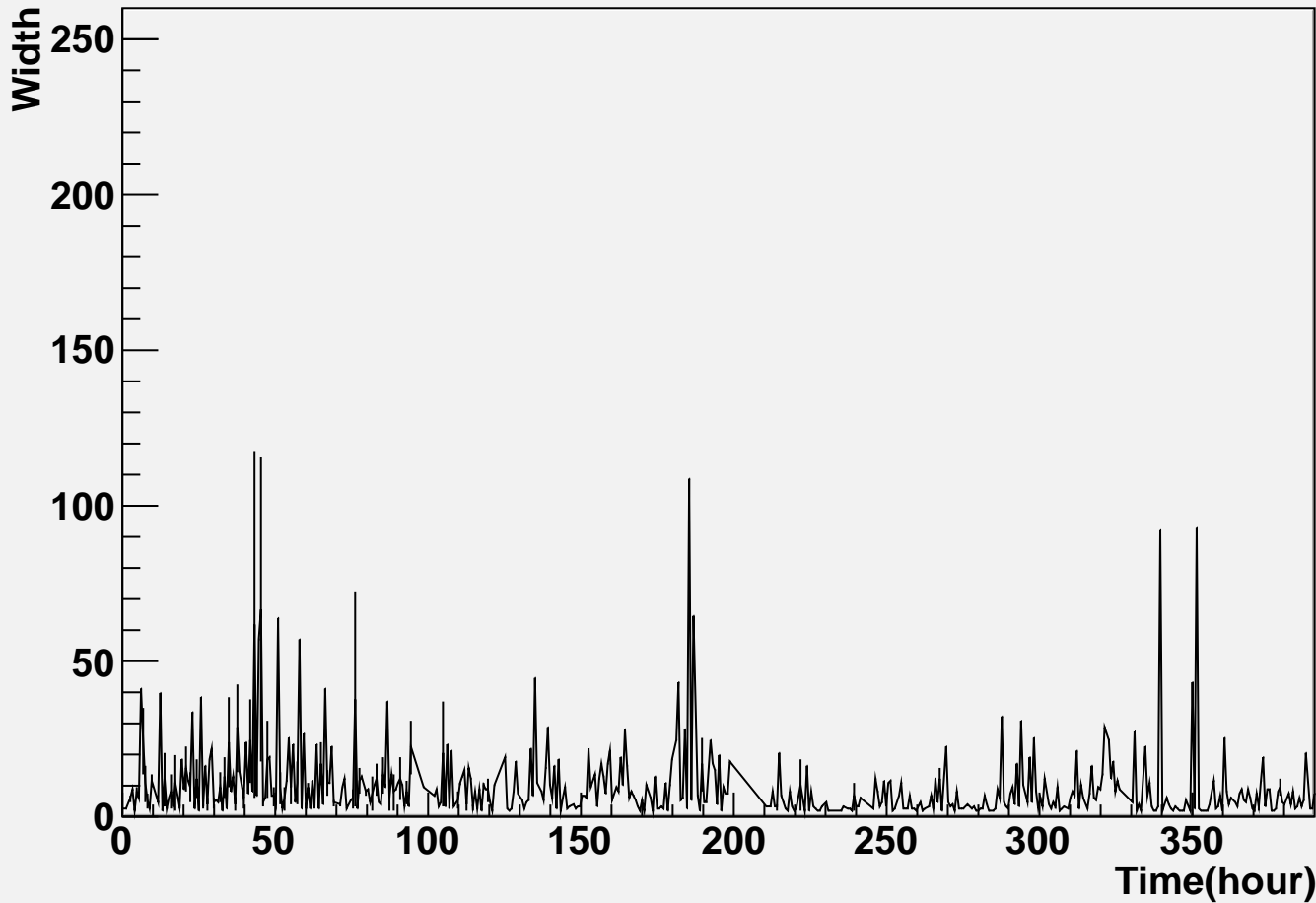
width-S0-SFET2C-Ch0



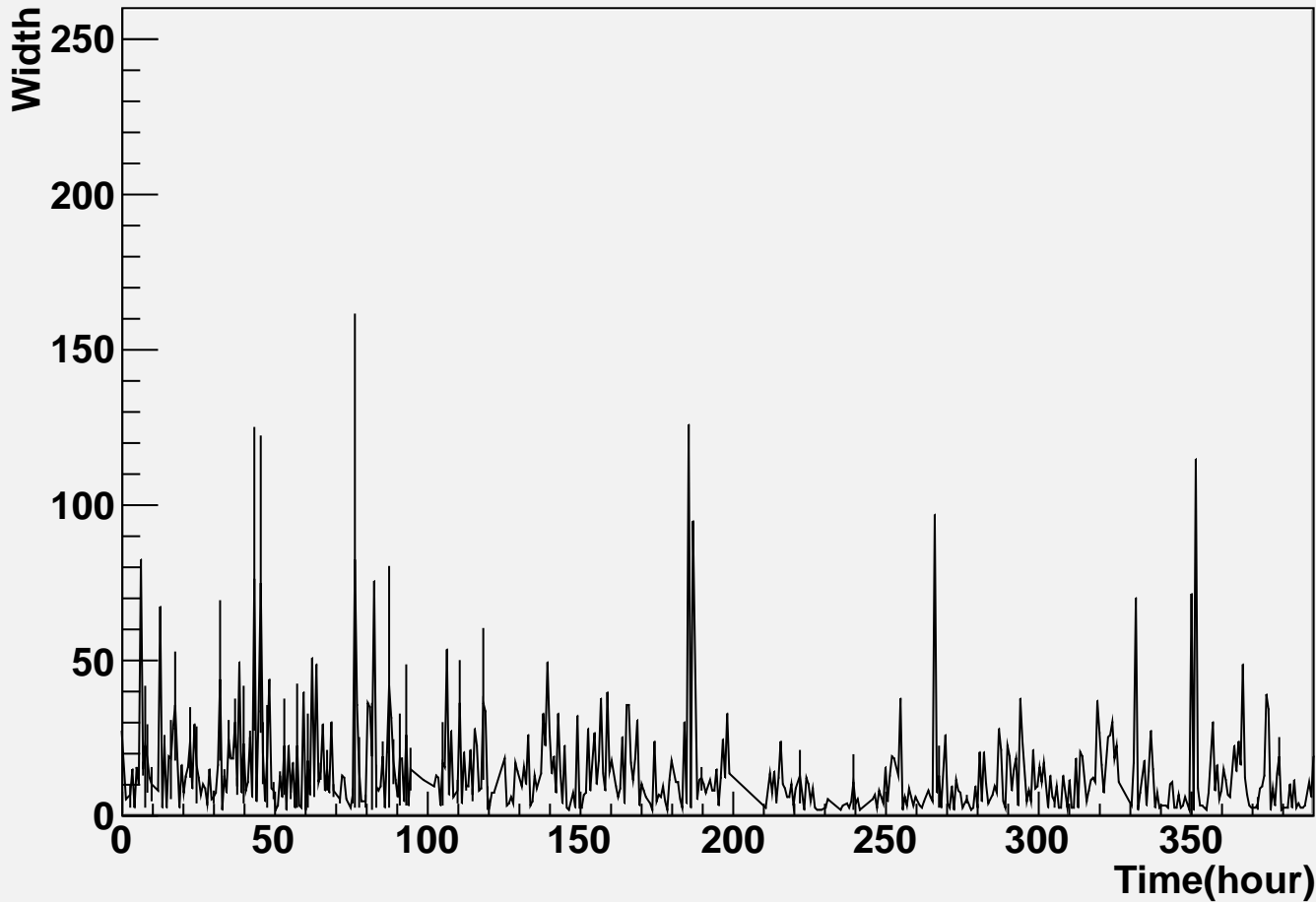
width-S0-SFET2C-Ch1



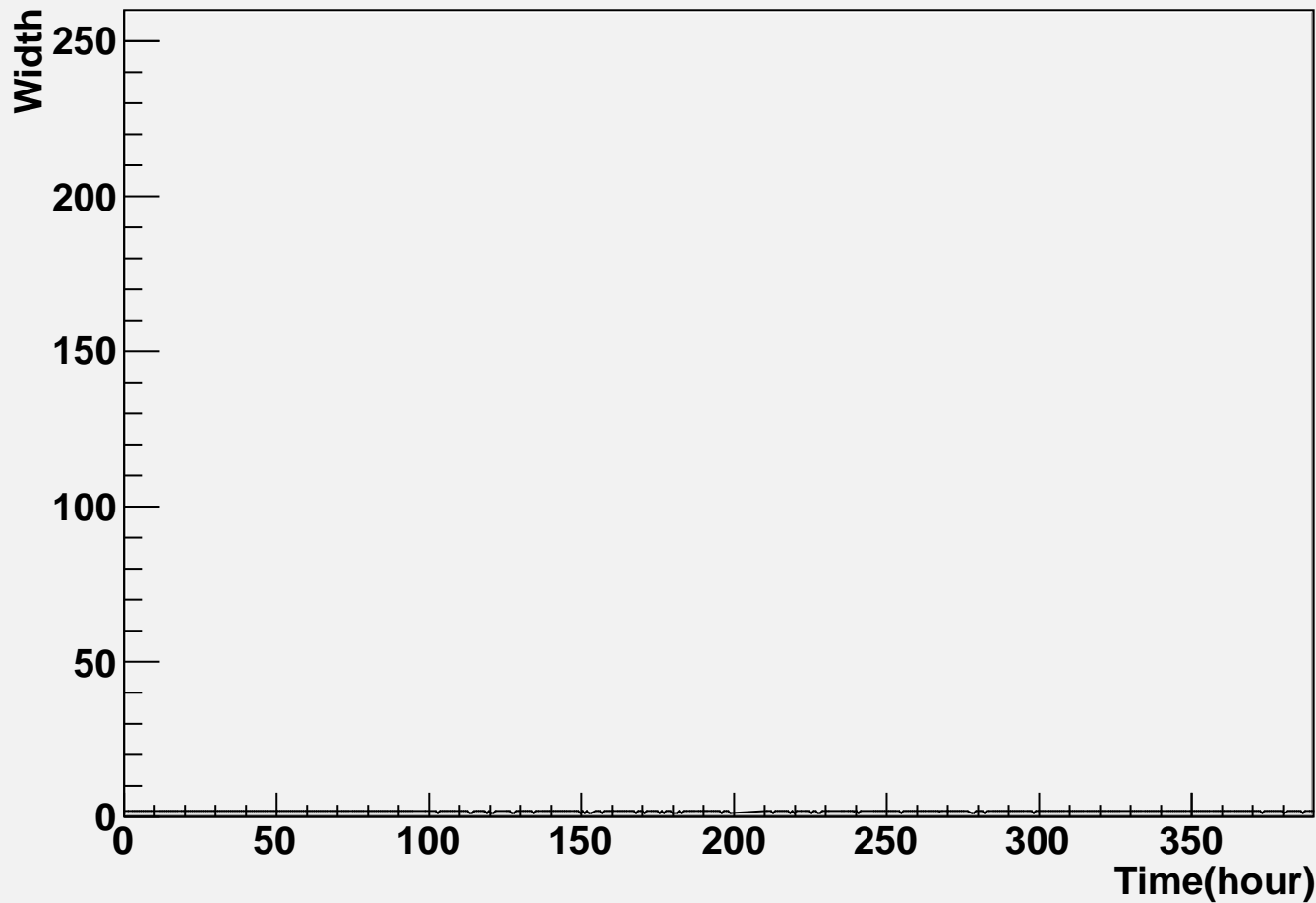
width-S0-SFET2C-Ch2



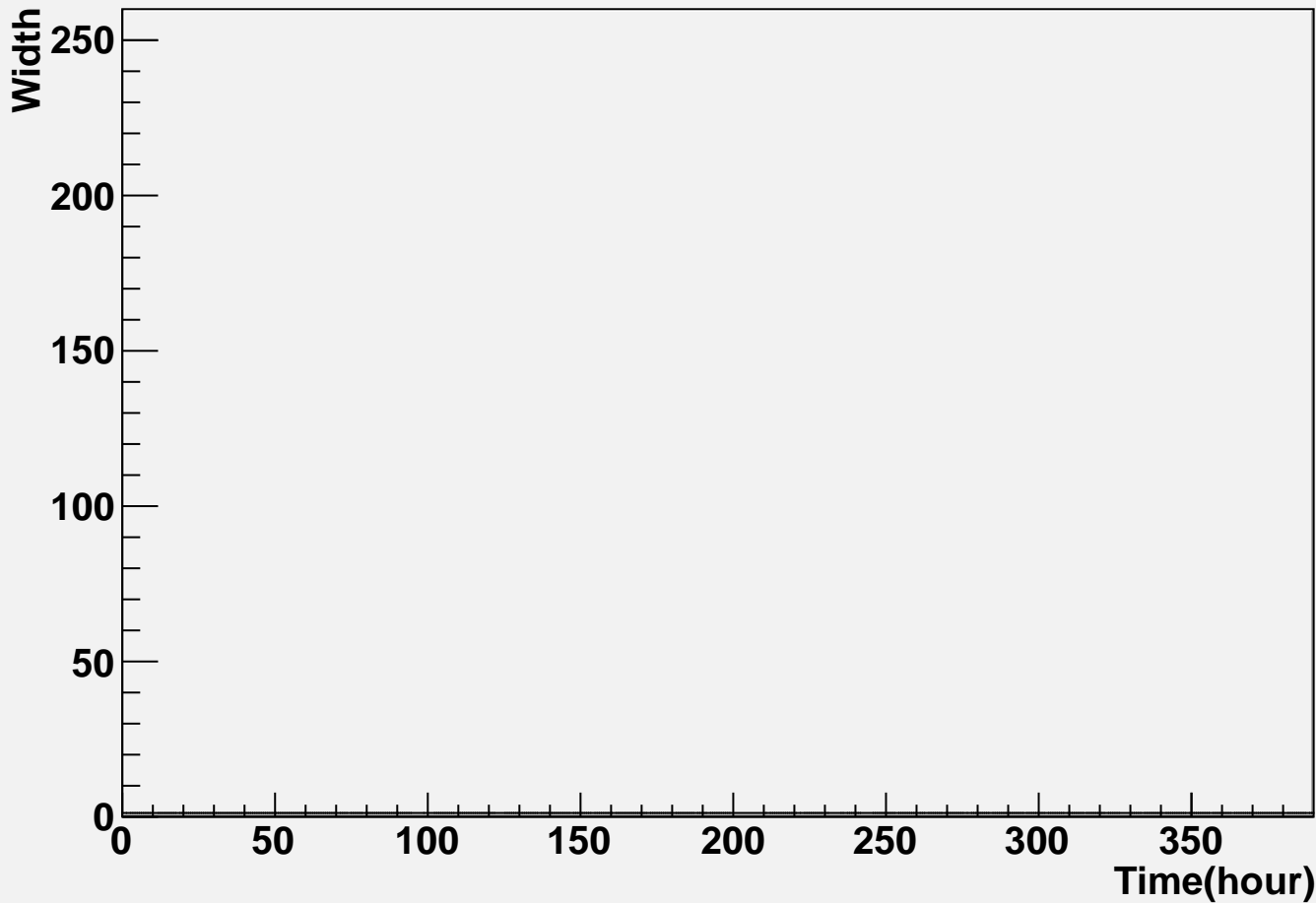
width-S0-SFET2C-Ch3



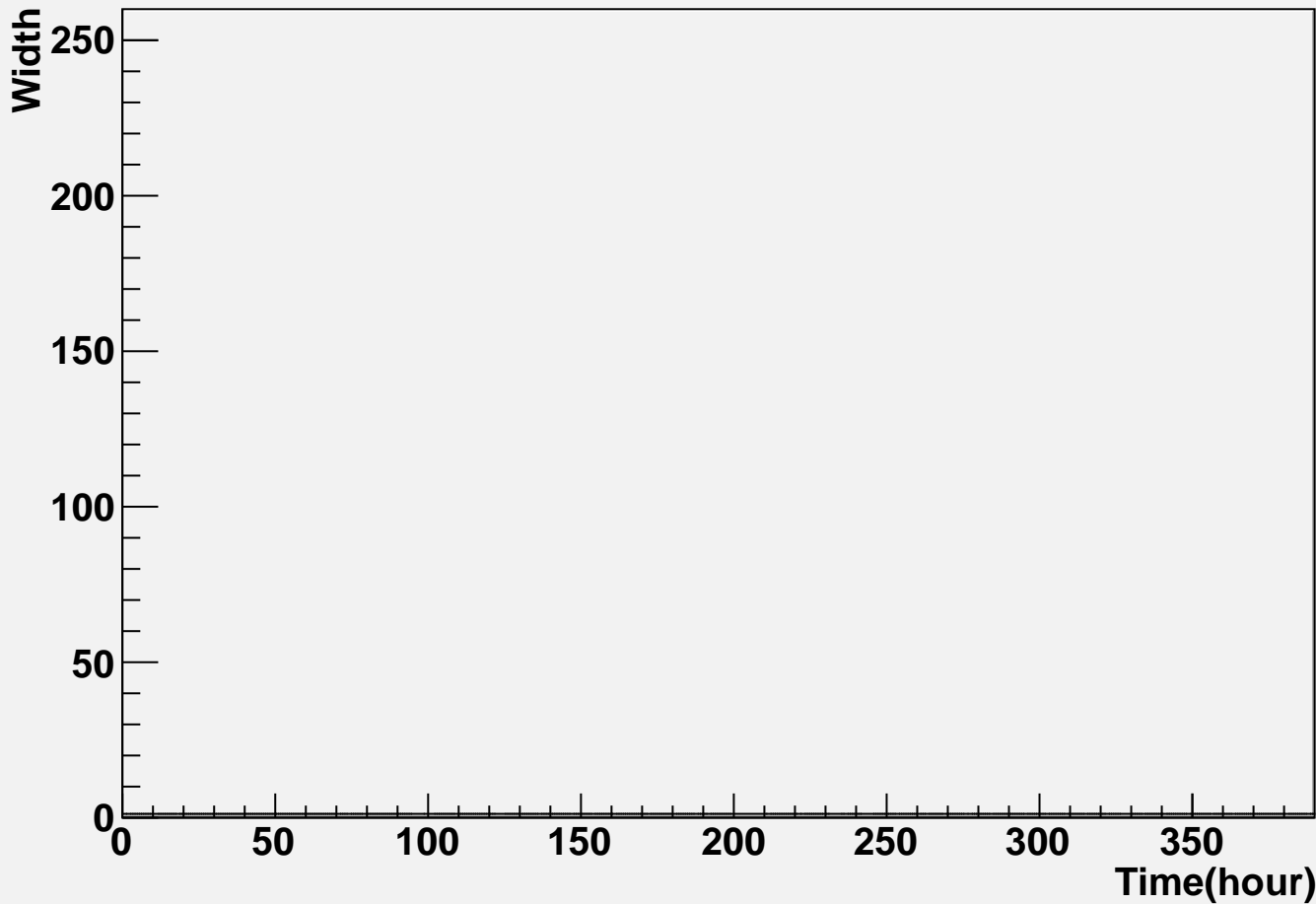
width-S0-SFET2C-Ch4



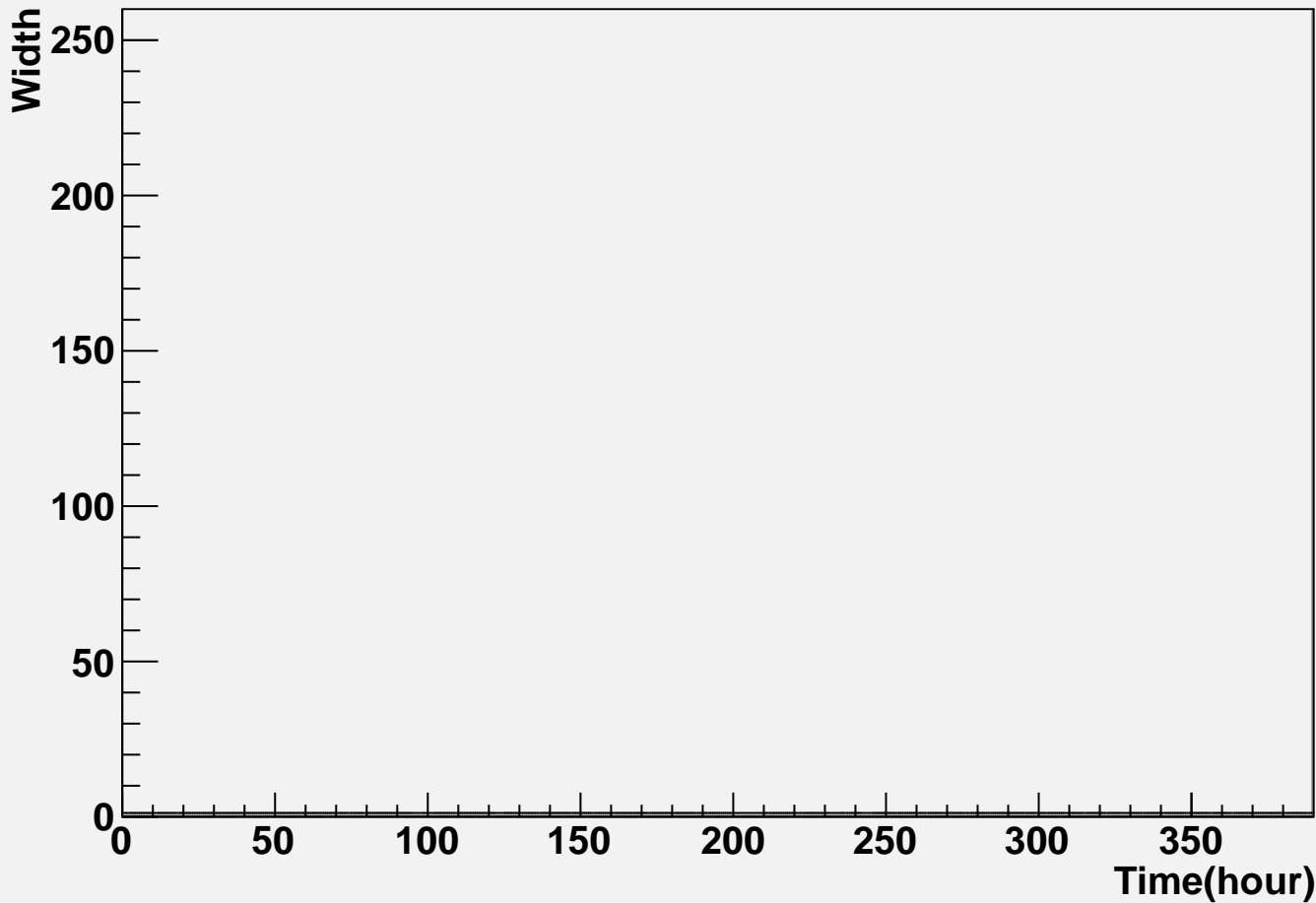
width-S0-SFET2C-Ch5



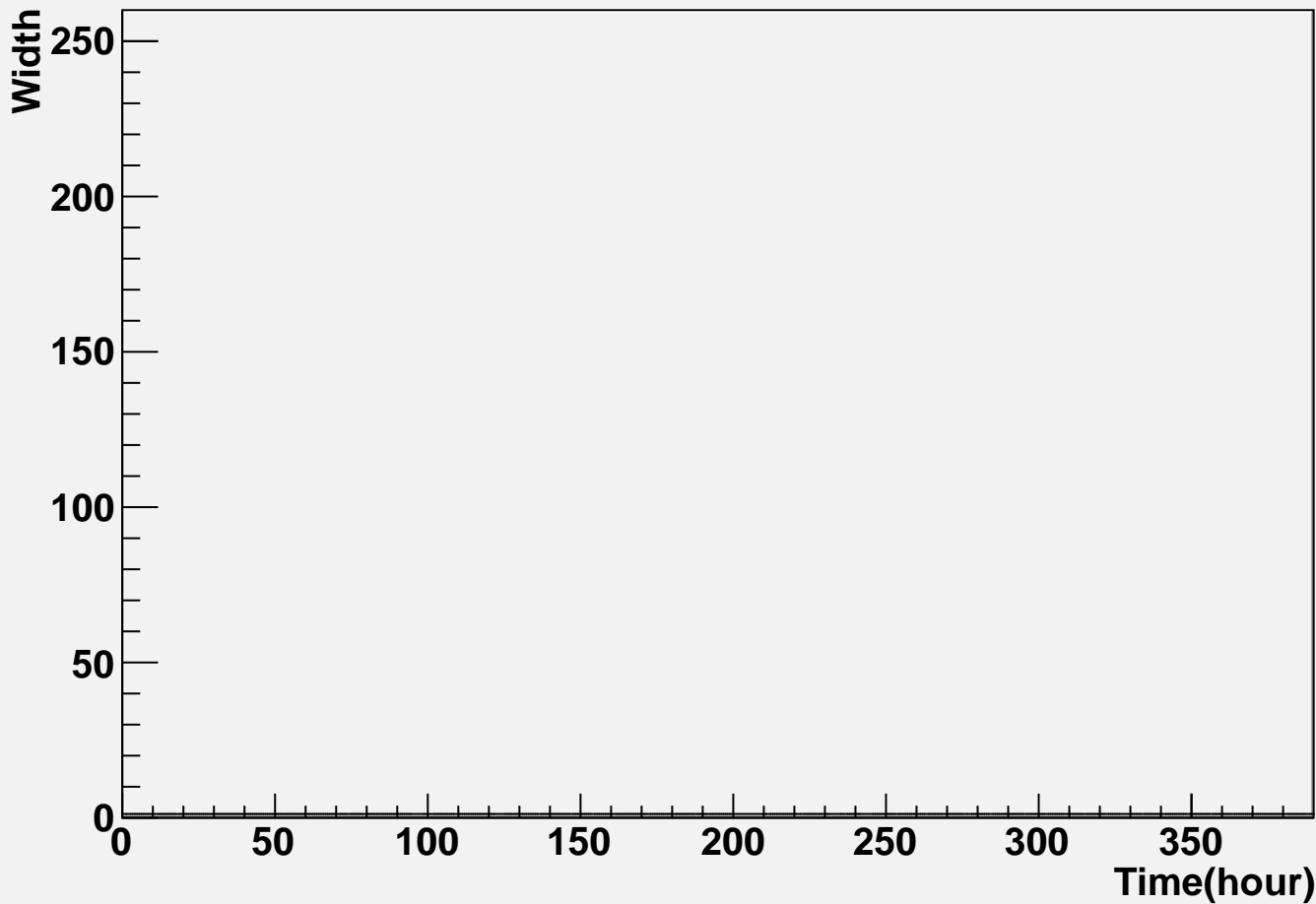
width-S0-SFET2C-Ch6



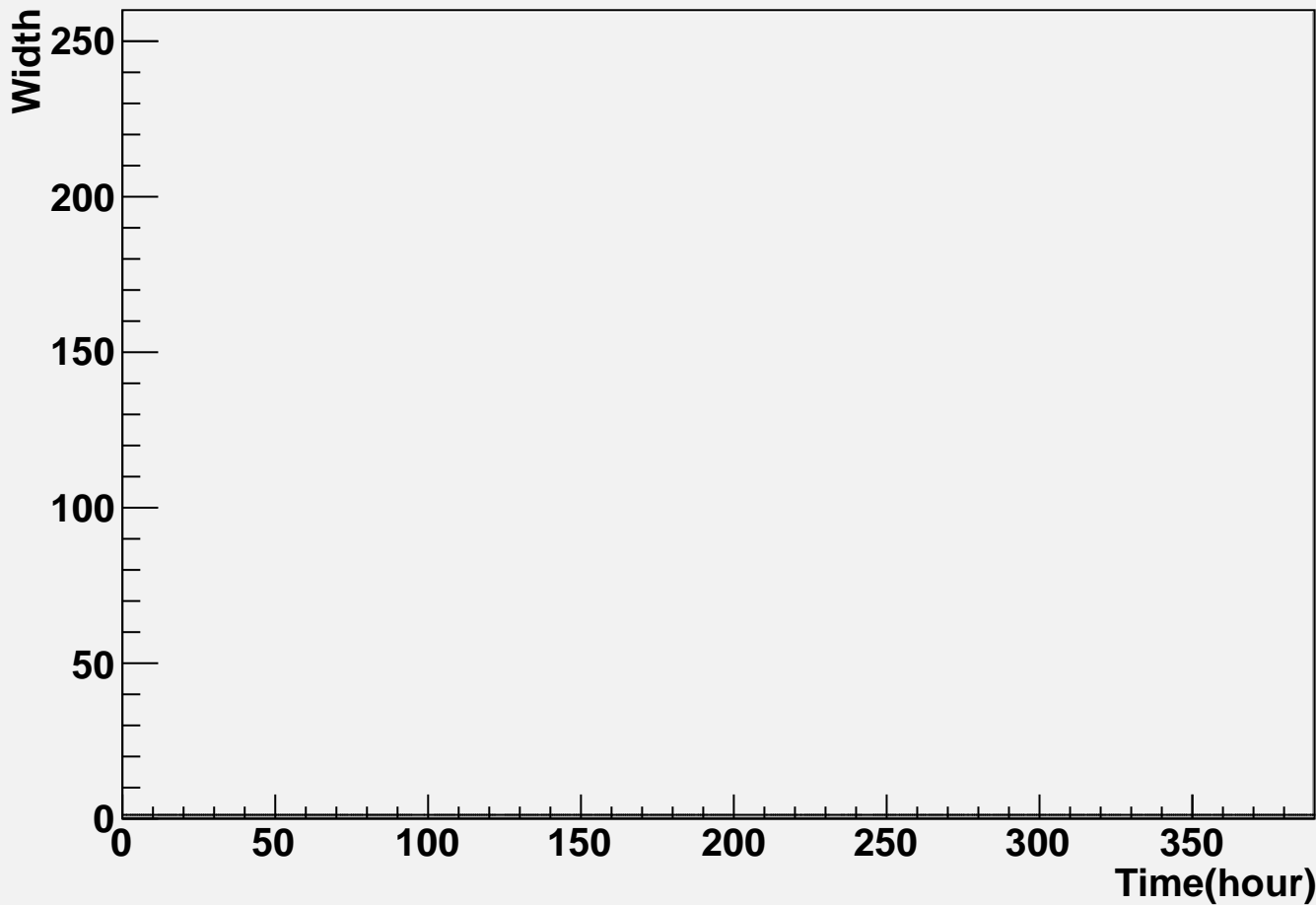
width-S0-SFET2C-Ch7



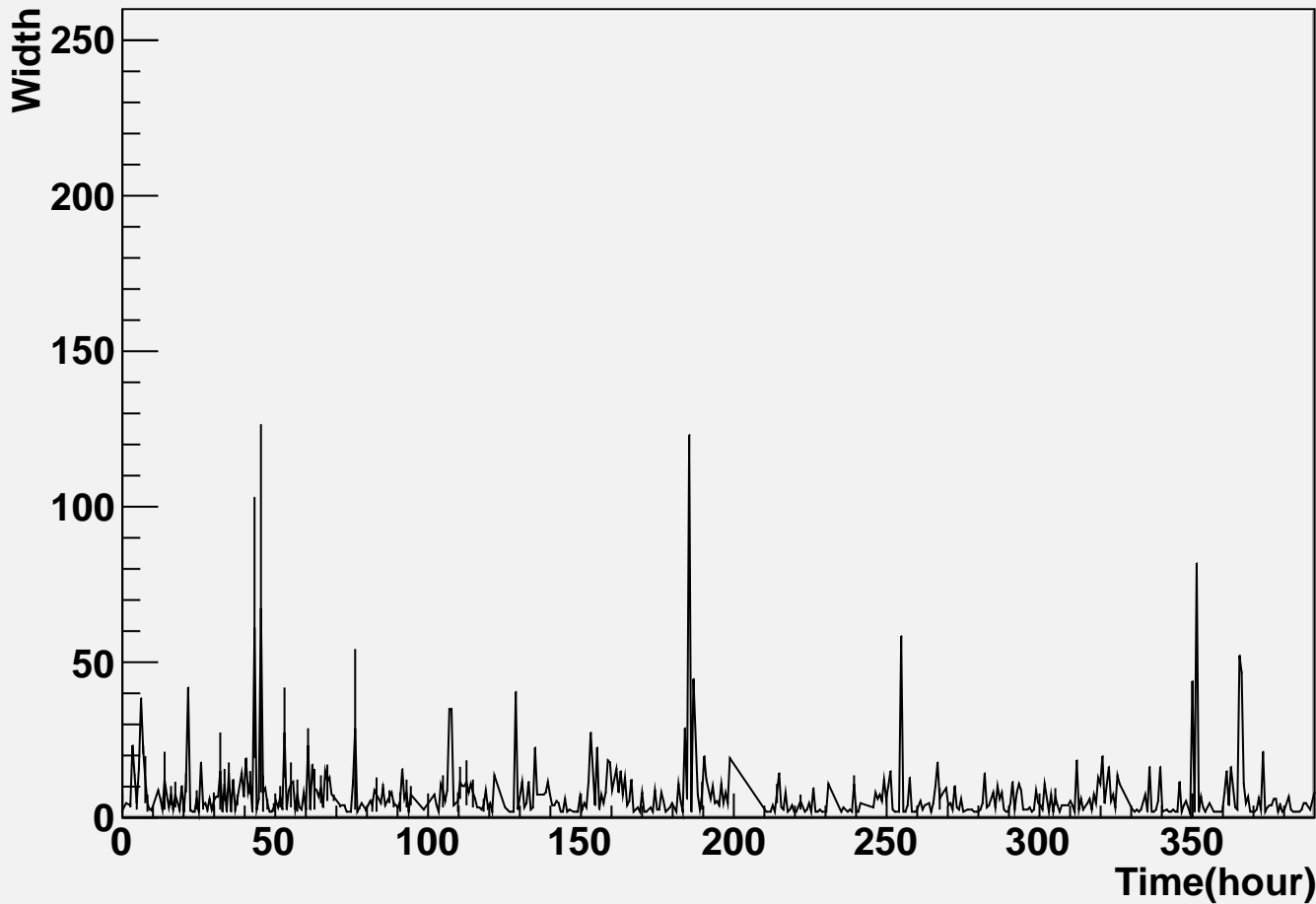
width-S0-SFET2C-Ch8



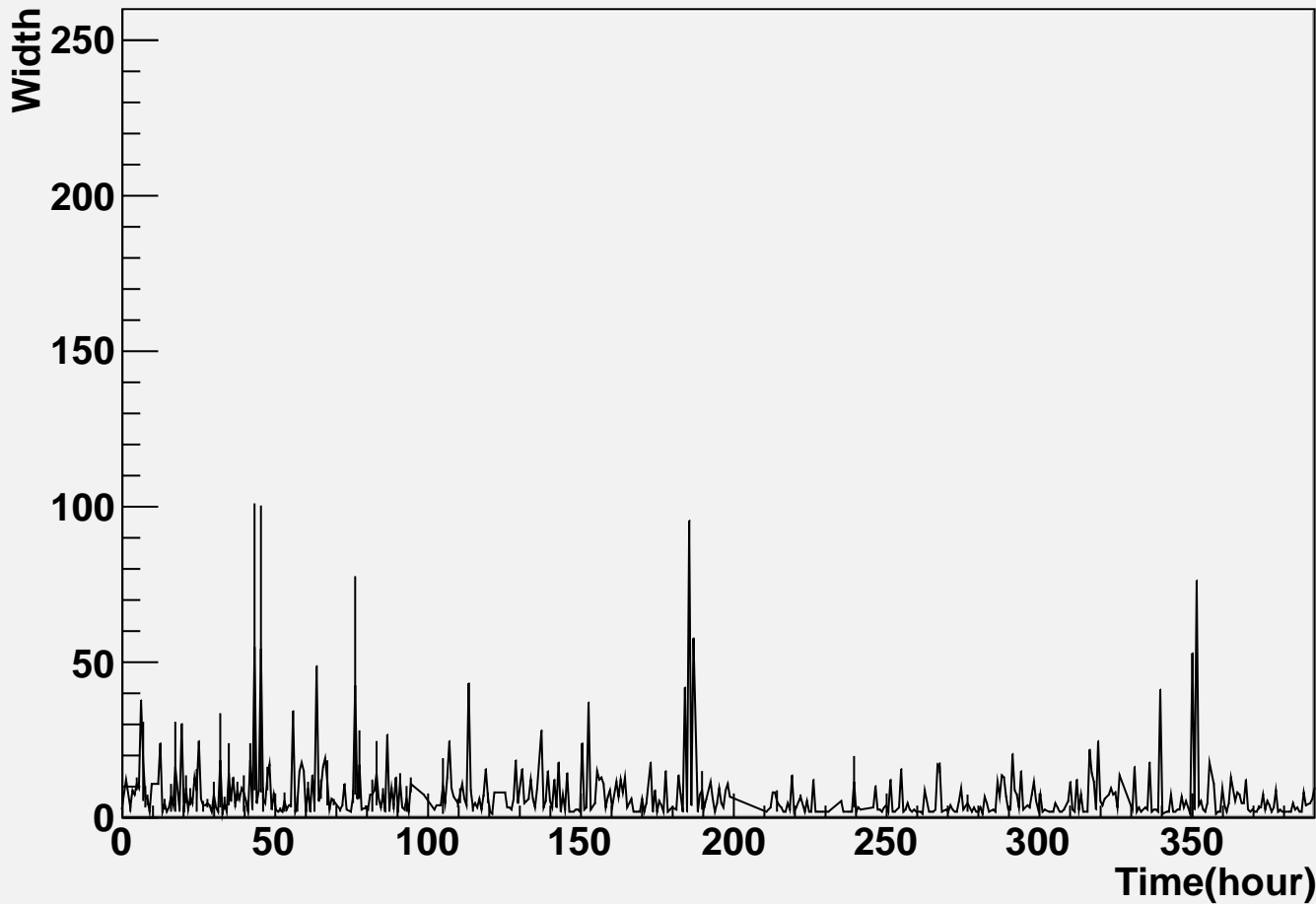
width-S0-SFET2C-Ch9



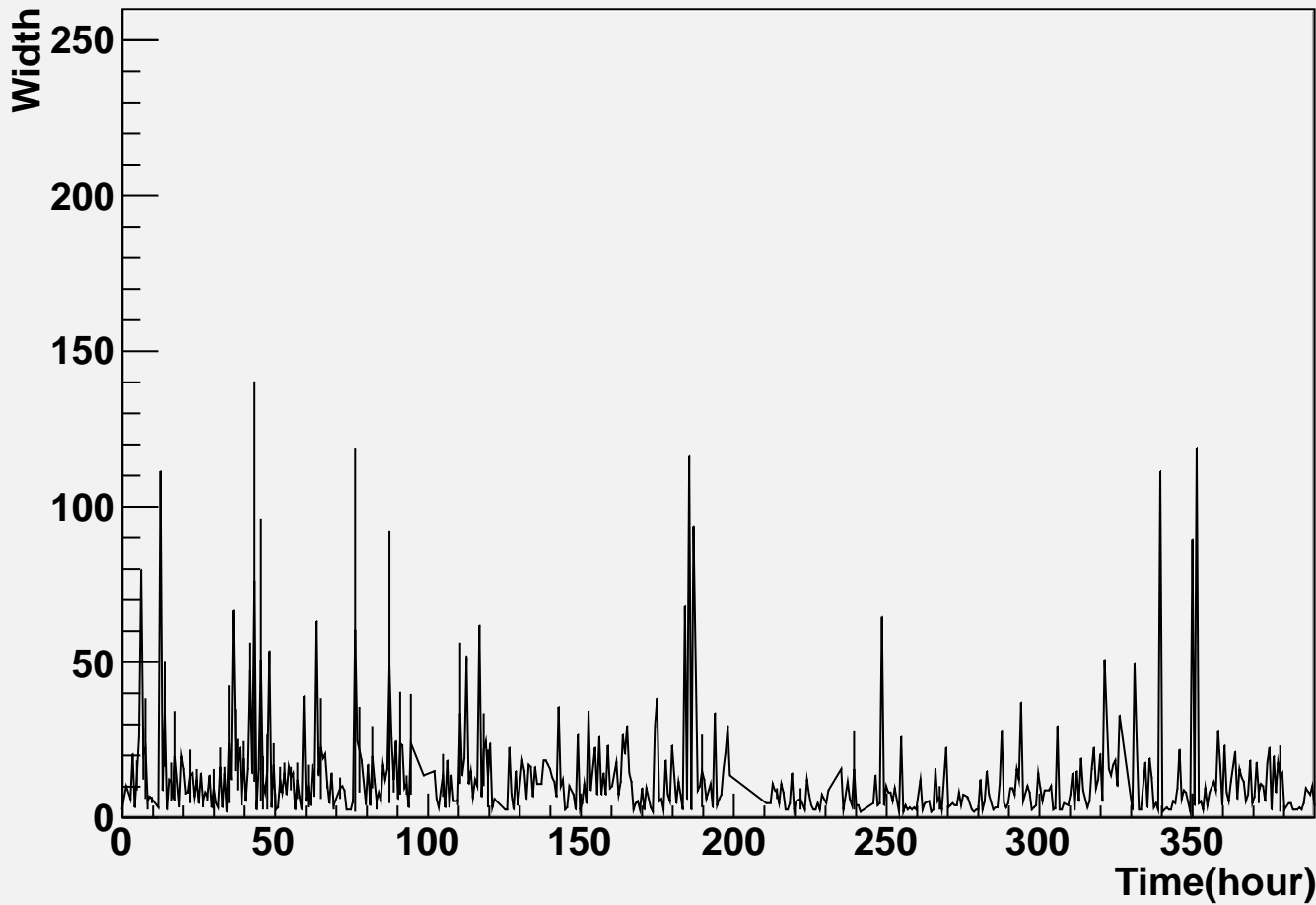
width-S0-SFET2D-Ch0



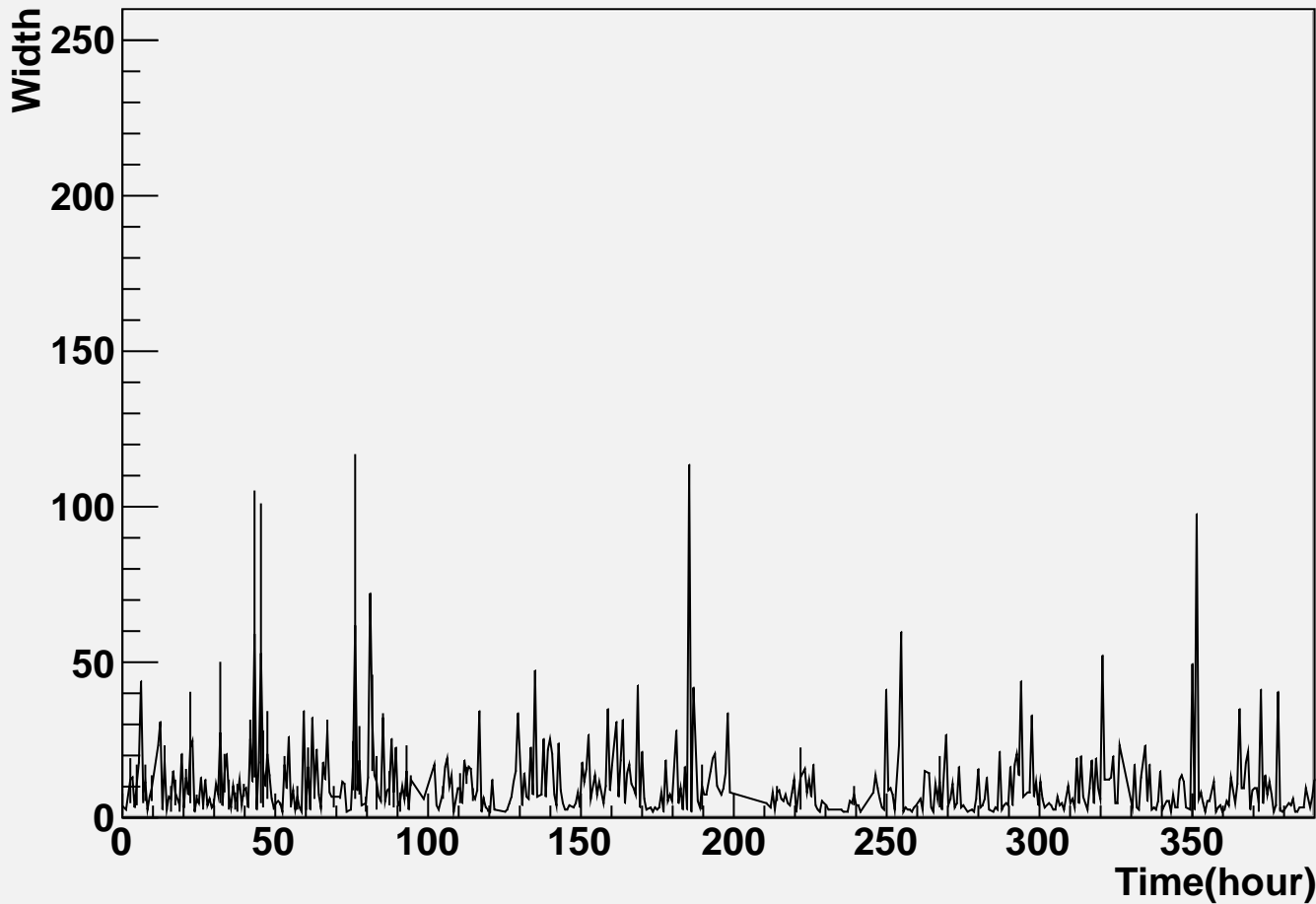
width-S0-SFET2D-Ch1



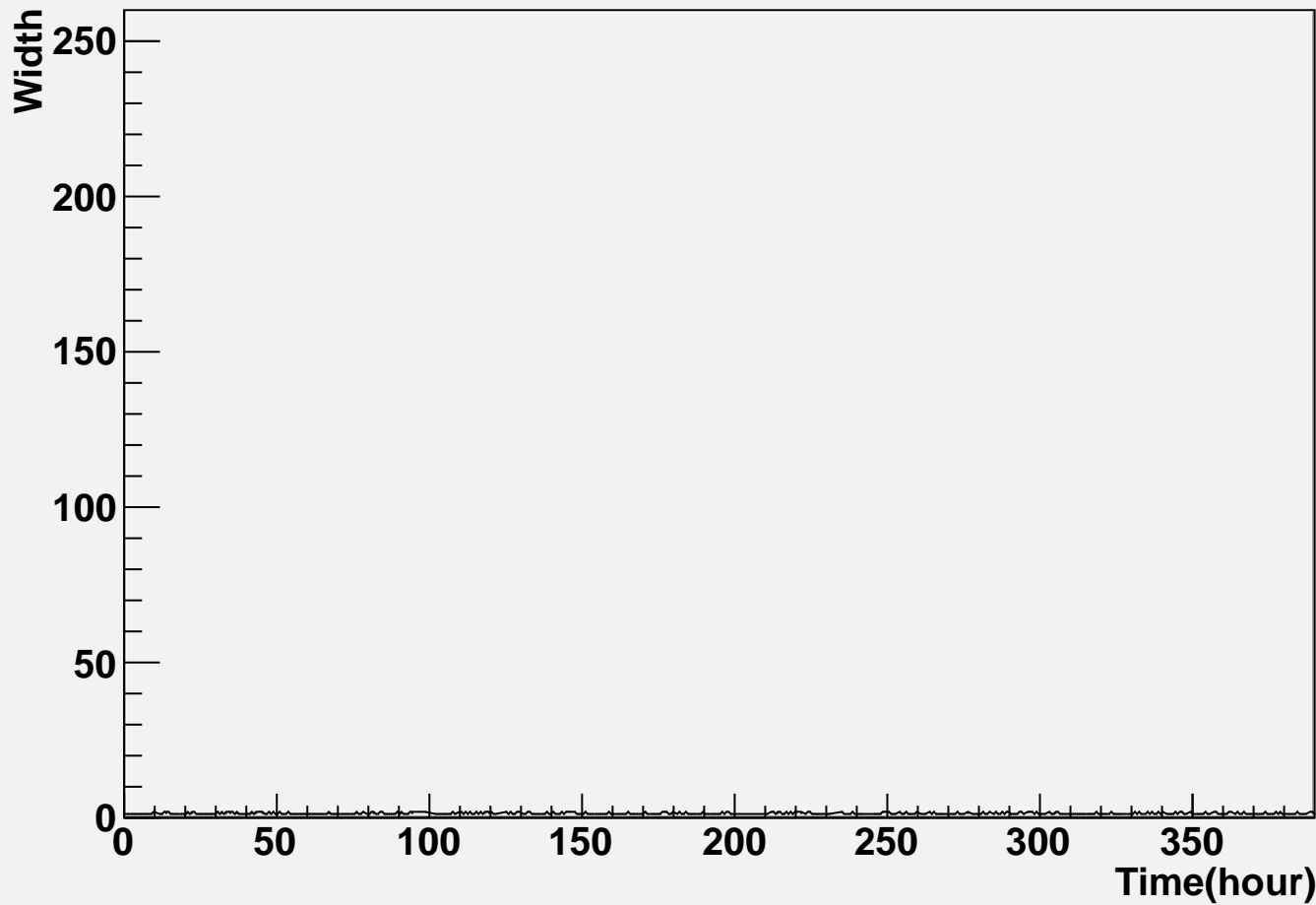
width-S0-SFET2D-Ch2



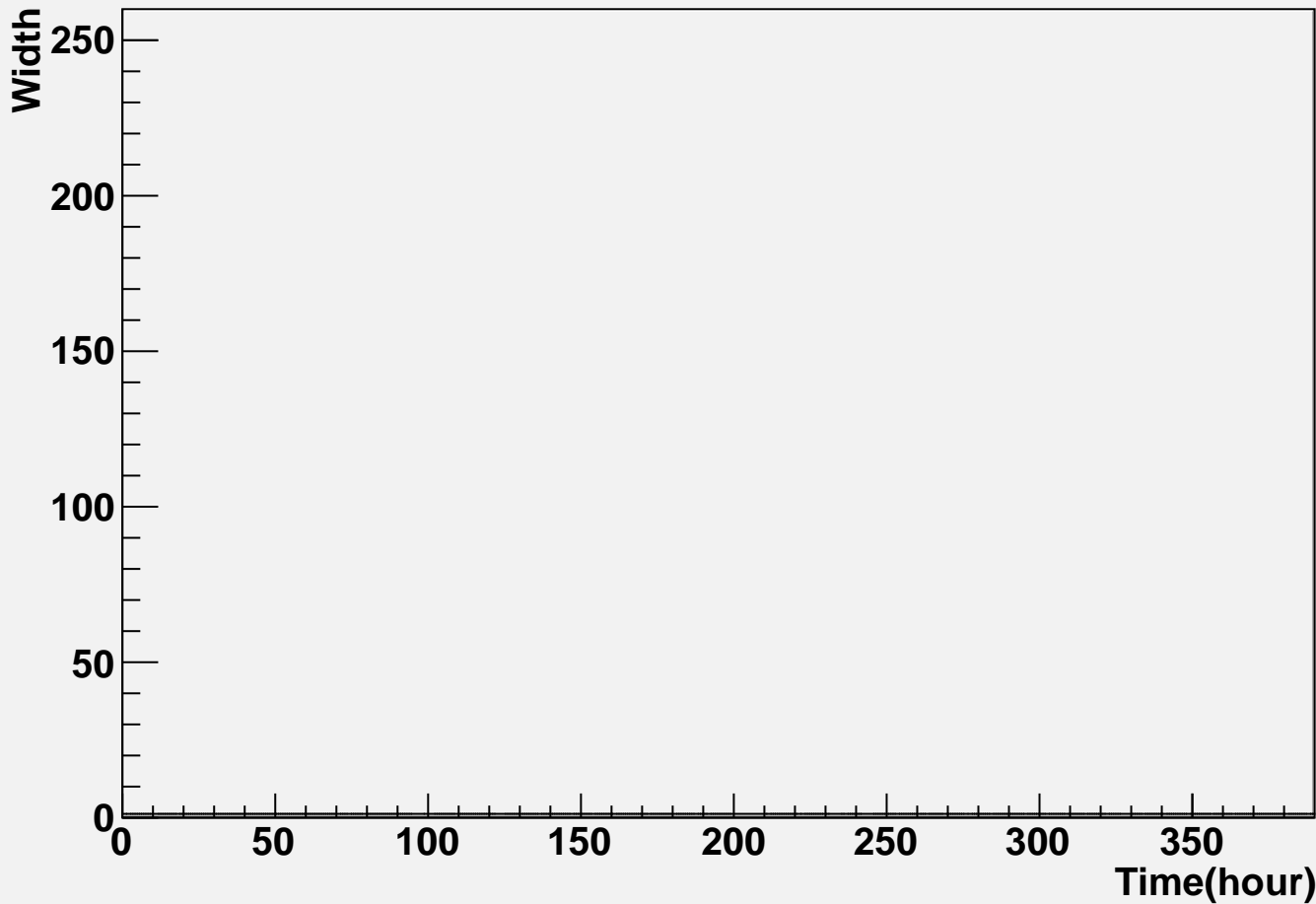
width-S0-SFET2D-Ch3



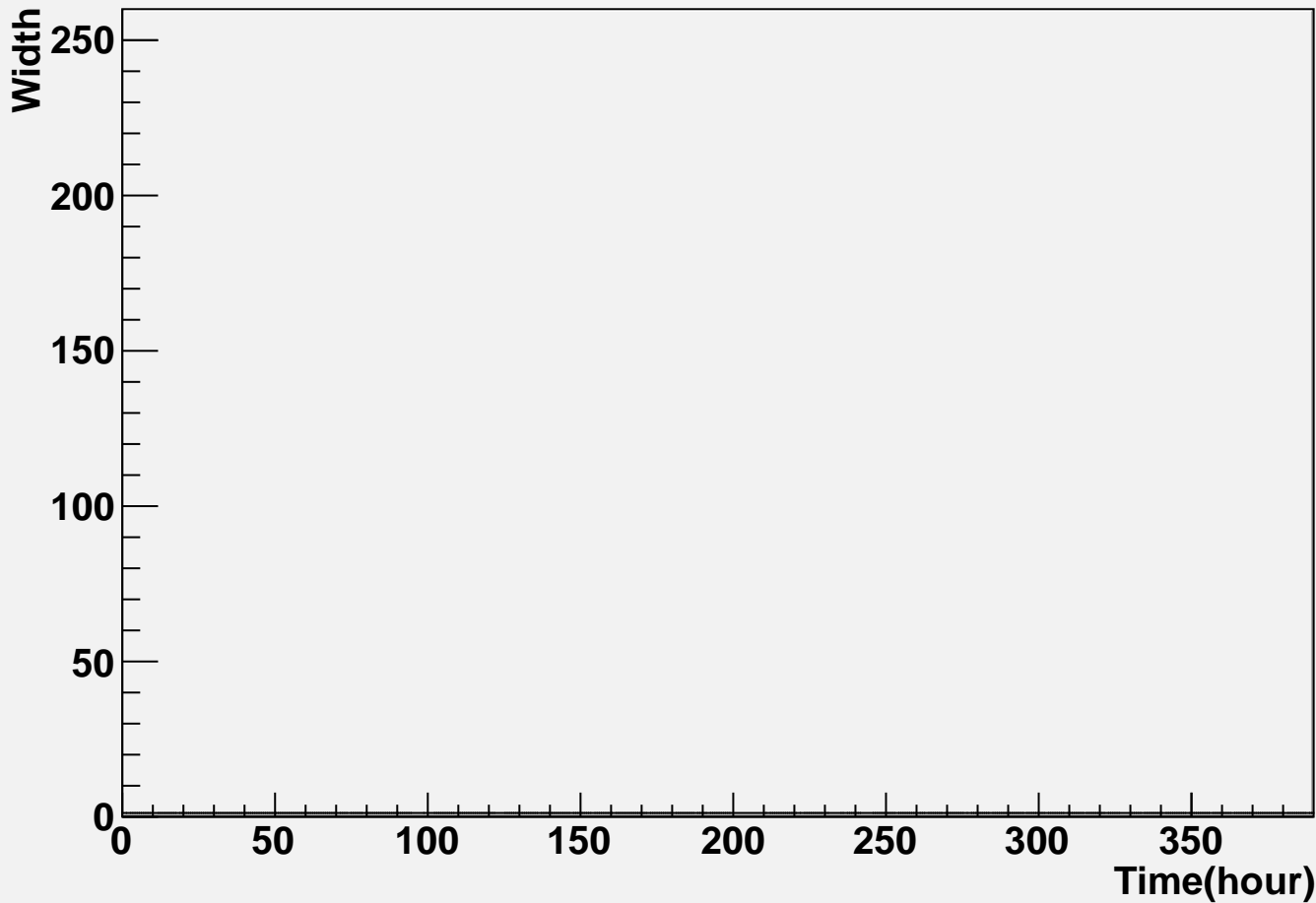
width-S0-SFET2D-Ch4



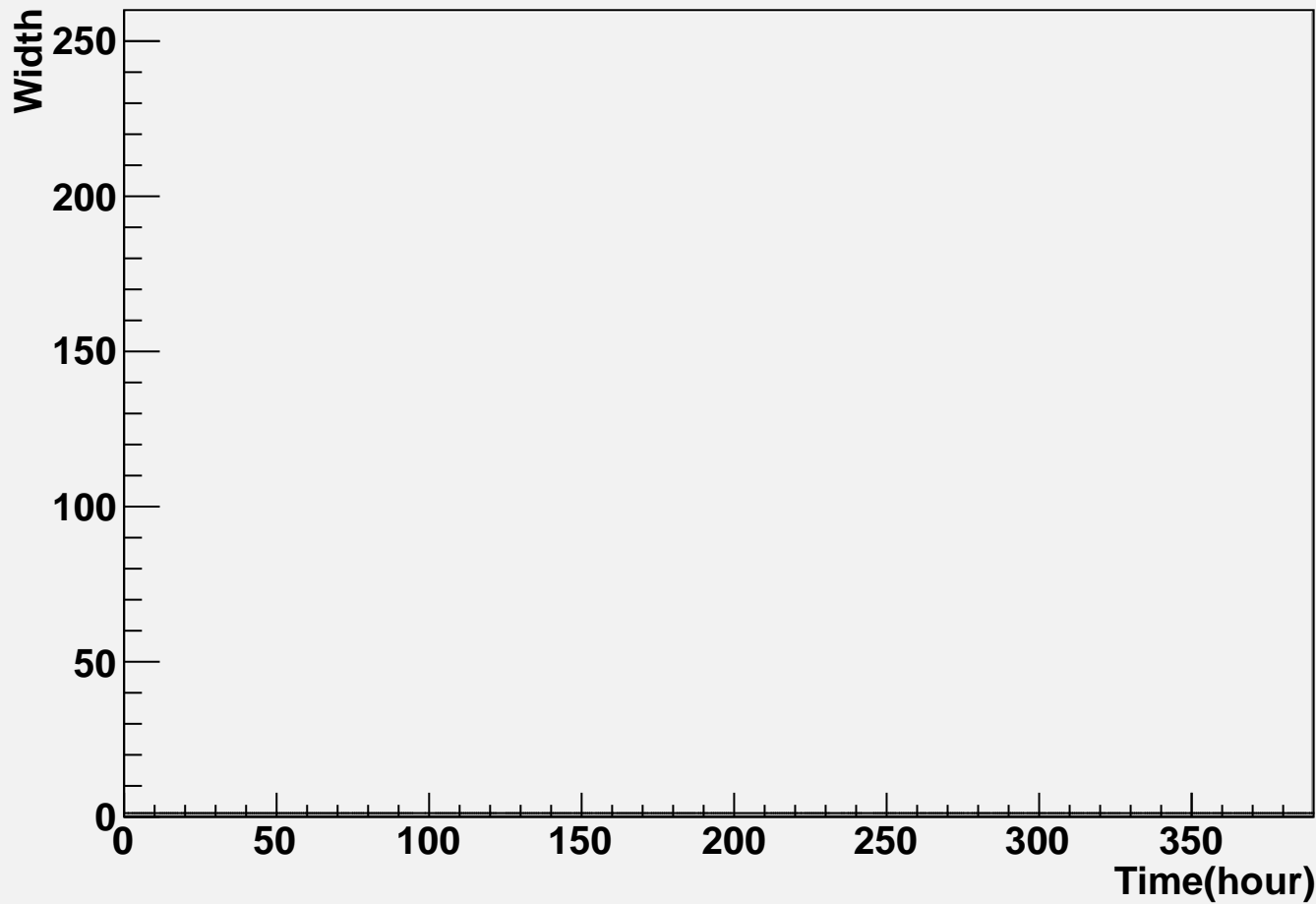
width-S0-SFET2D-Ch5



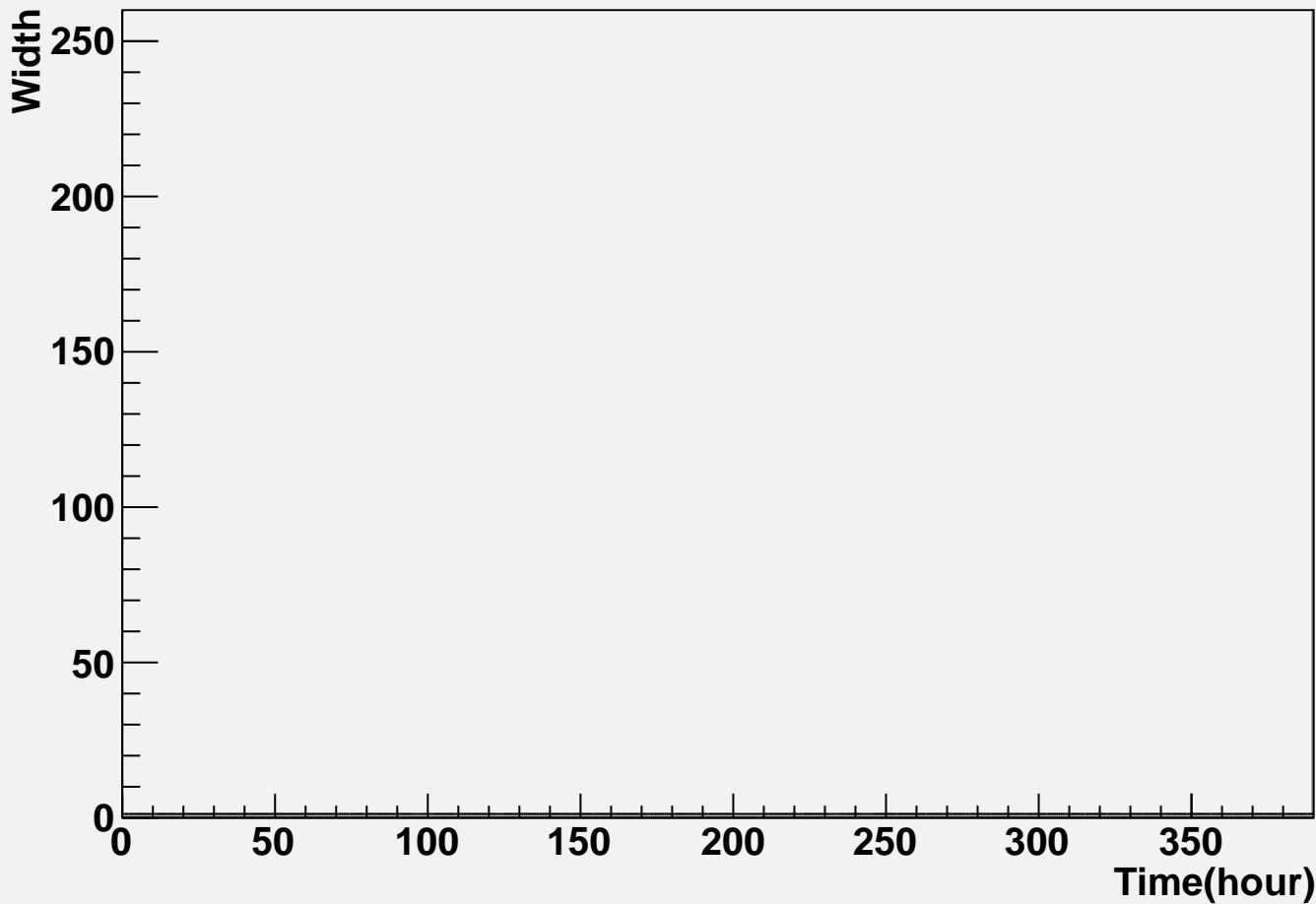
width-S0-SFET2D-Ch6



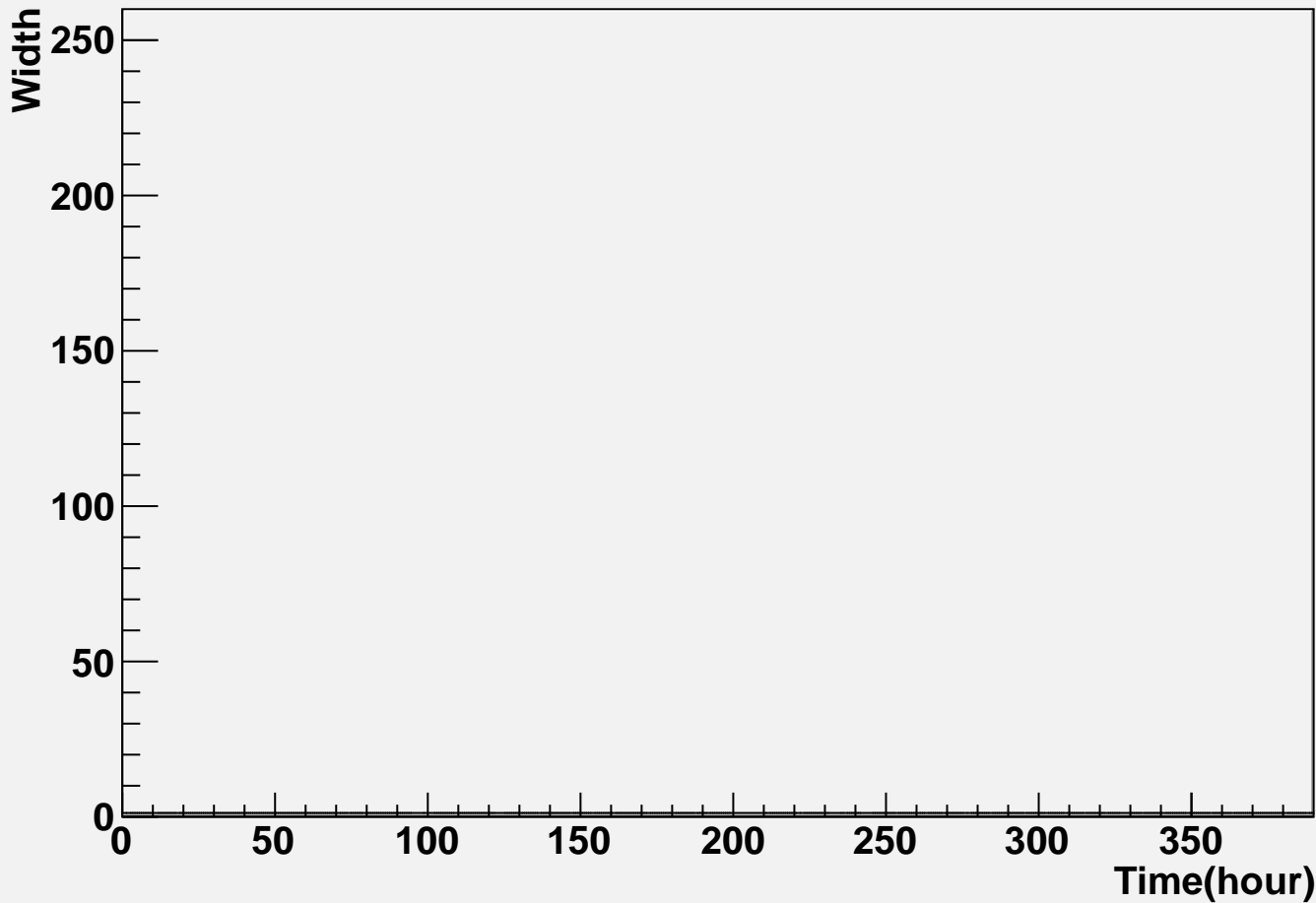
width-S0-SFET2D-Ch7



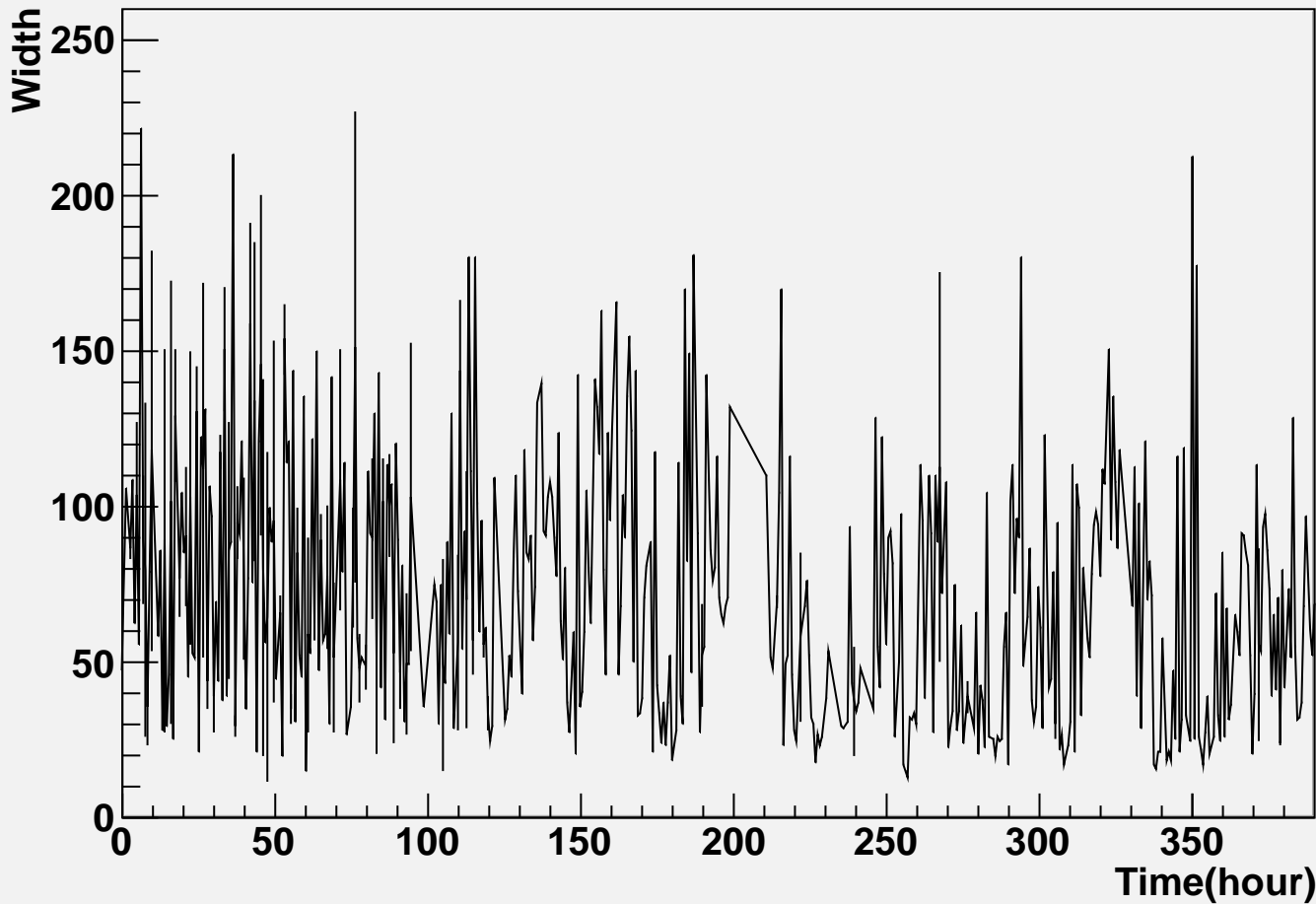
width-S0-SFET2D-Ch8



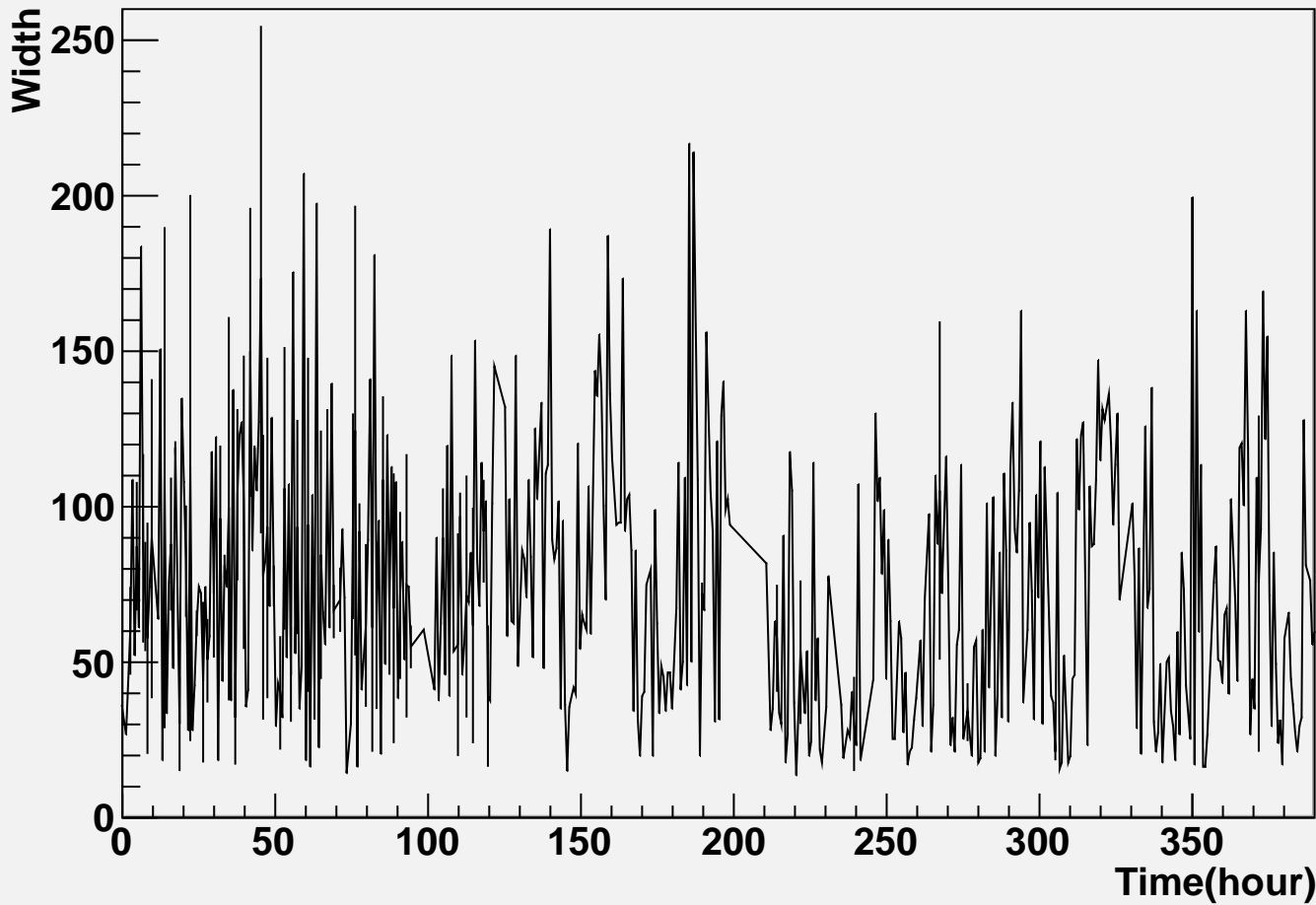
width-S0-SFET2D-Ch9



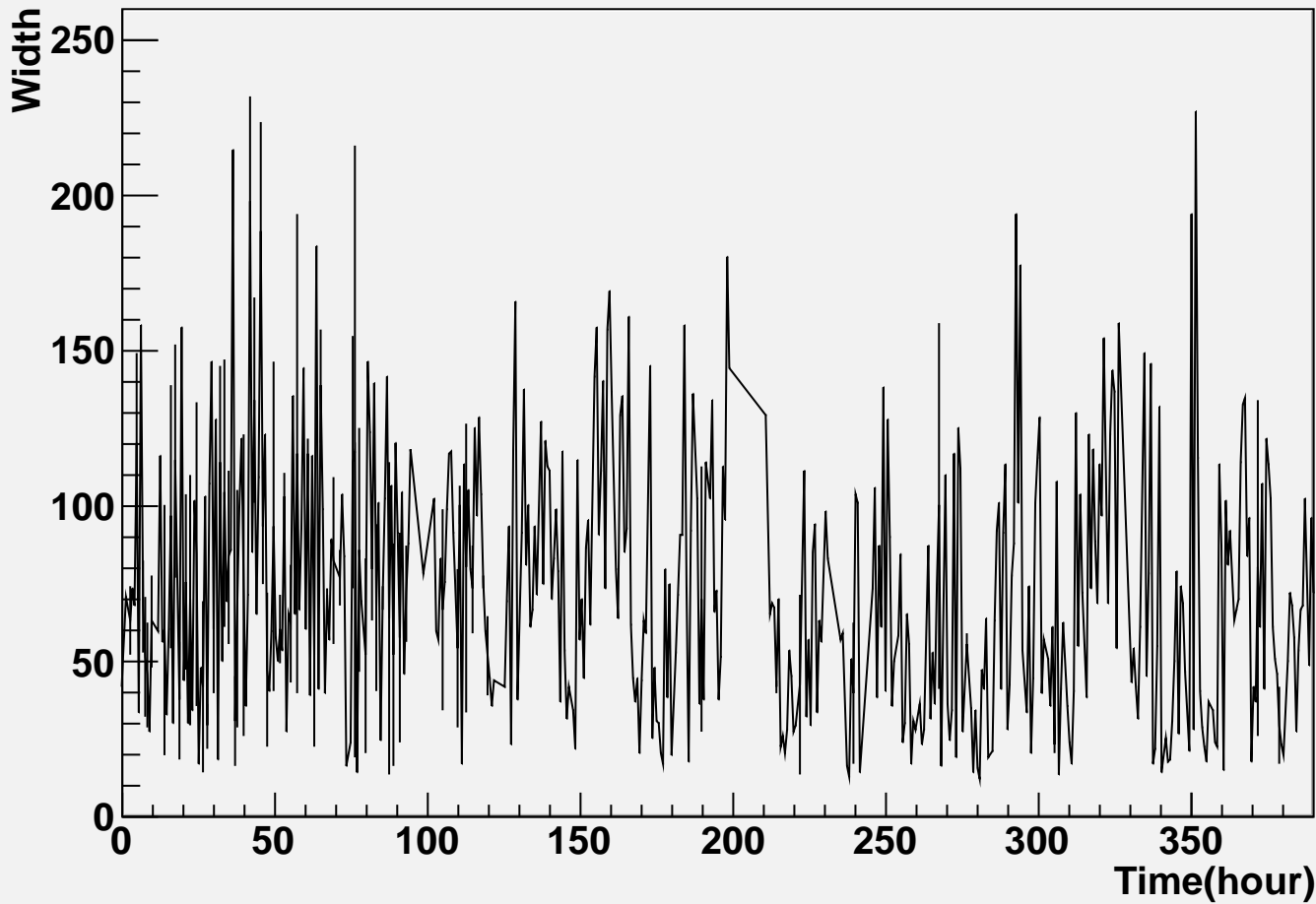
width-S0-SFEA2-Ch0



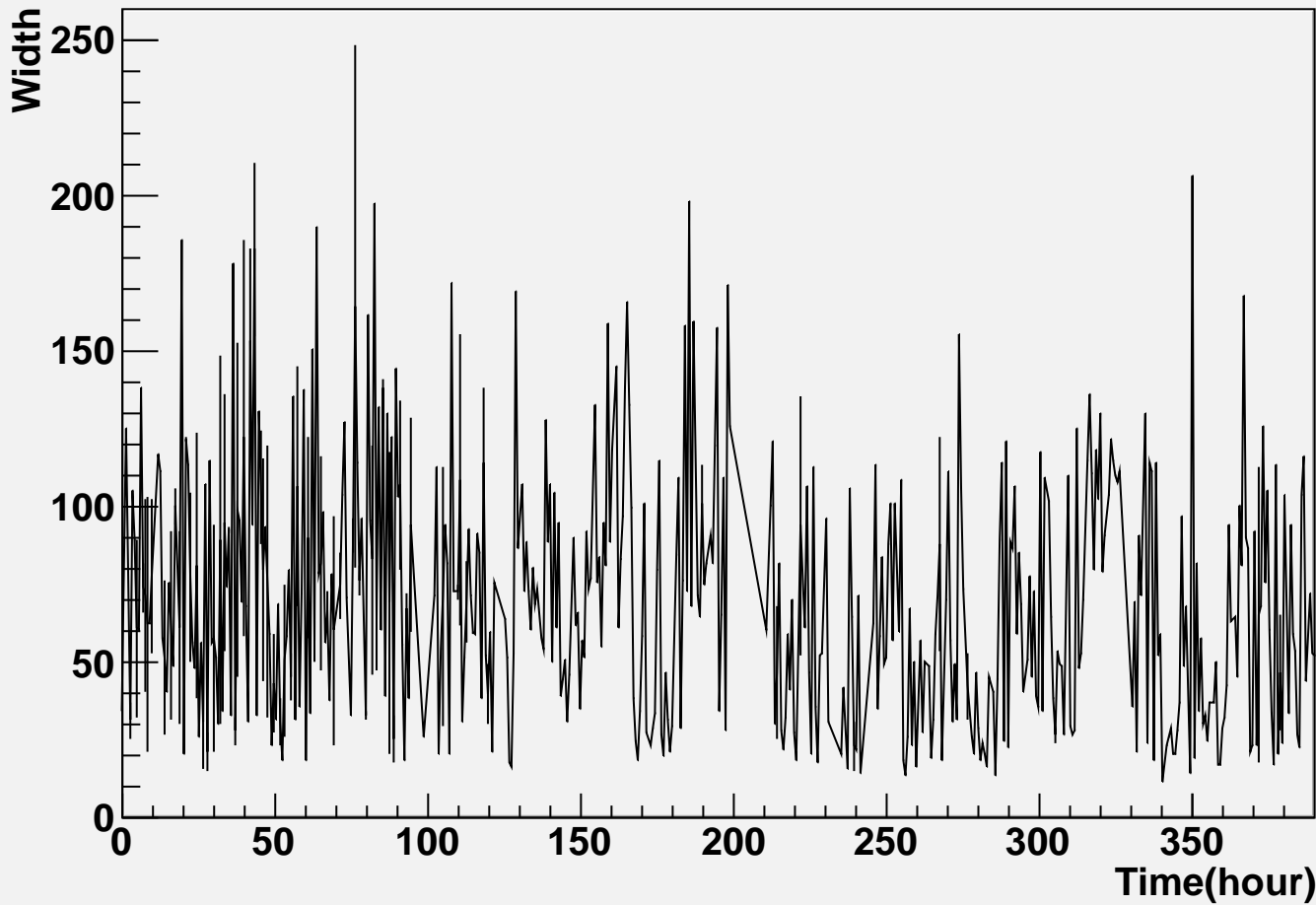
width-S0-SFEA2-Ch1



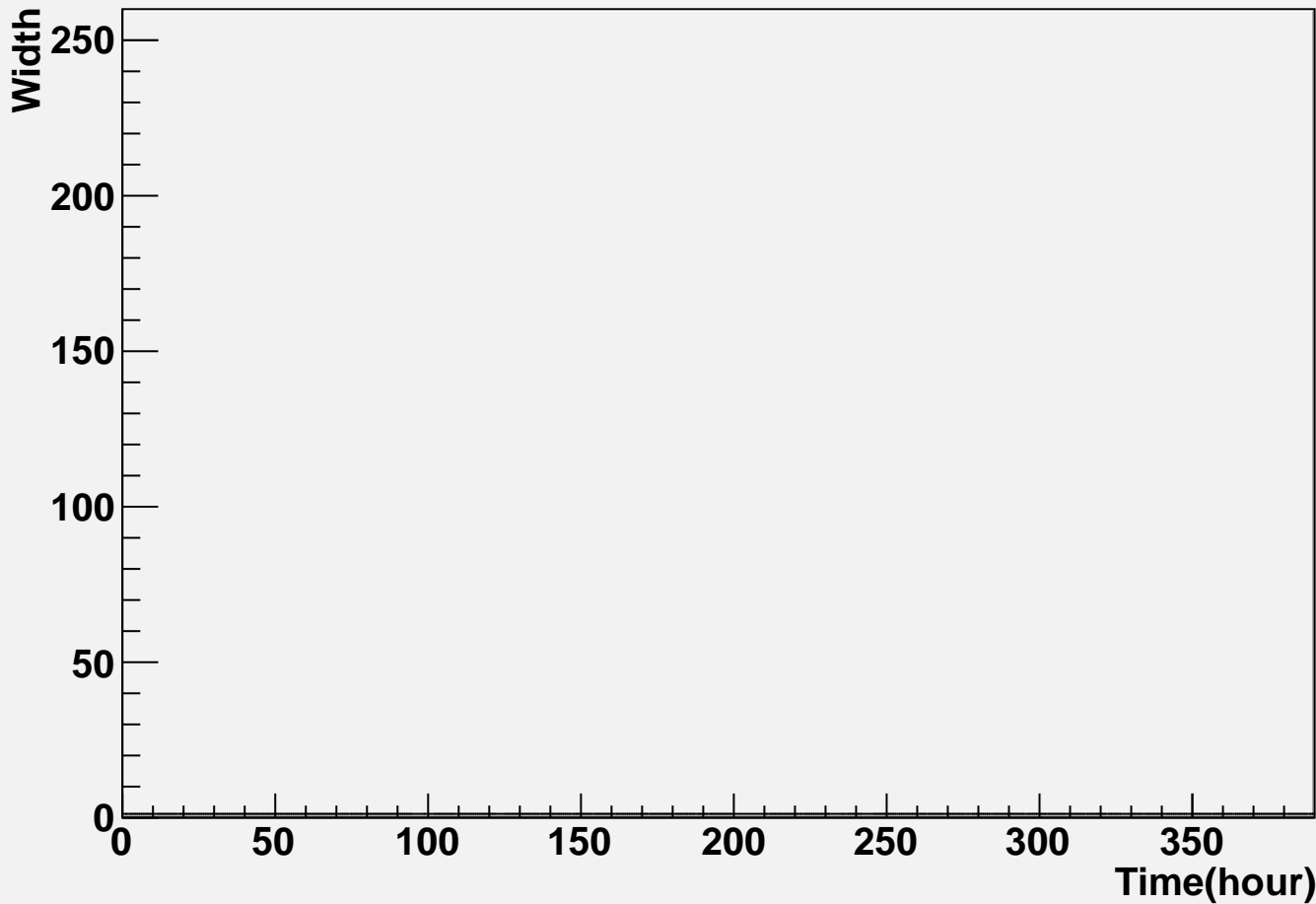
width-S0-SFEA2-Ch2



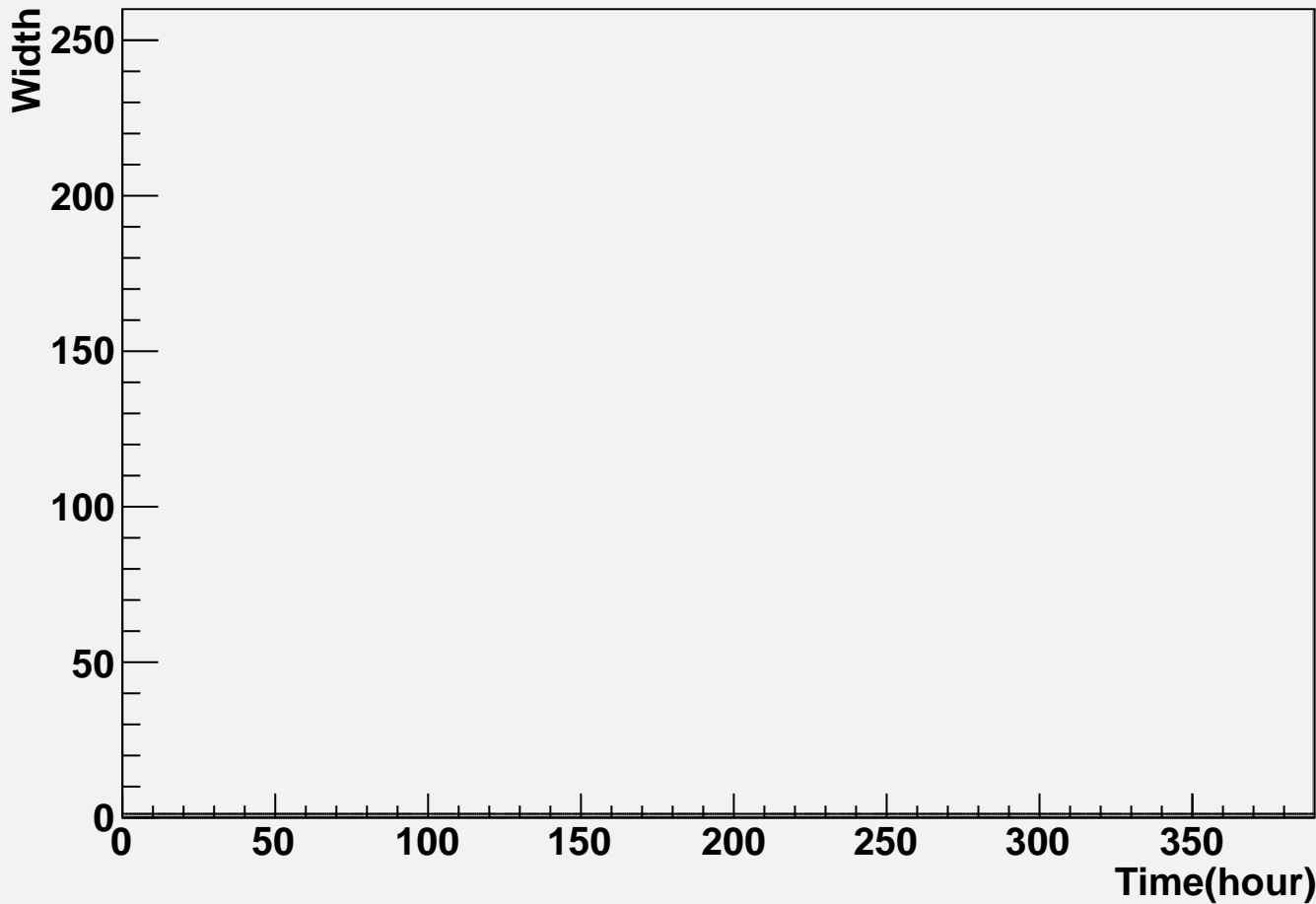
width-S0-SFEA2-Ch3



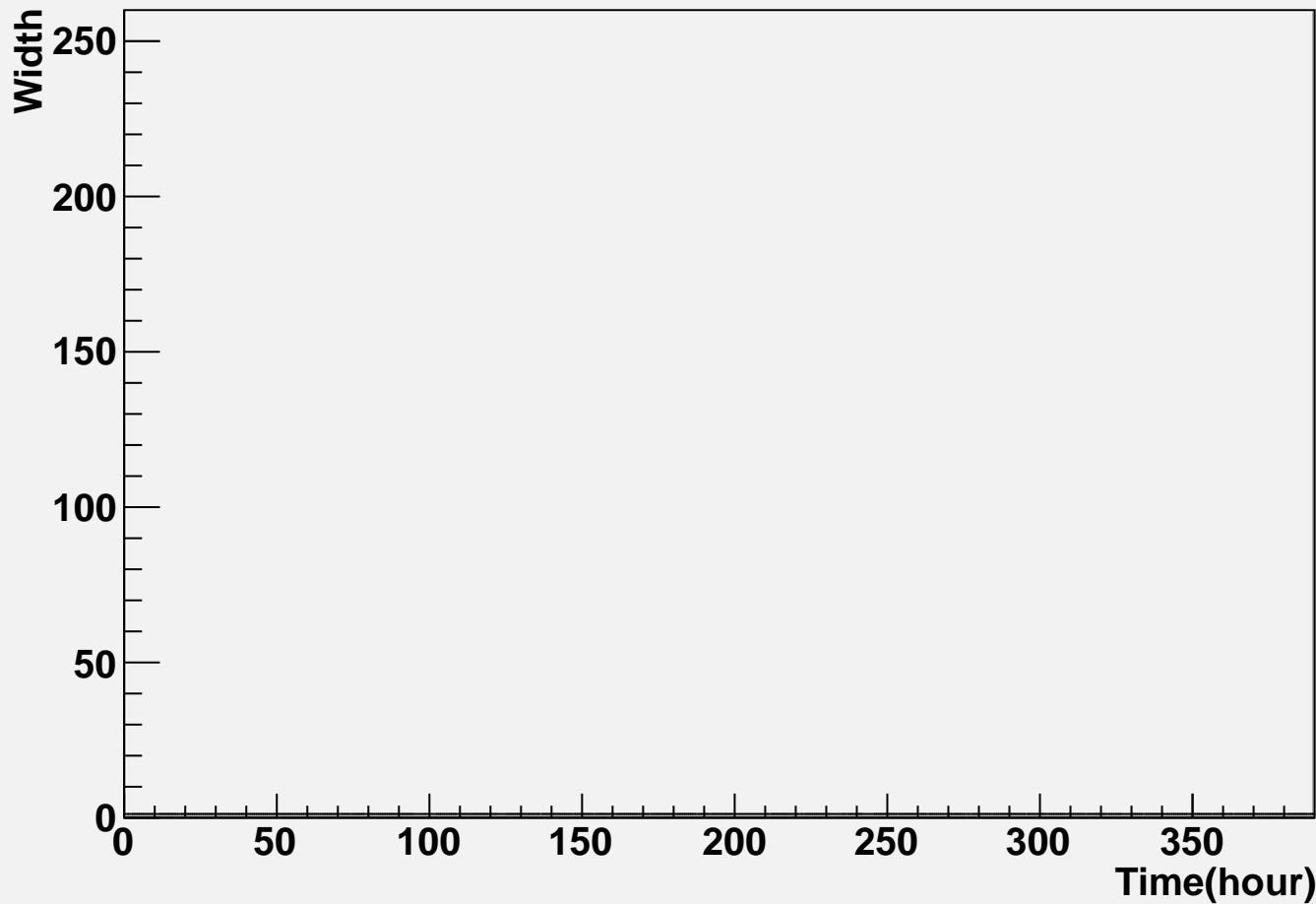
width-S0-SFEA2-Ch4



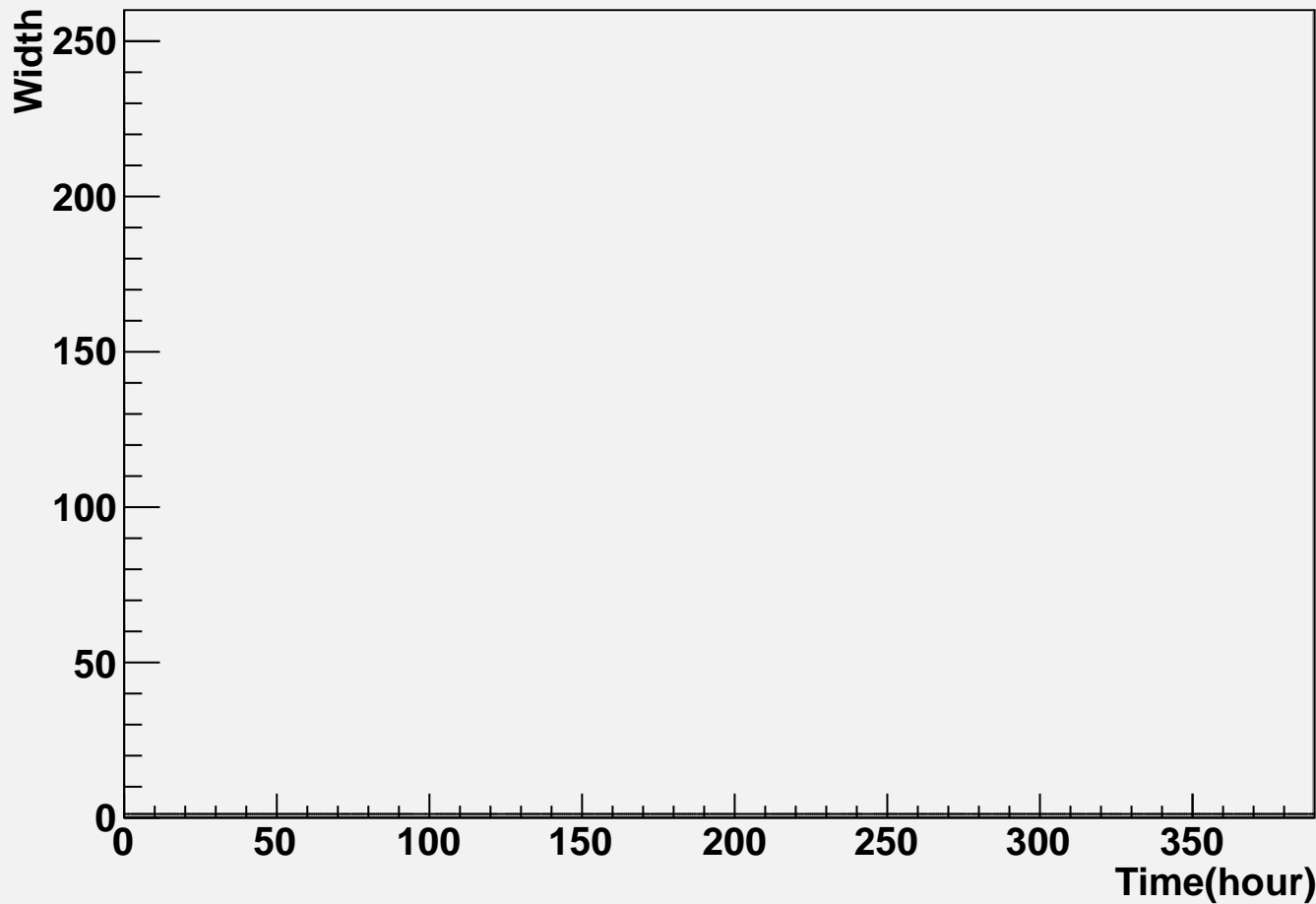
width-S0-SFEA2-Ch5



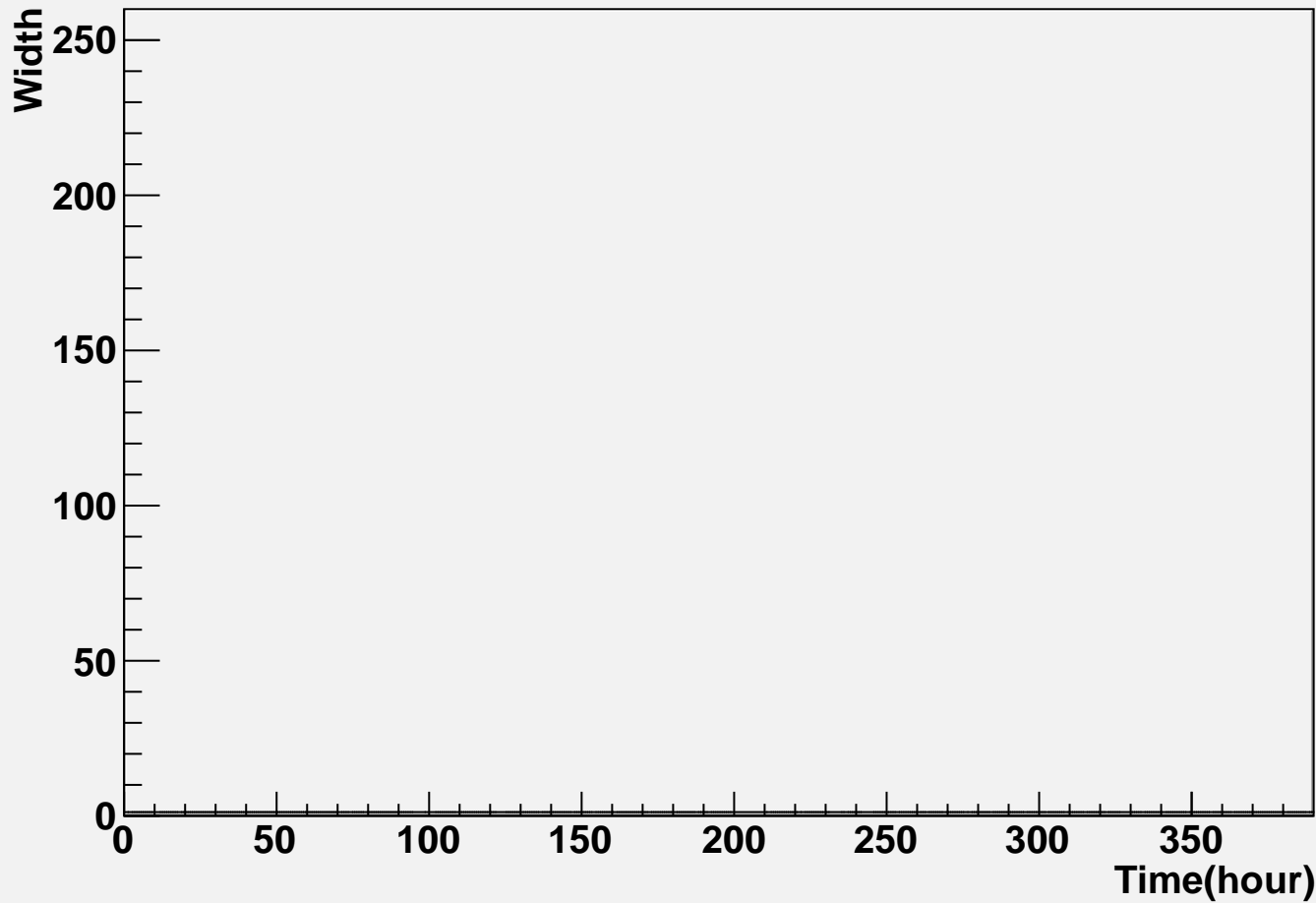
width-S0-SFEA2-Ch6



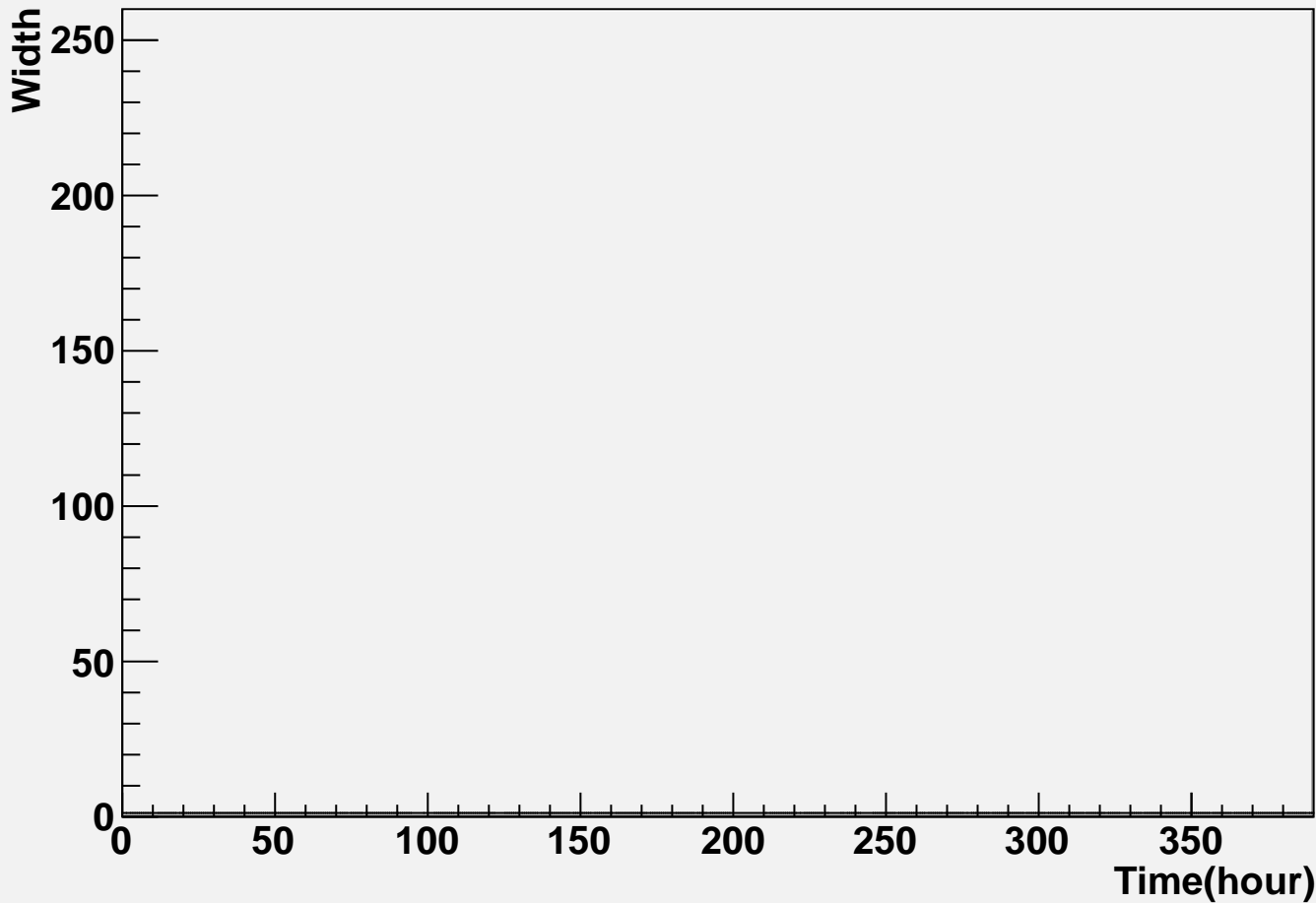
width-S0-SFEA2-Ch7



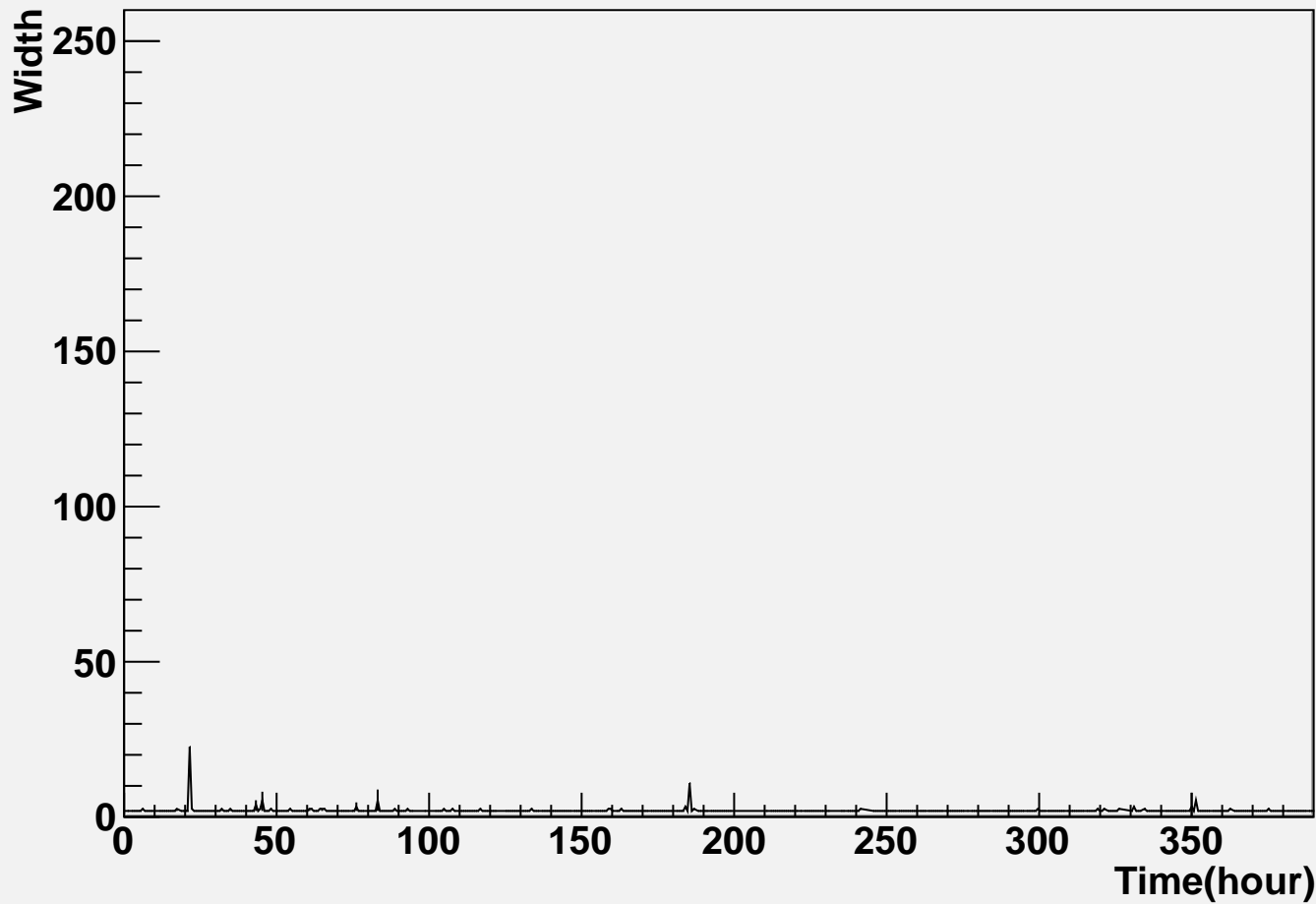
width-S0-SFEA2-Ch8



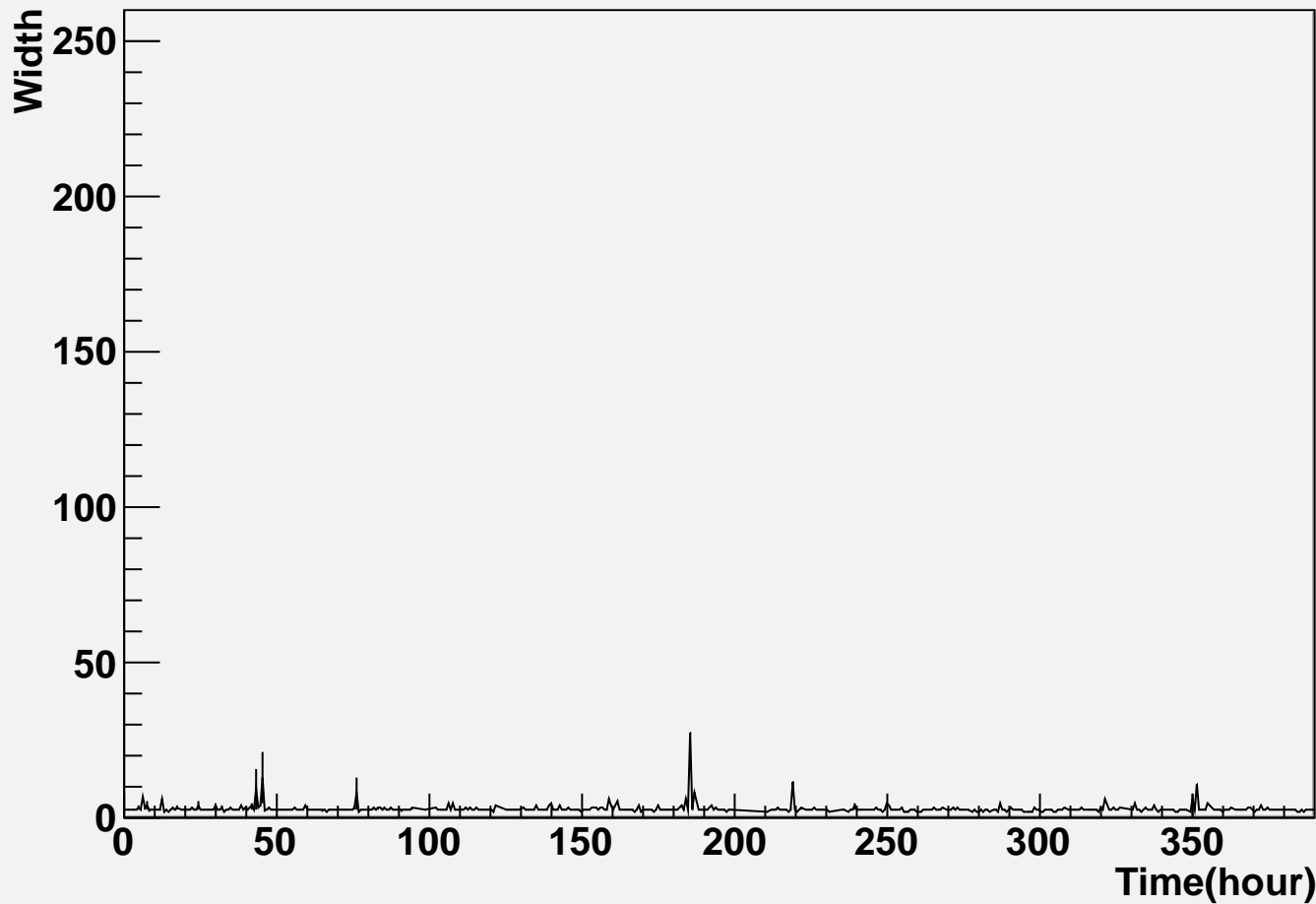
width-S0-SFEA2-Ch9



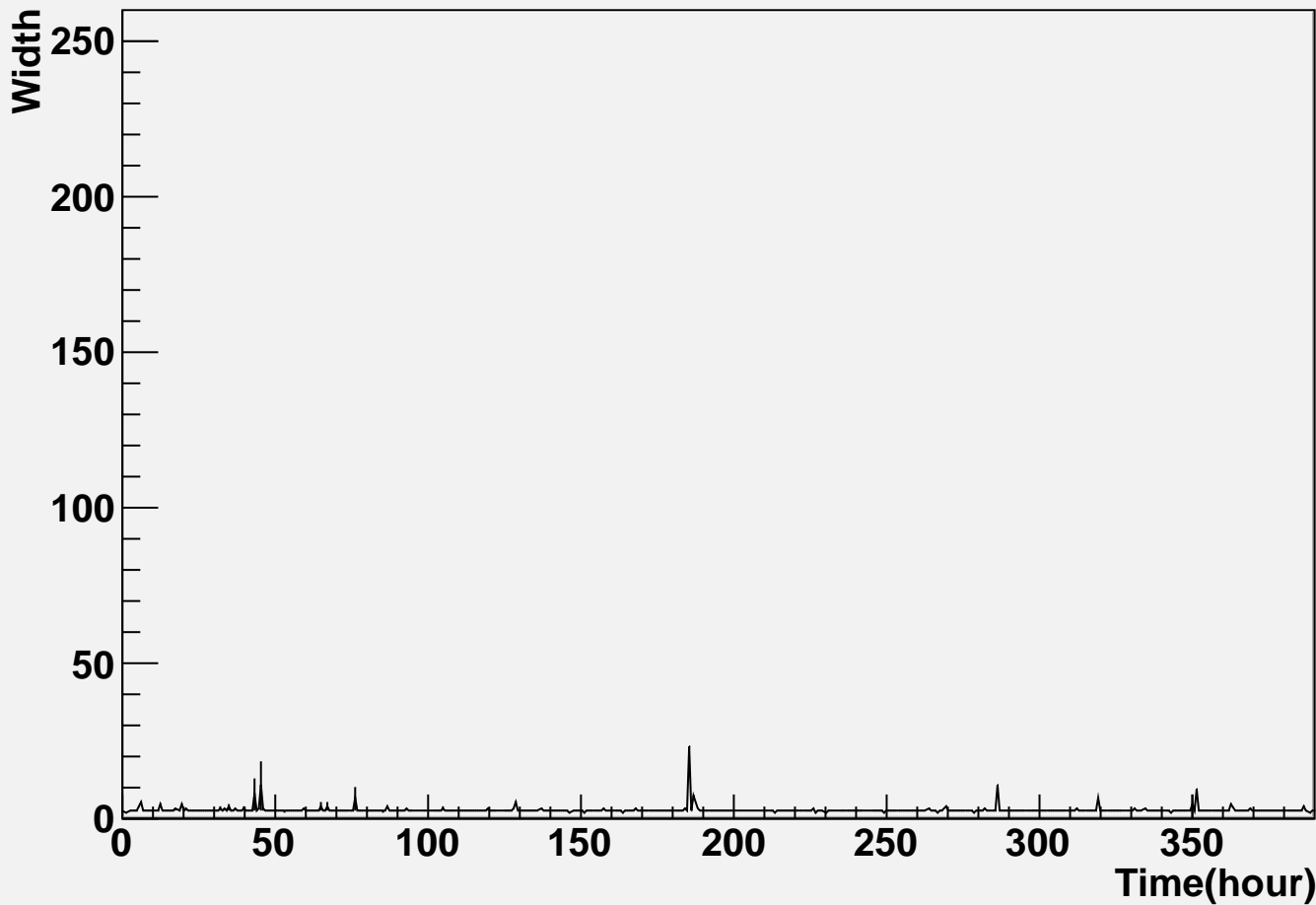
width-S0-SFEC00-Ch0



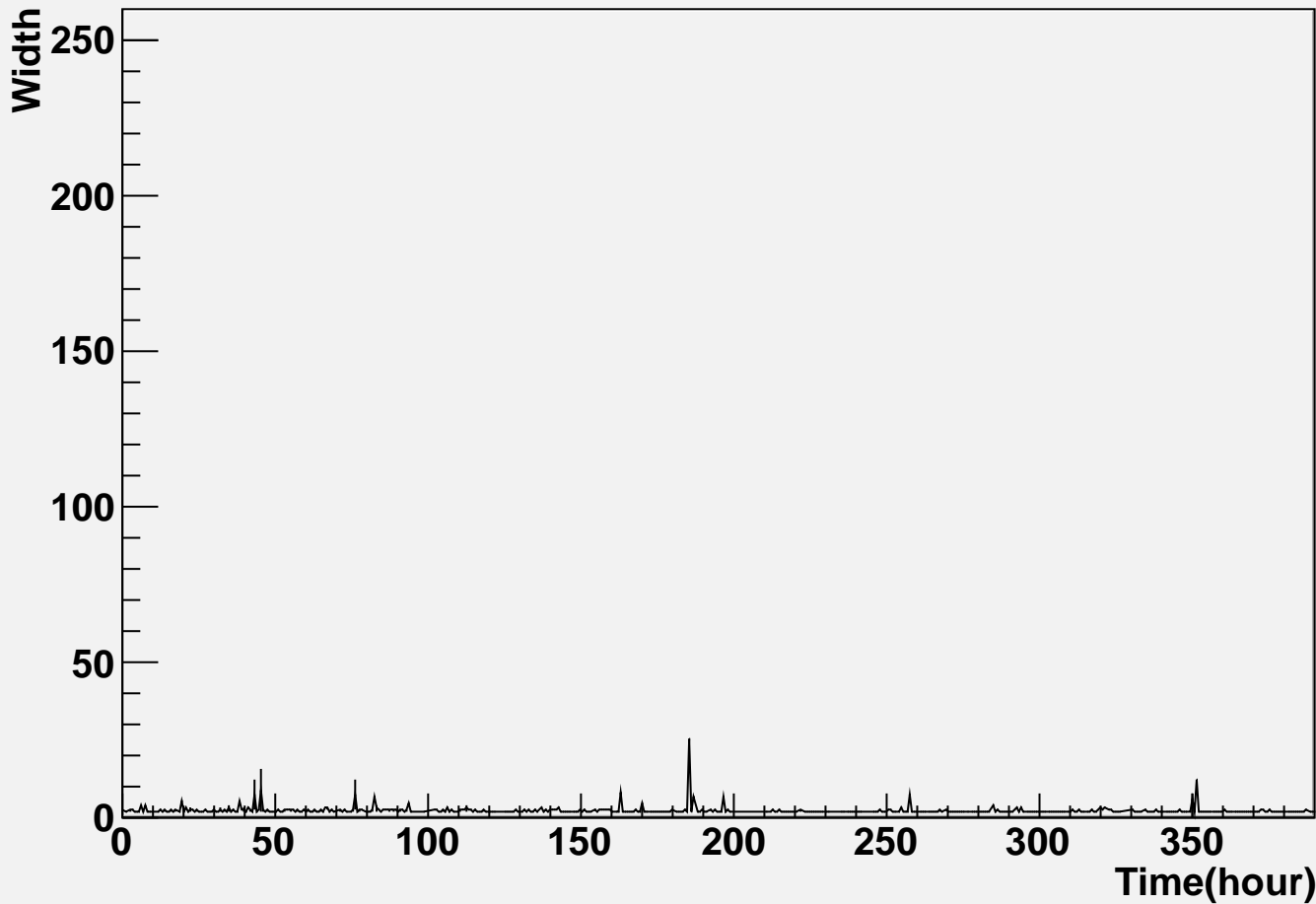
width-S0-SFEC00-Ch1



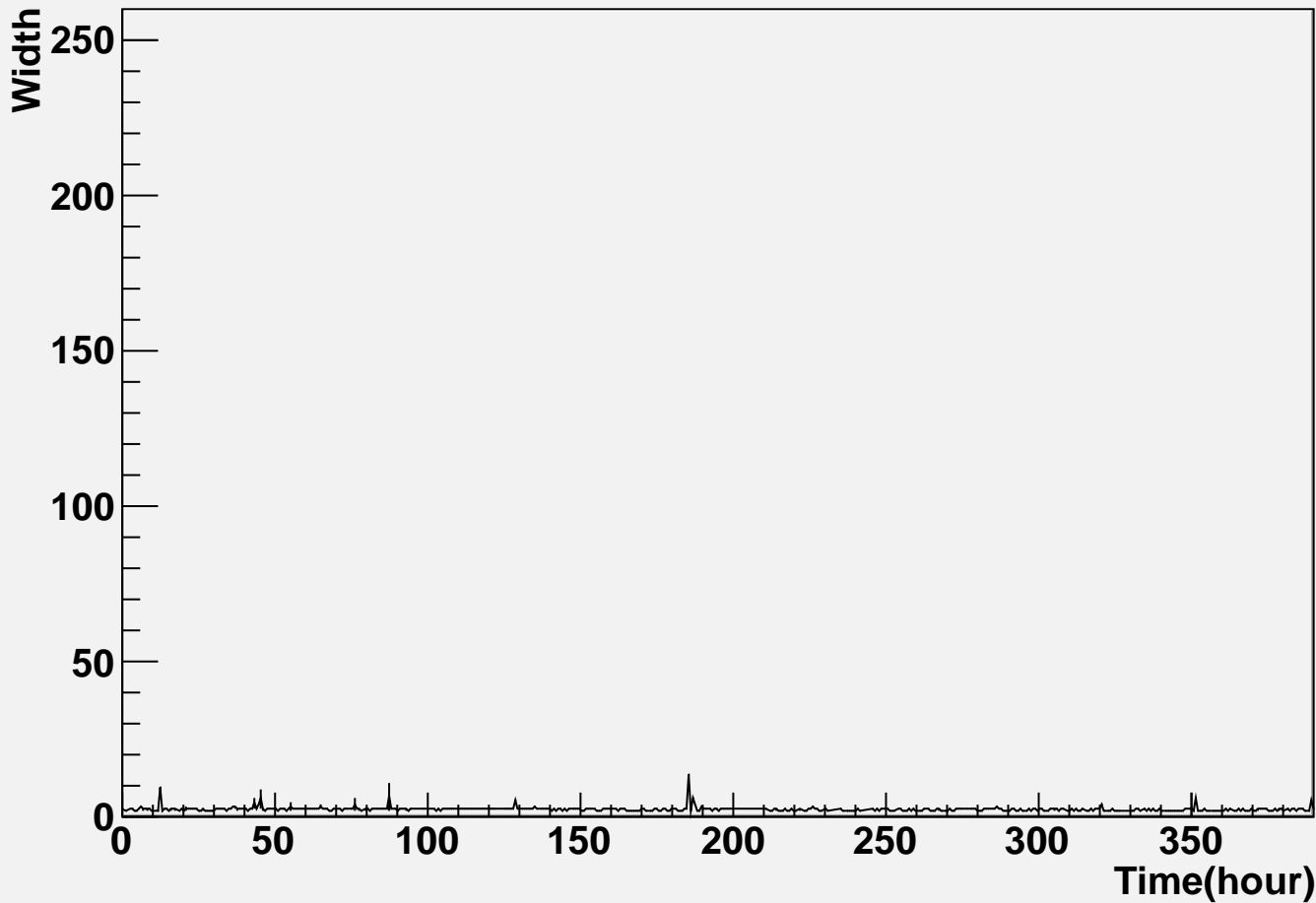
width-S0-SFEC00-Ch2



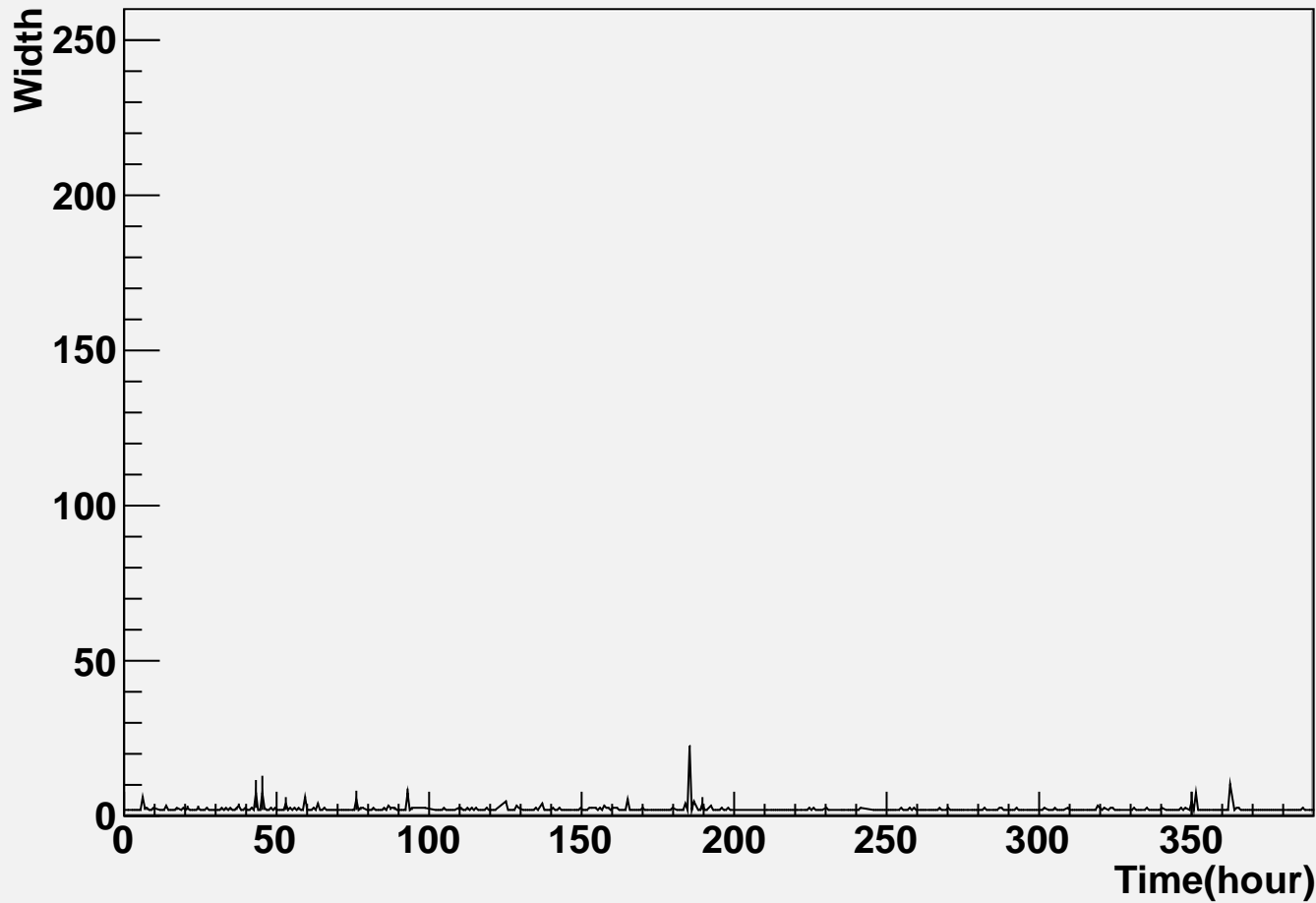
width-S0-SFEC00-Ch3



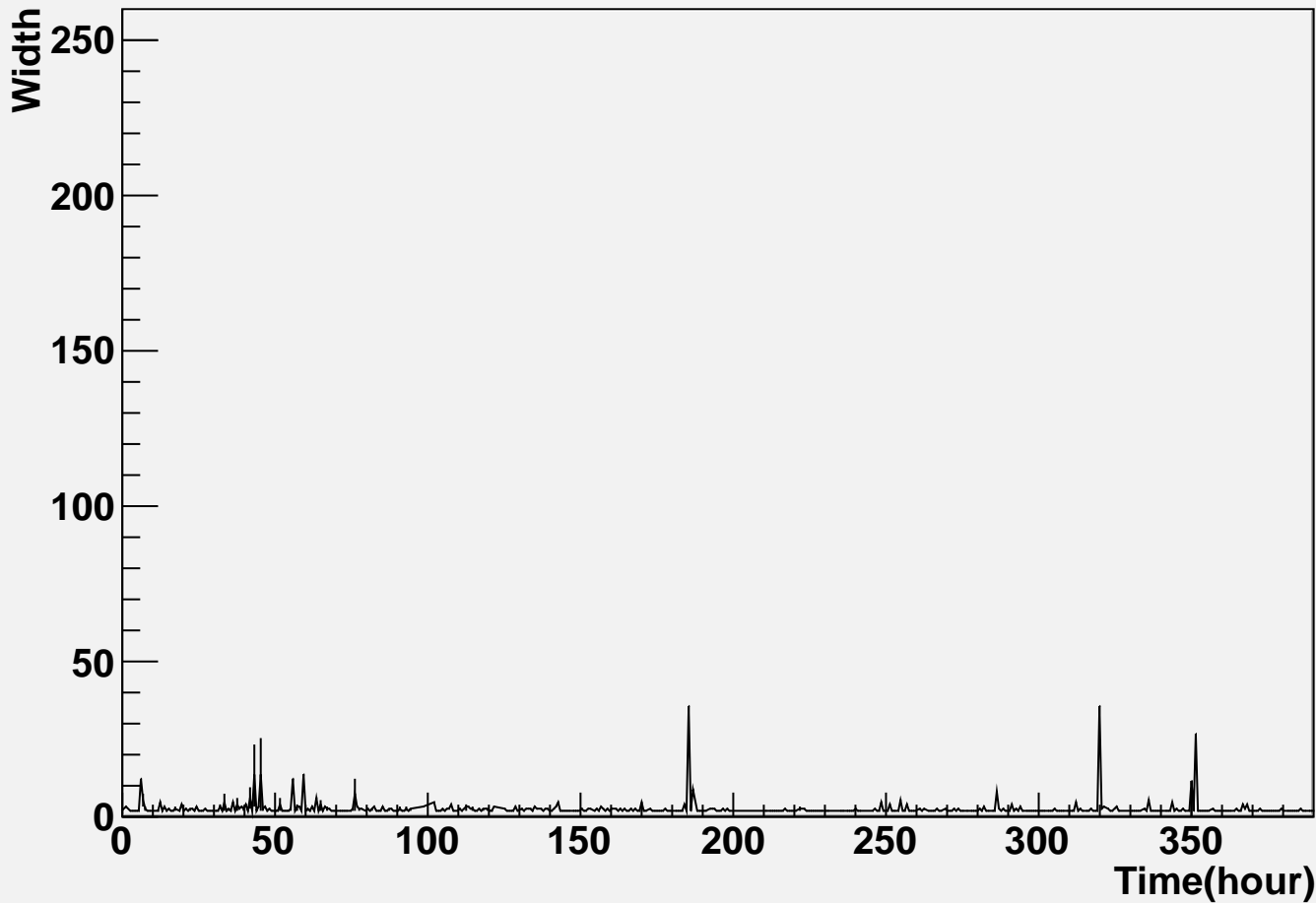
width-S0-SFEC00-Ch4



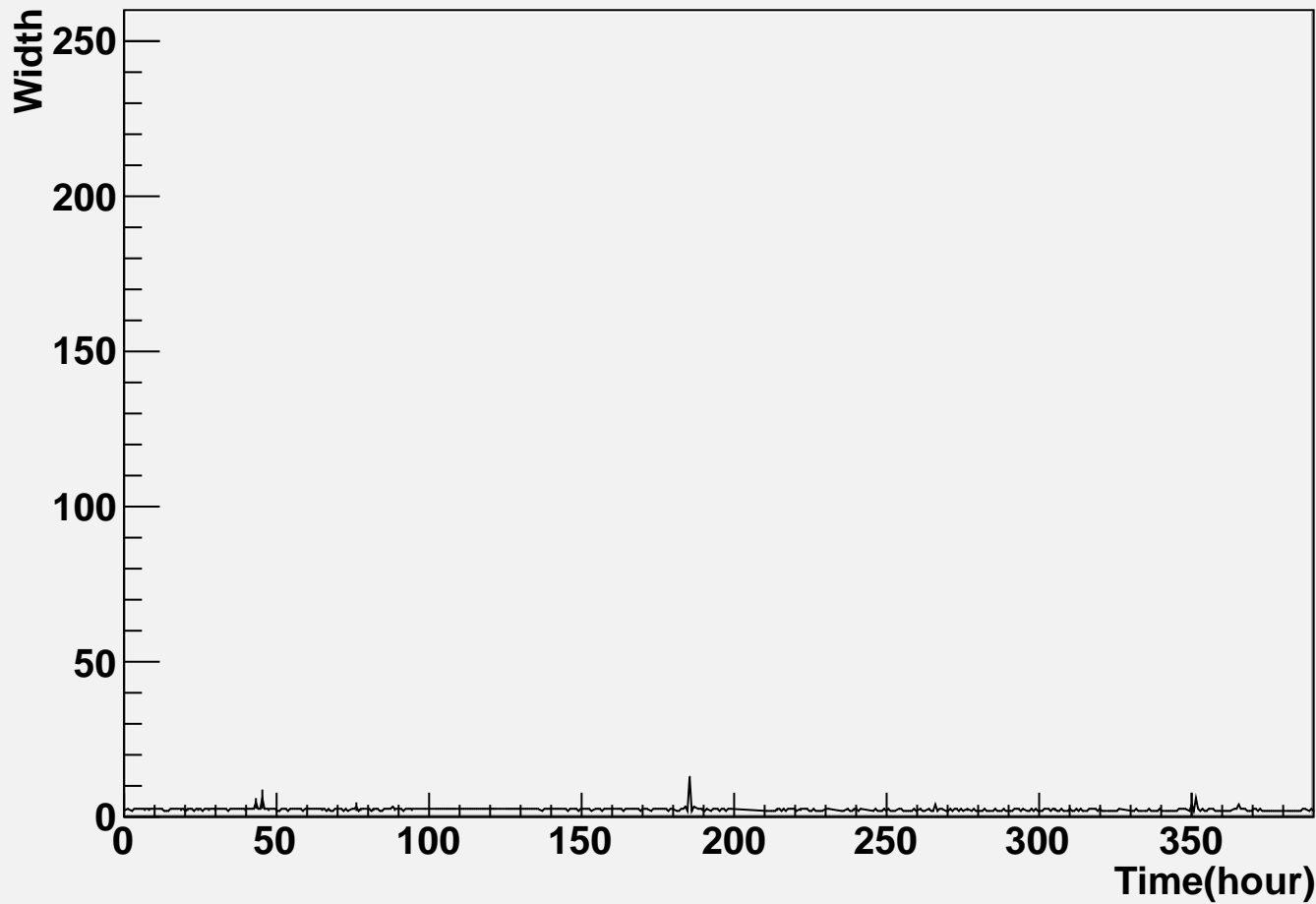
width-S0-SFEC00-Ch5



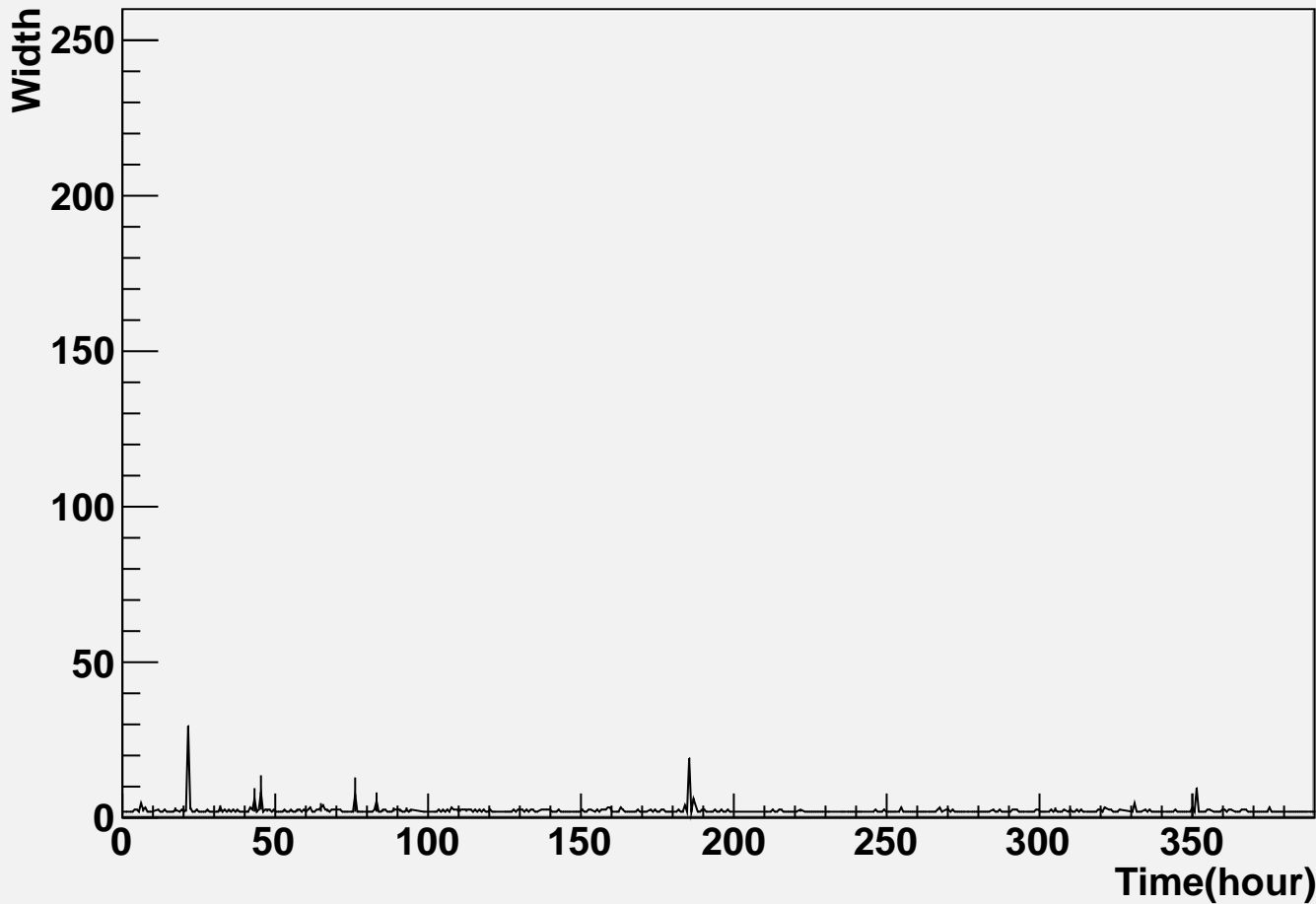
width-S0-SFEC00-Ch6



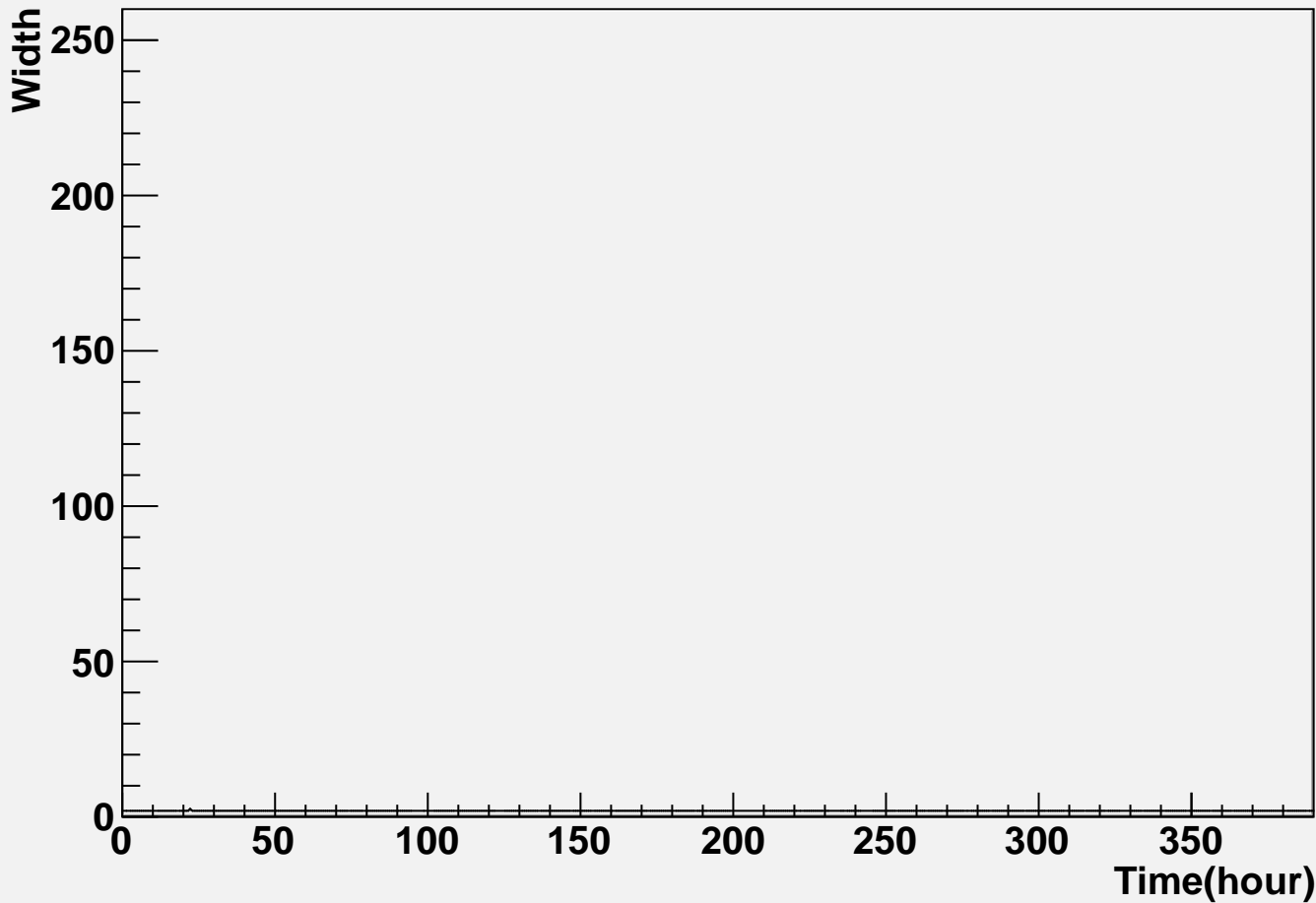
width-S0-SFEC00-Ch7



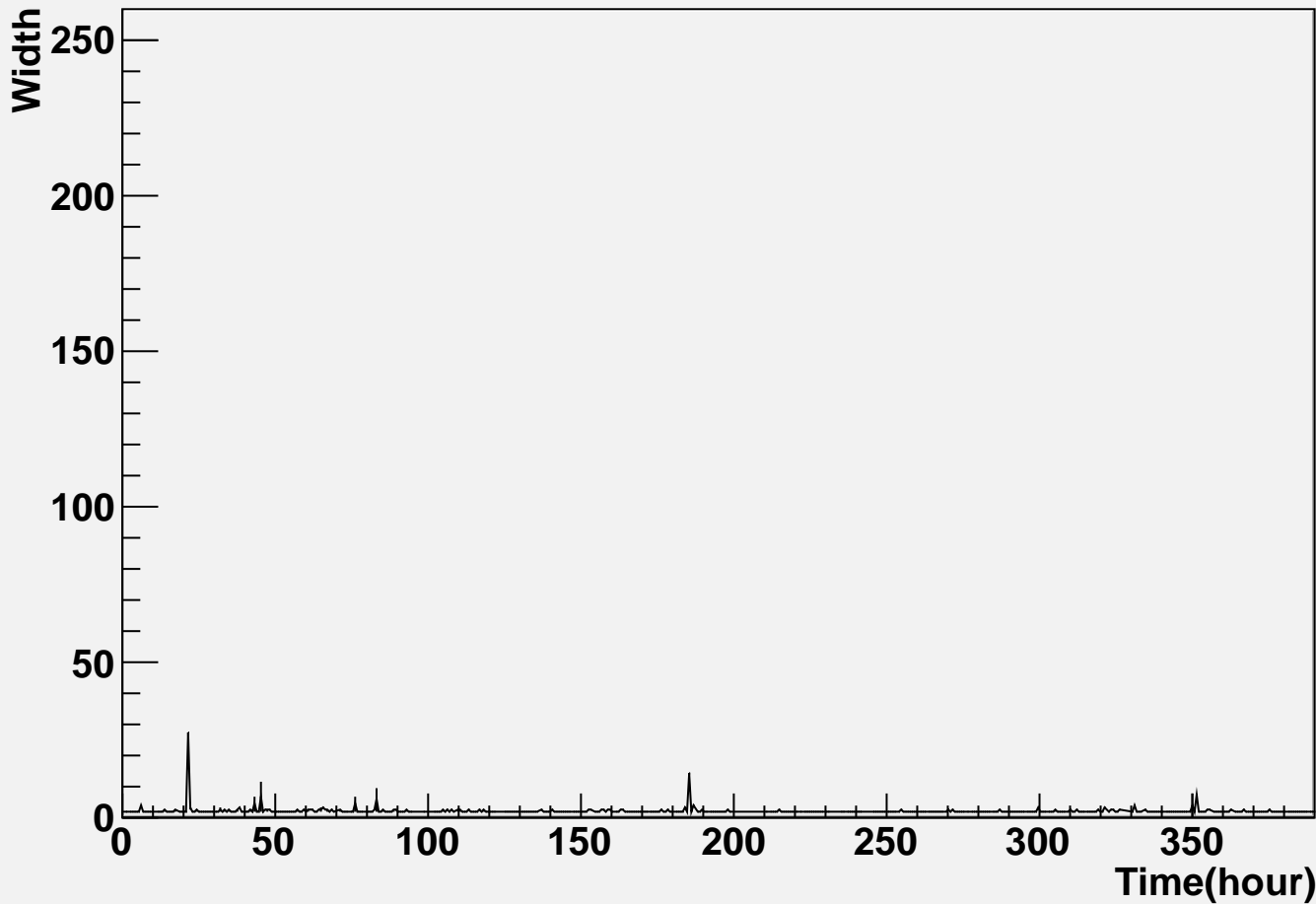
width-S0-SFEC00-Ch8



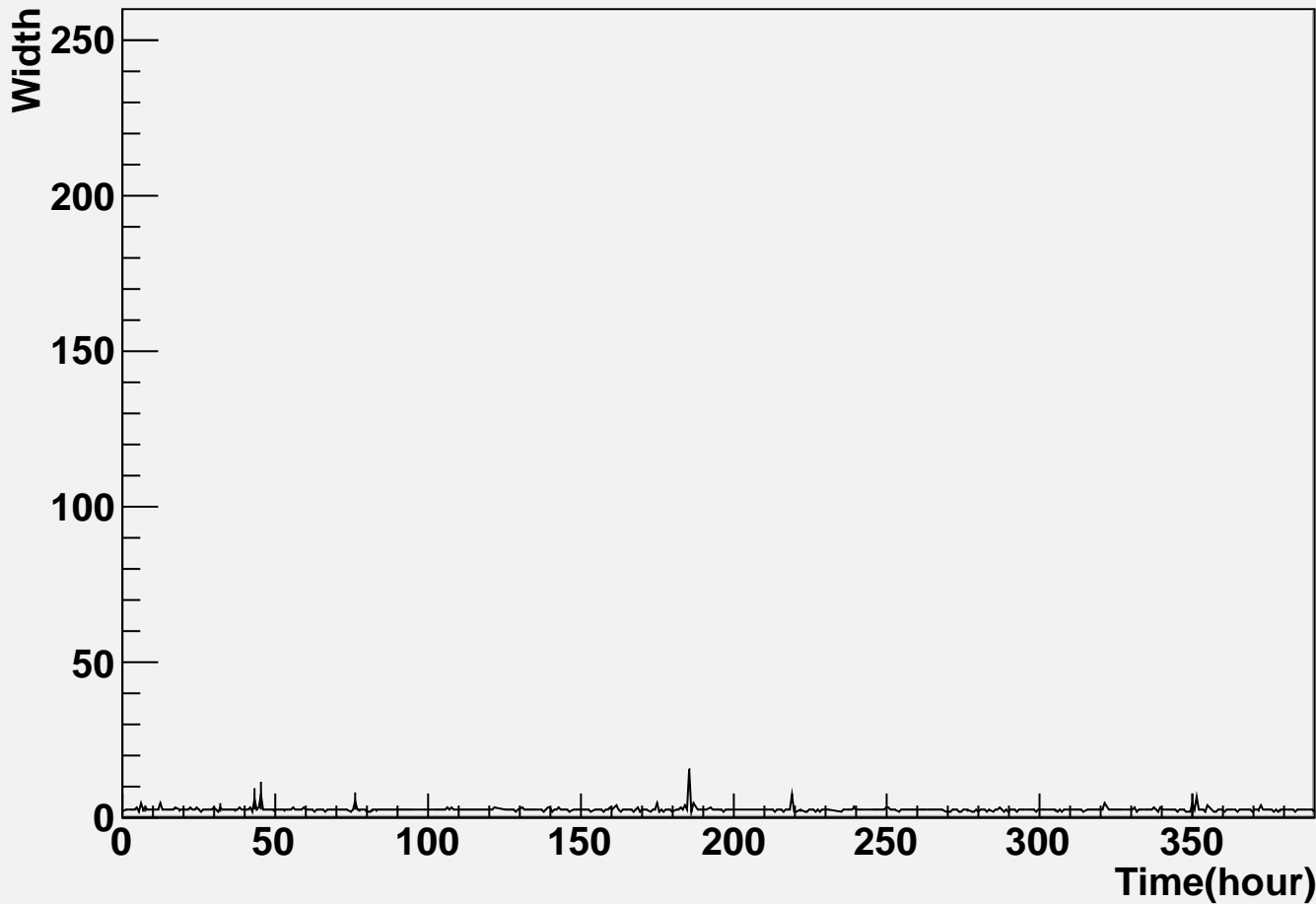
width-S0-SFEC00-Ch9



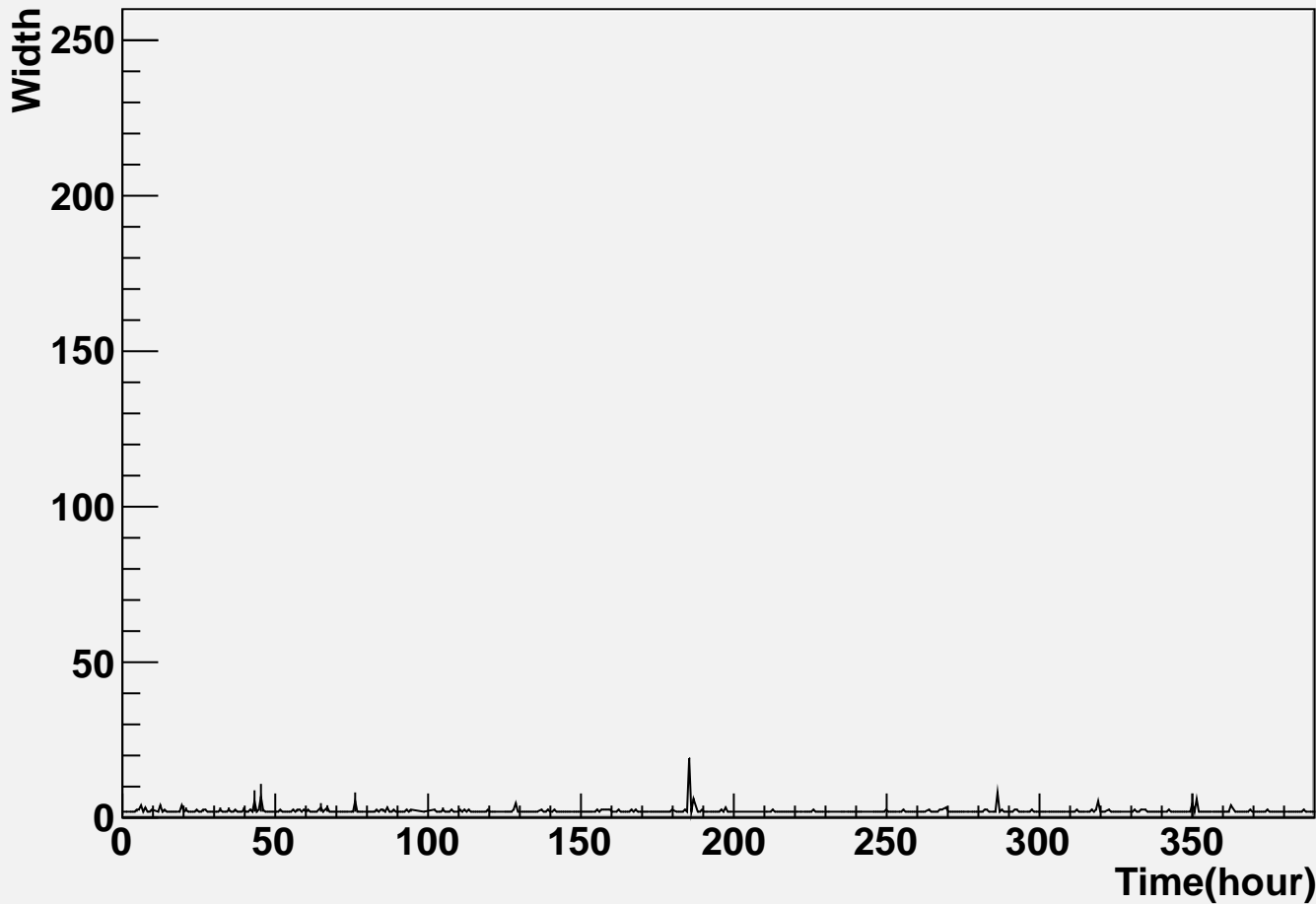
width-S0-SFEC01-Ch0



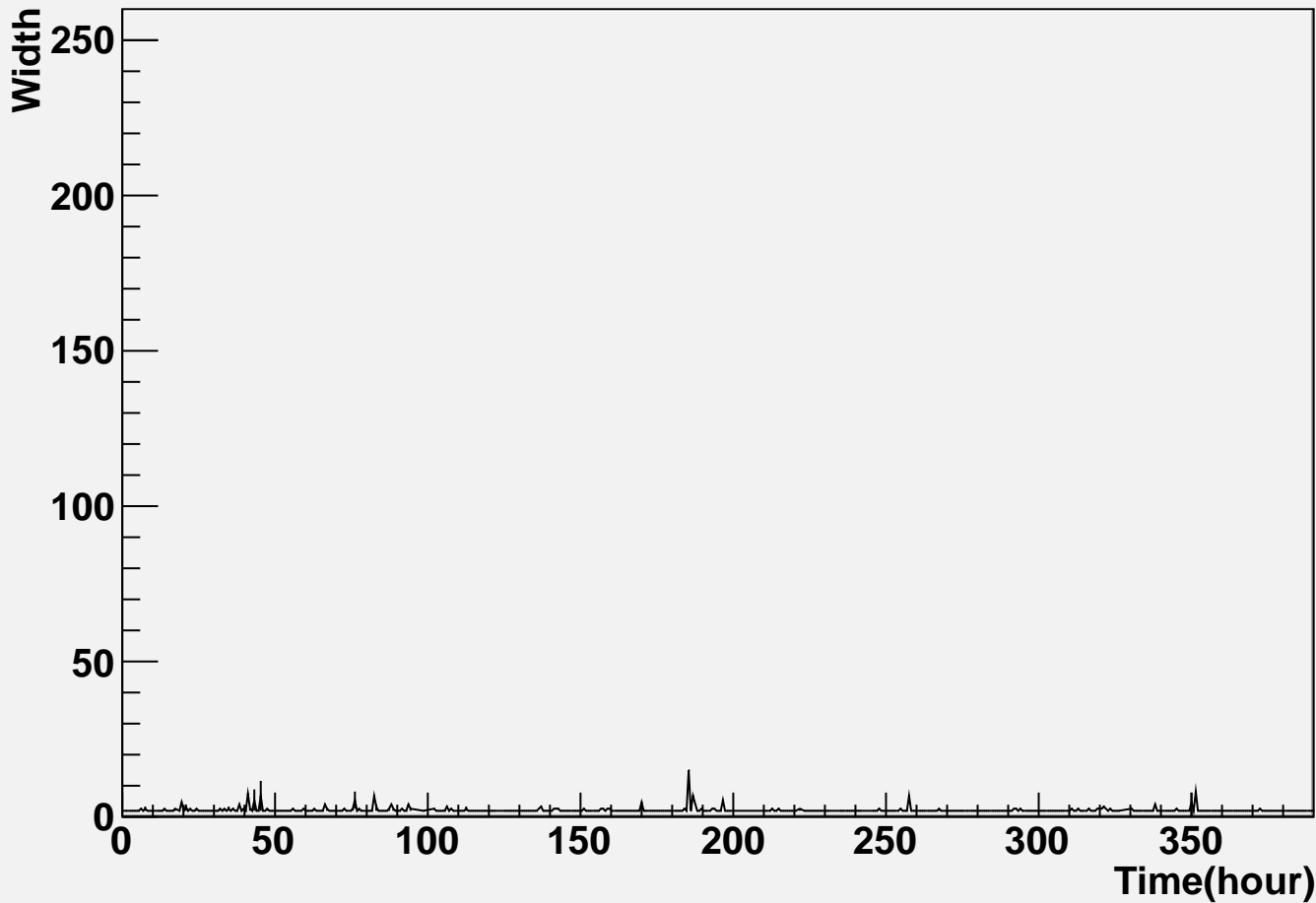
width-S0-SFEC01-Ch1



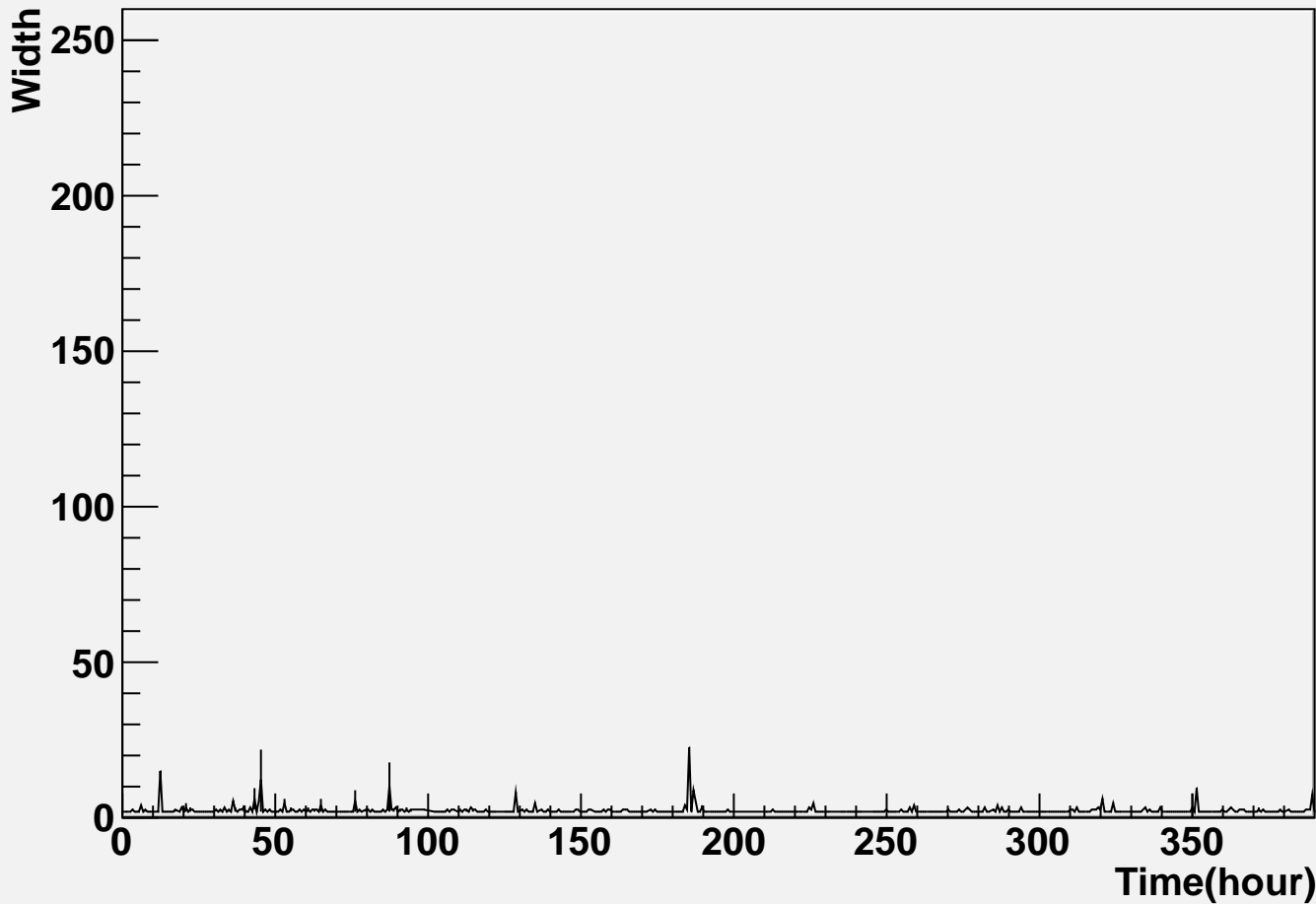
width-S0-SFEC01-Ch2



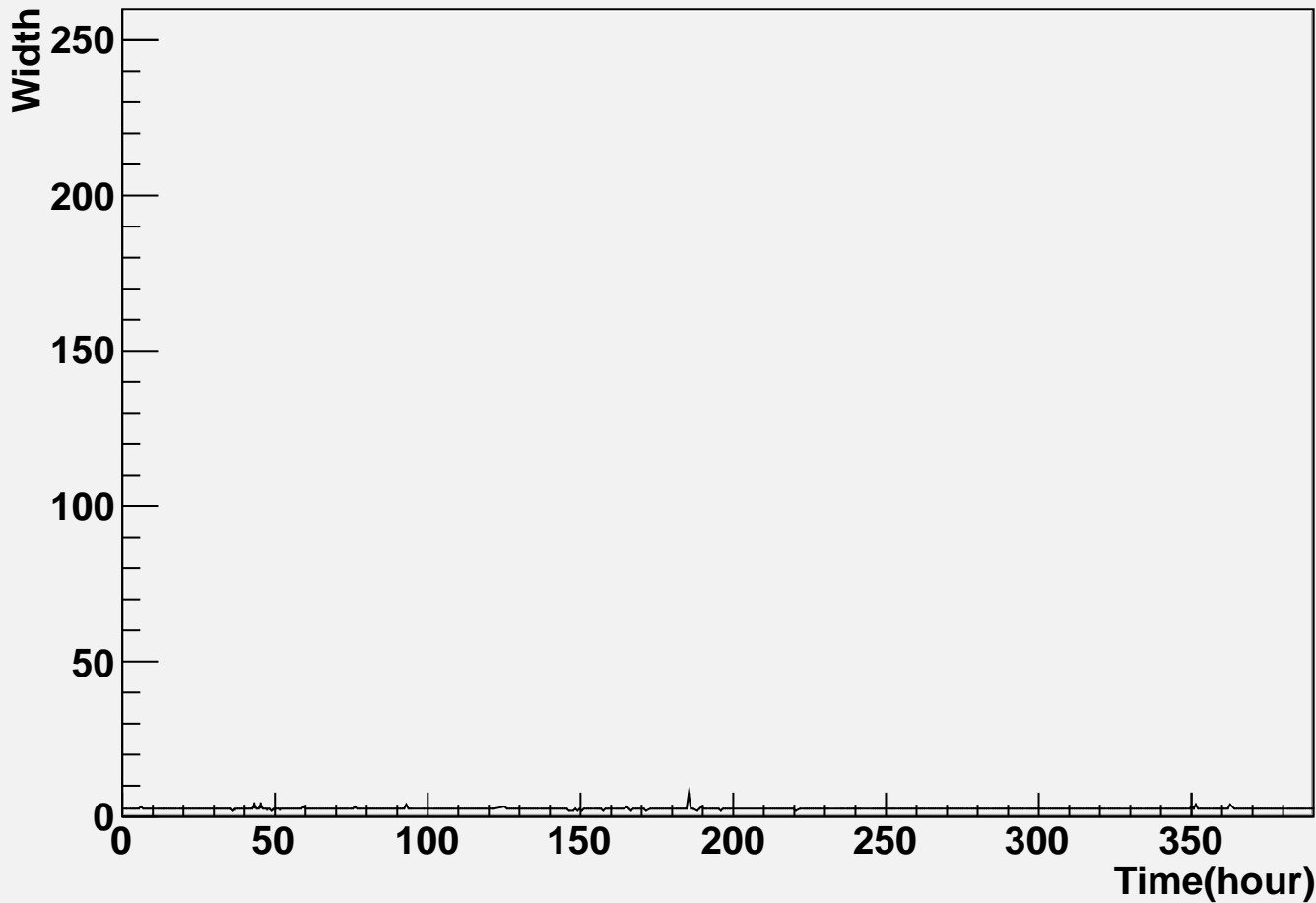
width-S0-SFEC01-Ch3



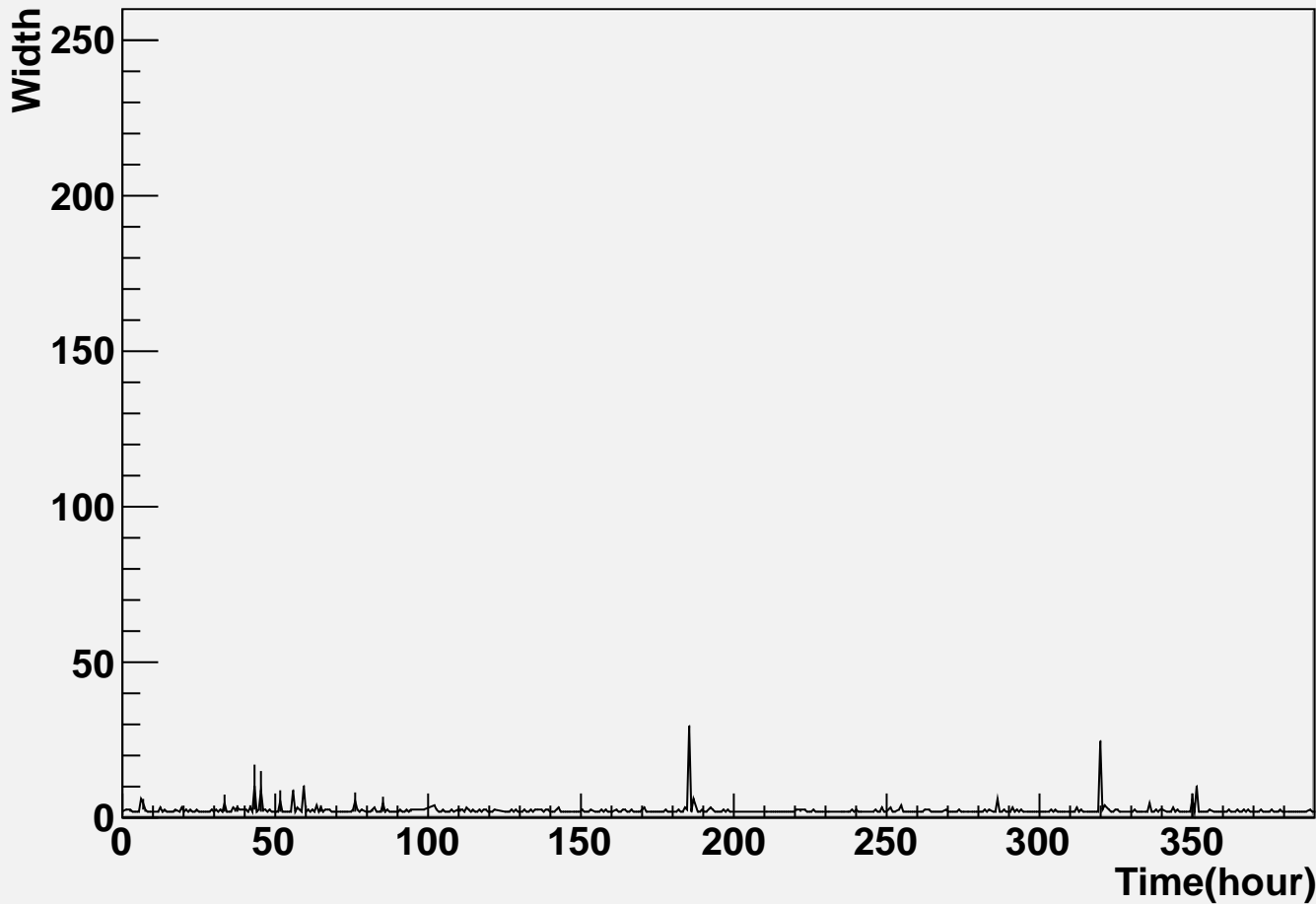
width-S0-SFEC01-Ch4



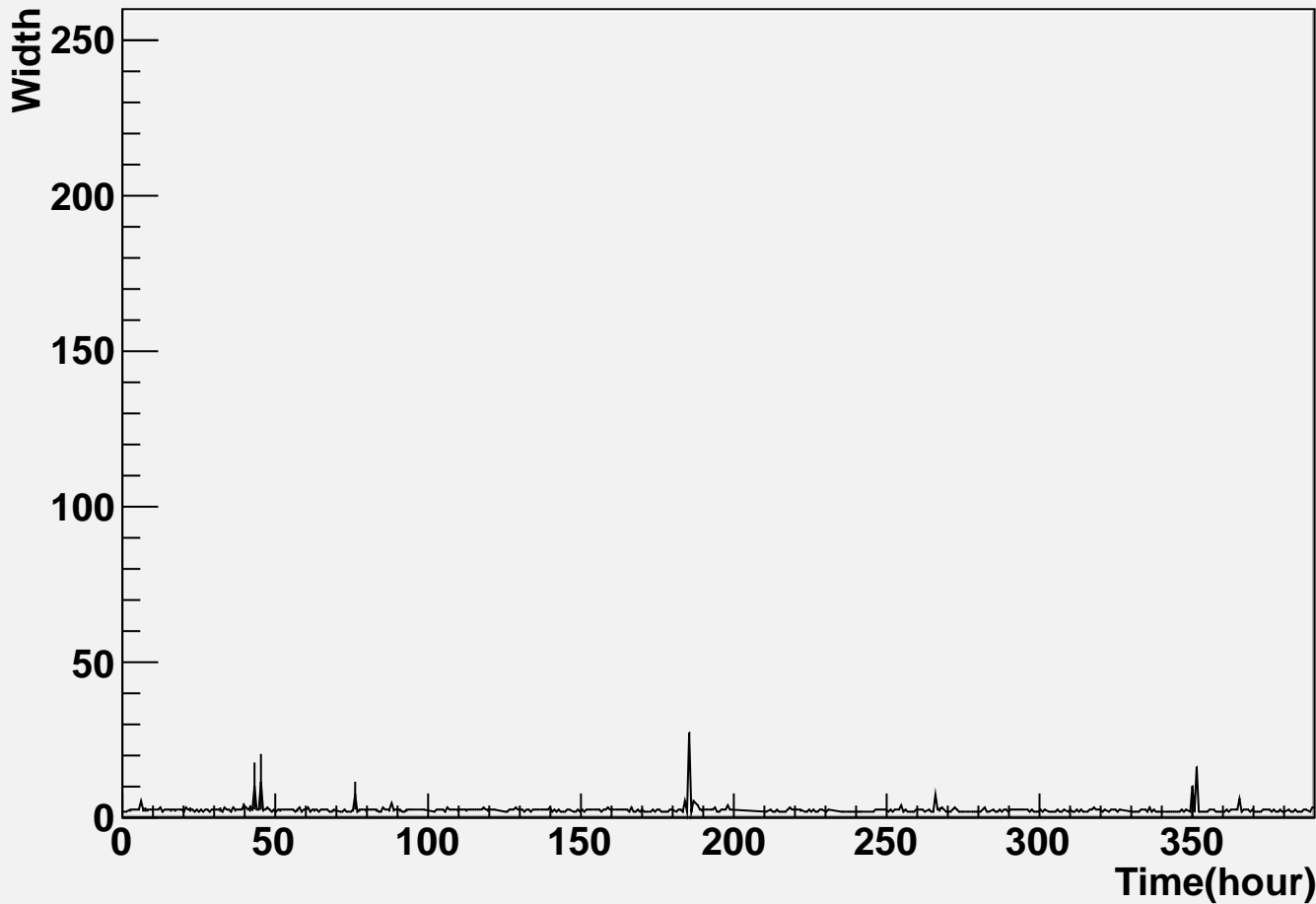
width-S0-SFEC01-Ch5



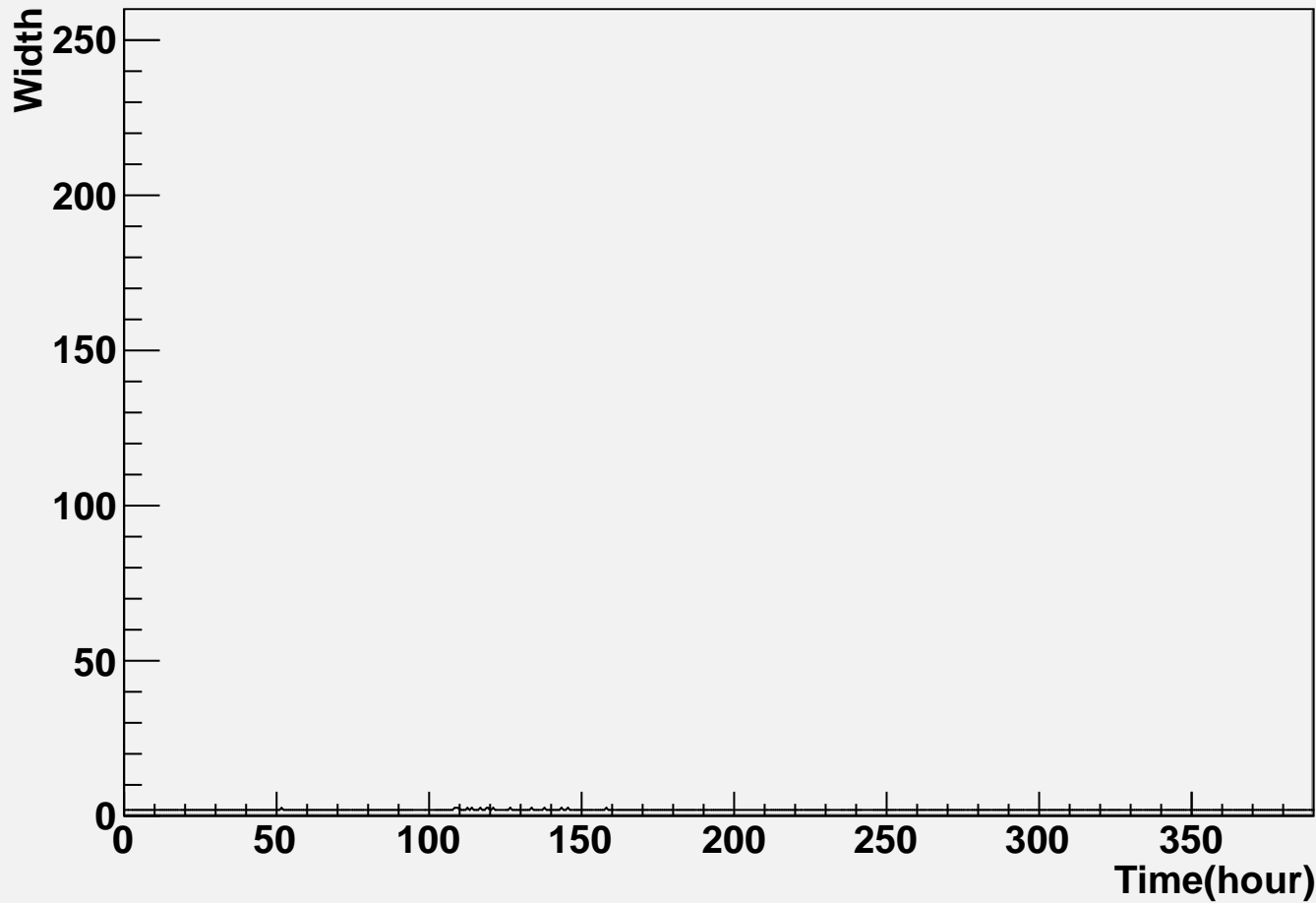
width-S0-SFEC01-Ch6



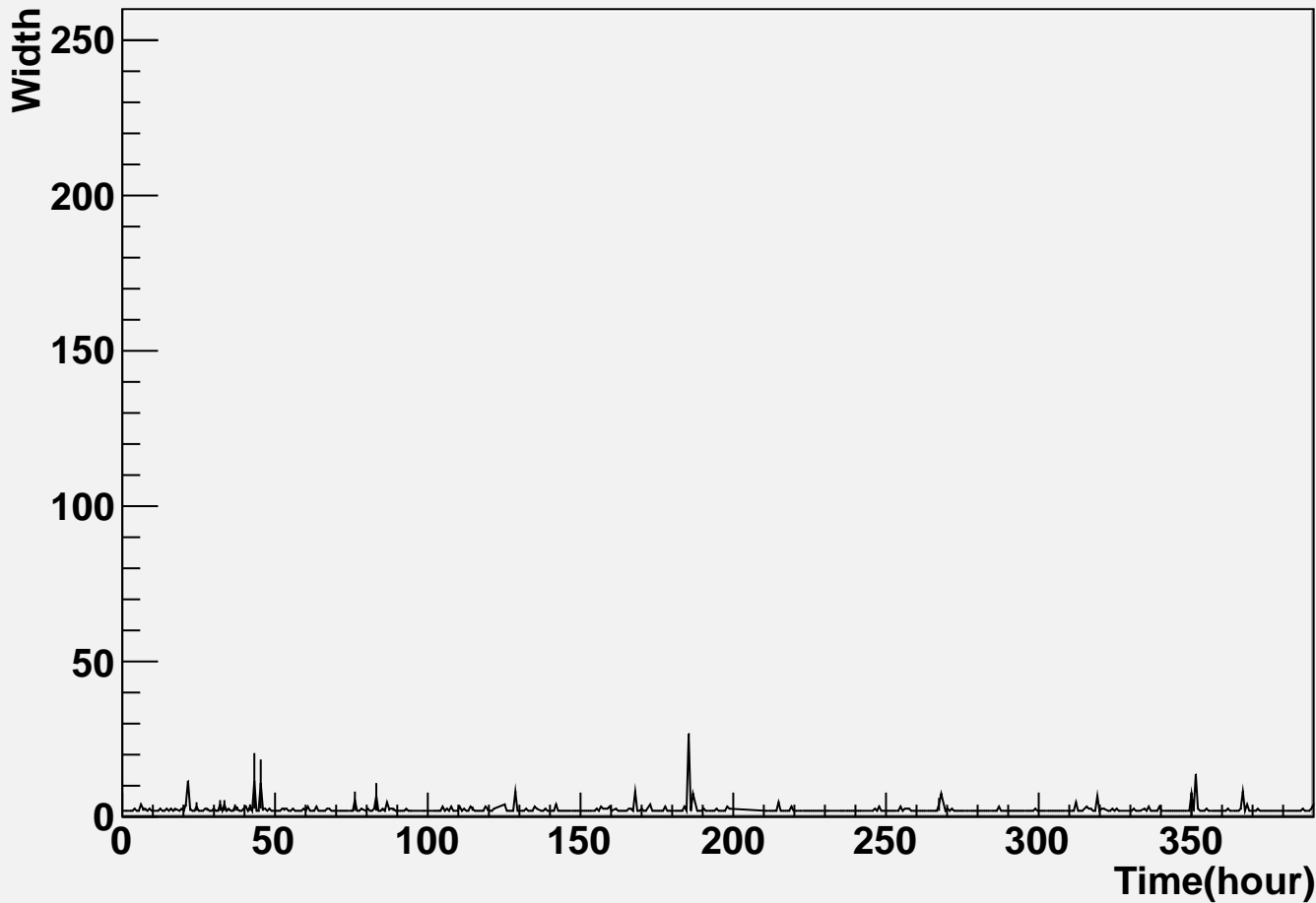
width-S0-SFEC01-Ch8



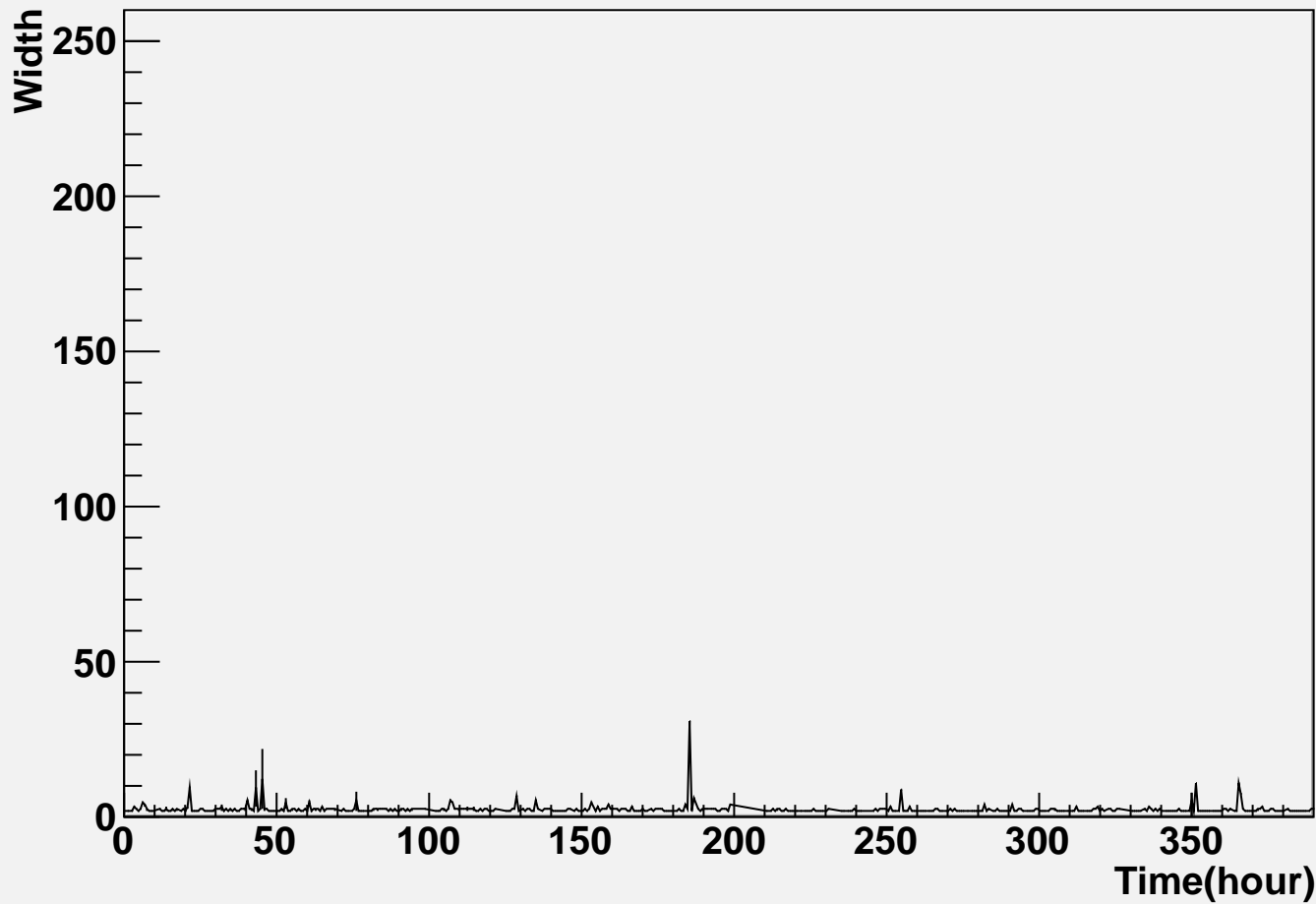
width-S0-SFEC01-Ch9



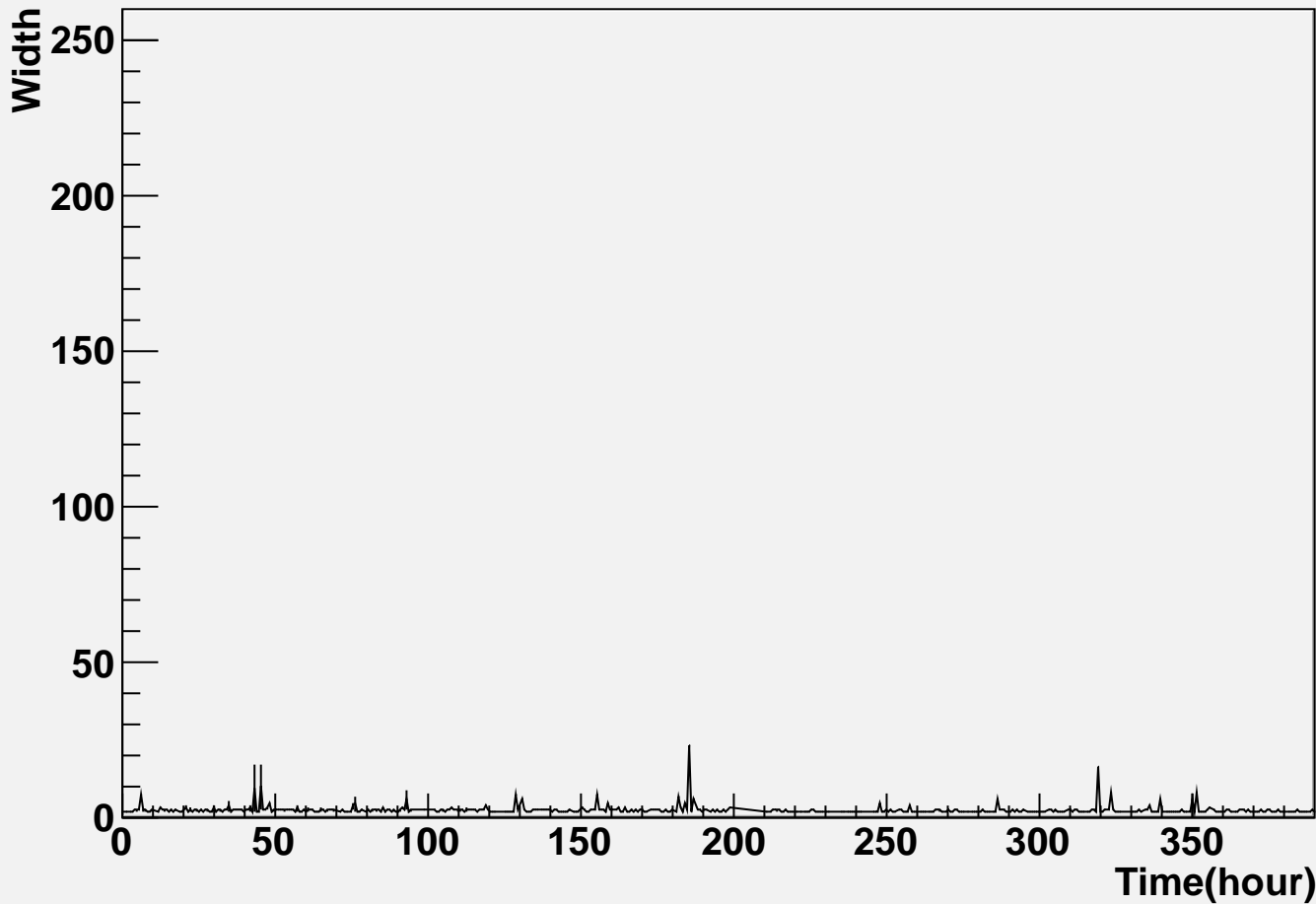
width-S0-SFEC10-Ch0



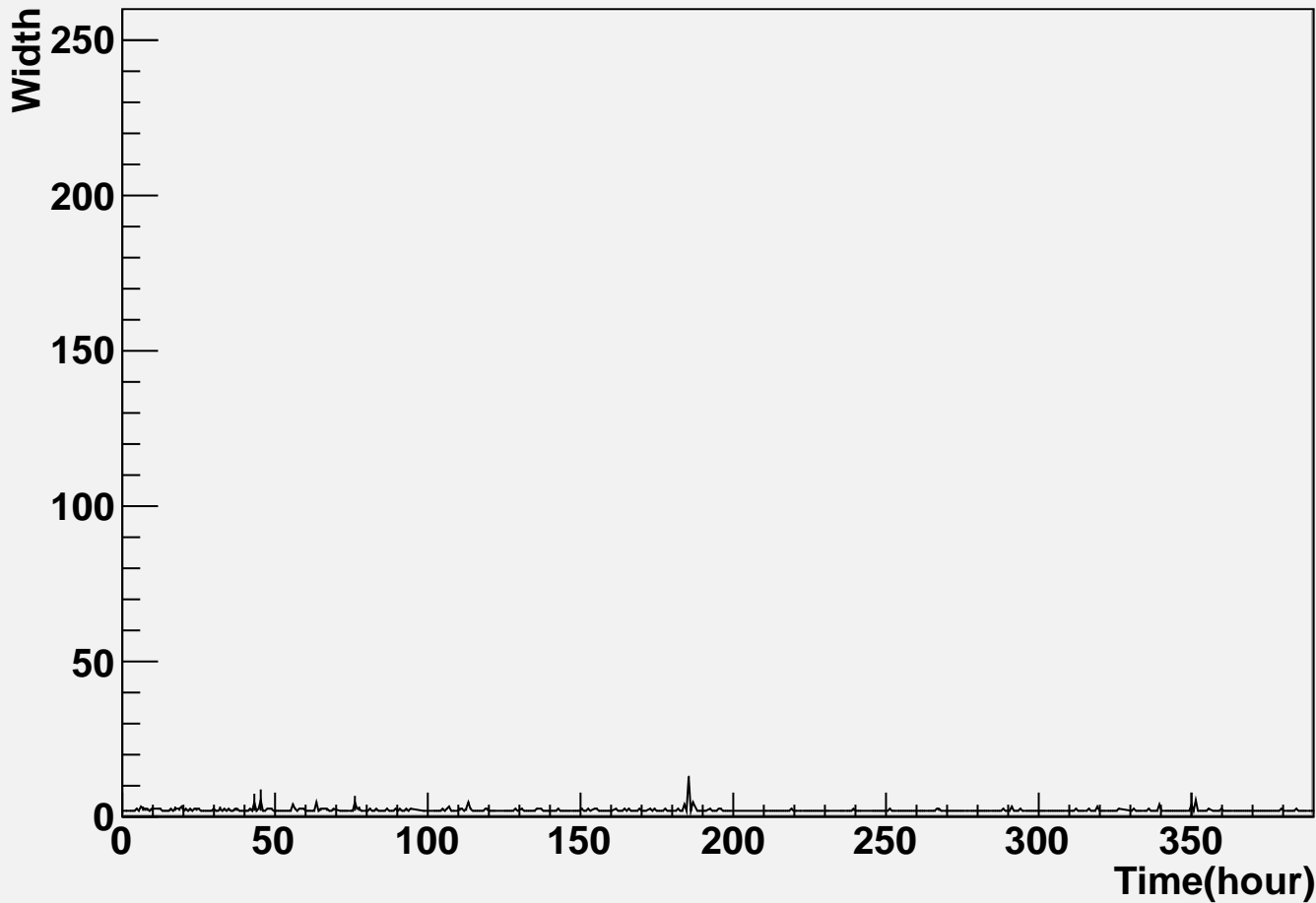
width-S0-SFEC10-Ch1



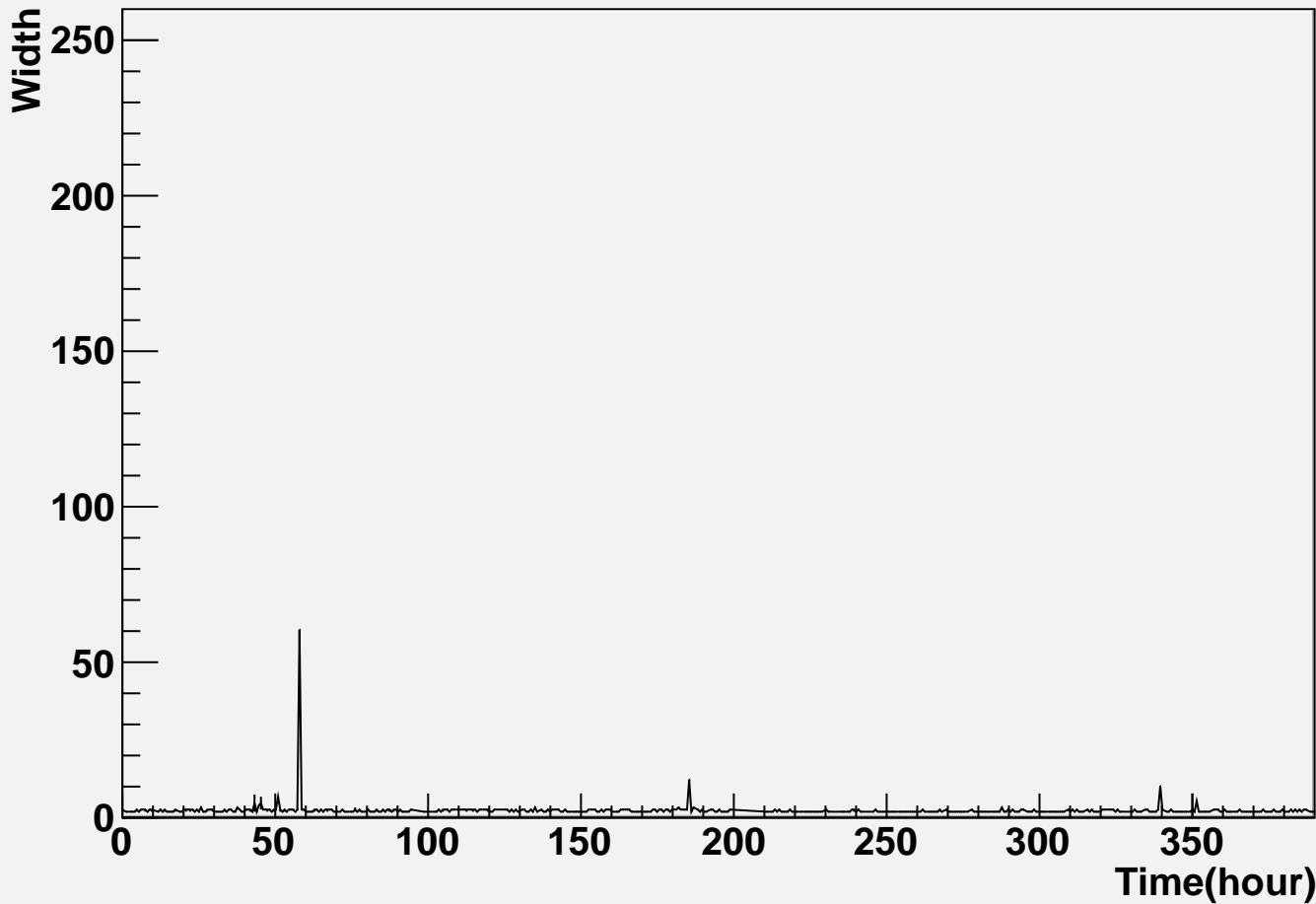
width-S0-SFEC10-Ch2



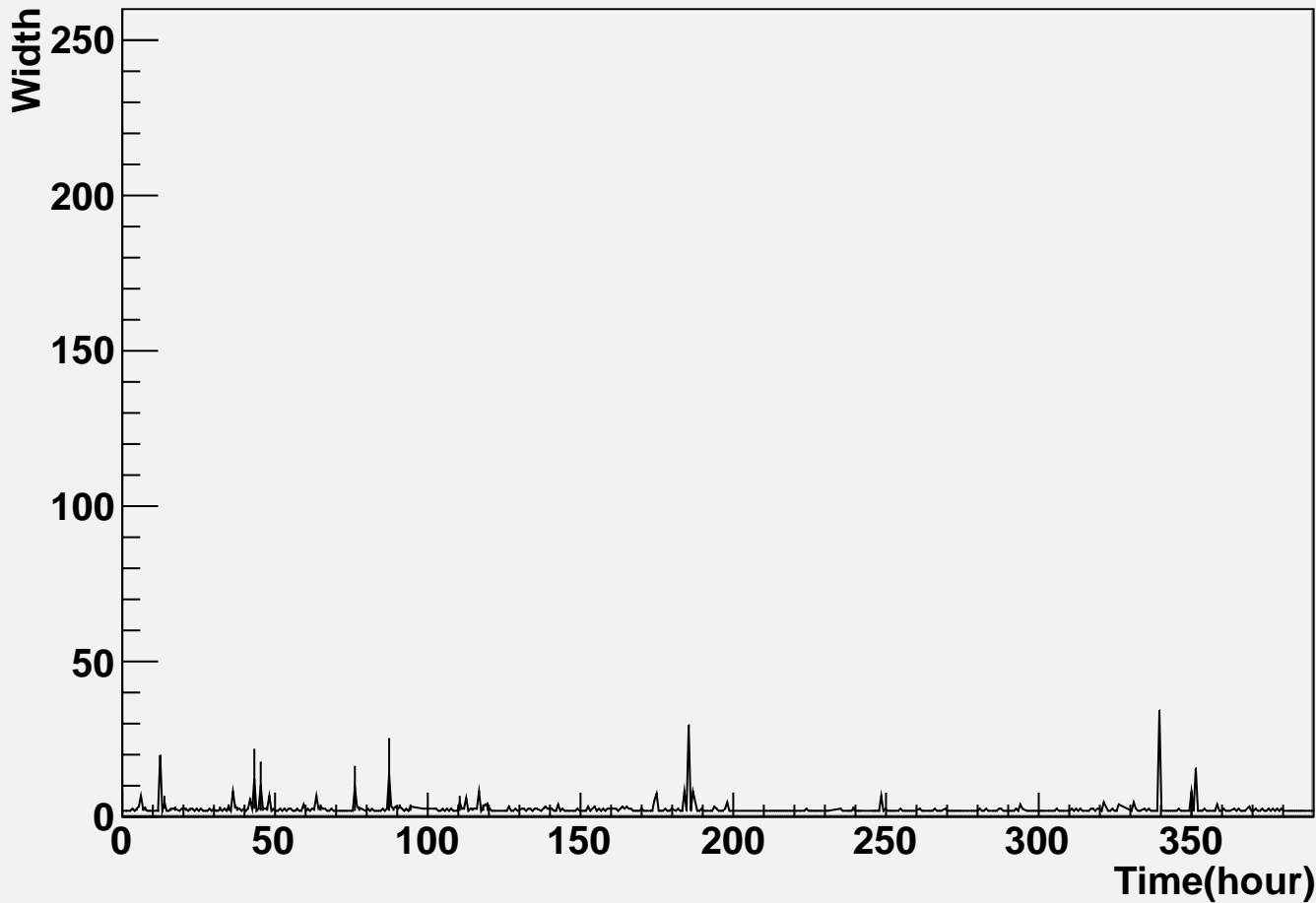
width-S0-SFEC10-Ch3



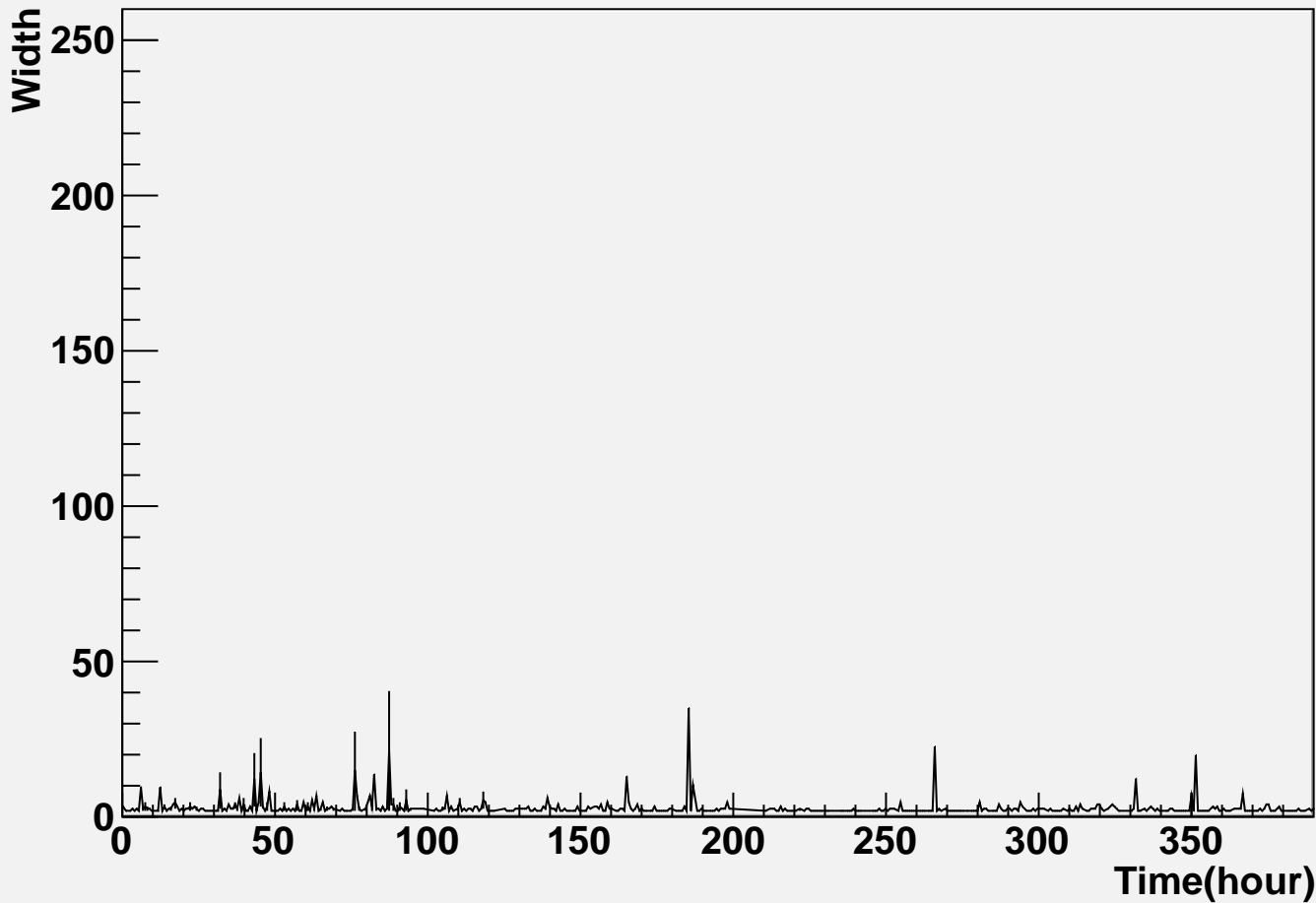
width-S0-SFEC10-Ch4



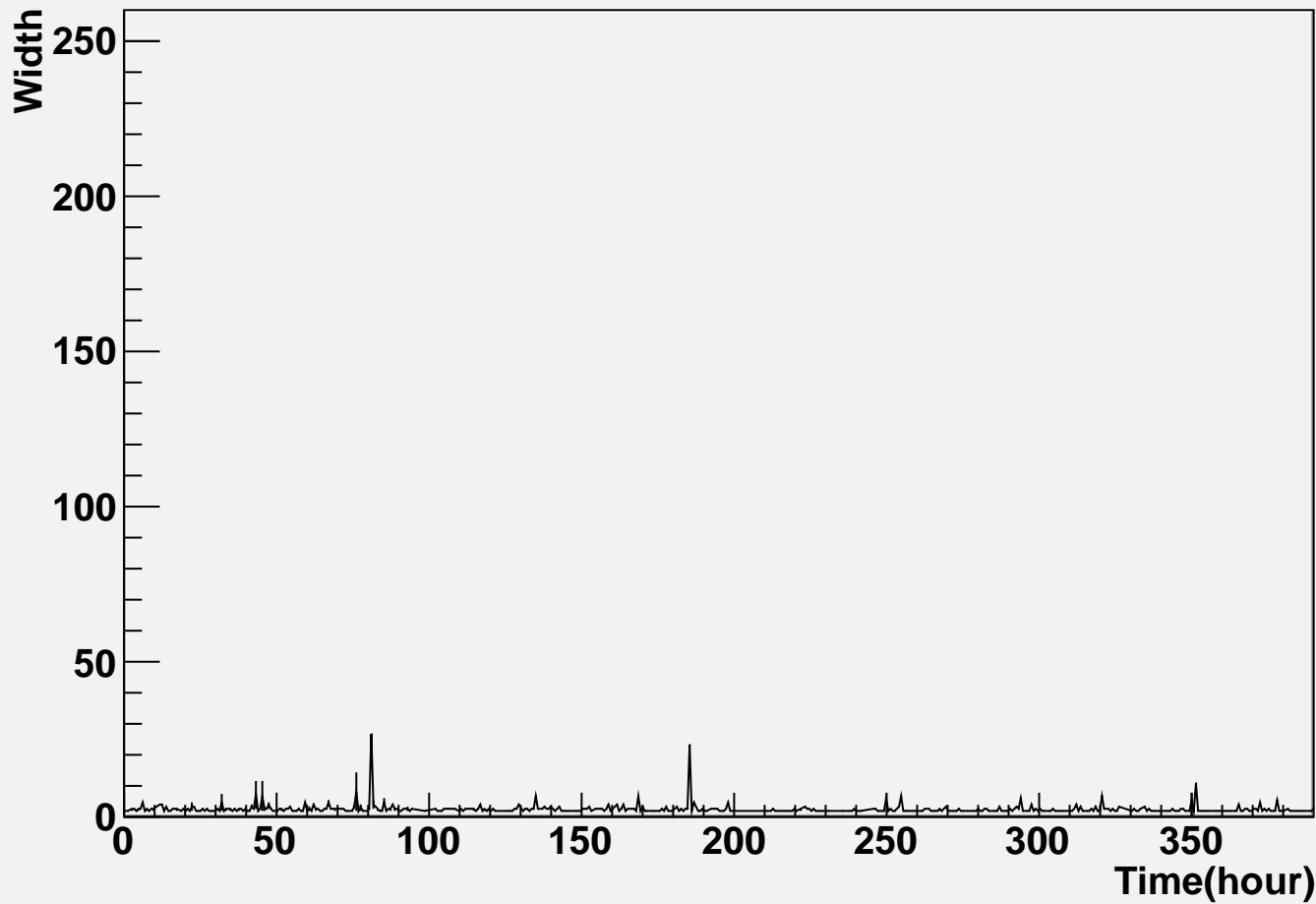
width-S0-SFEC10-Ch5



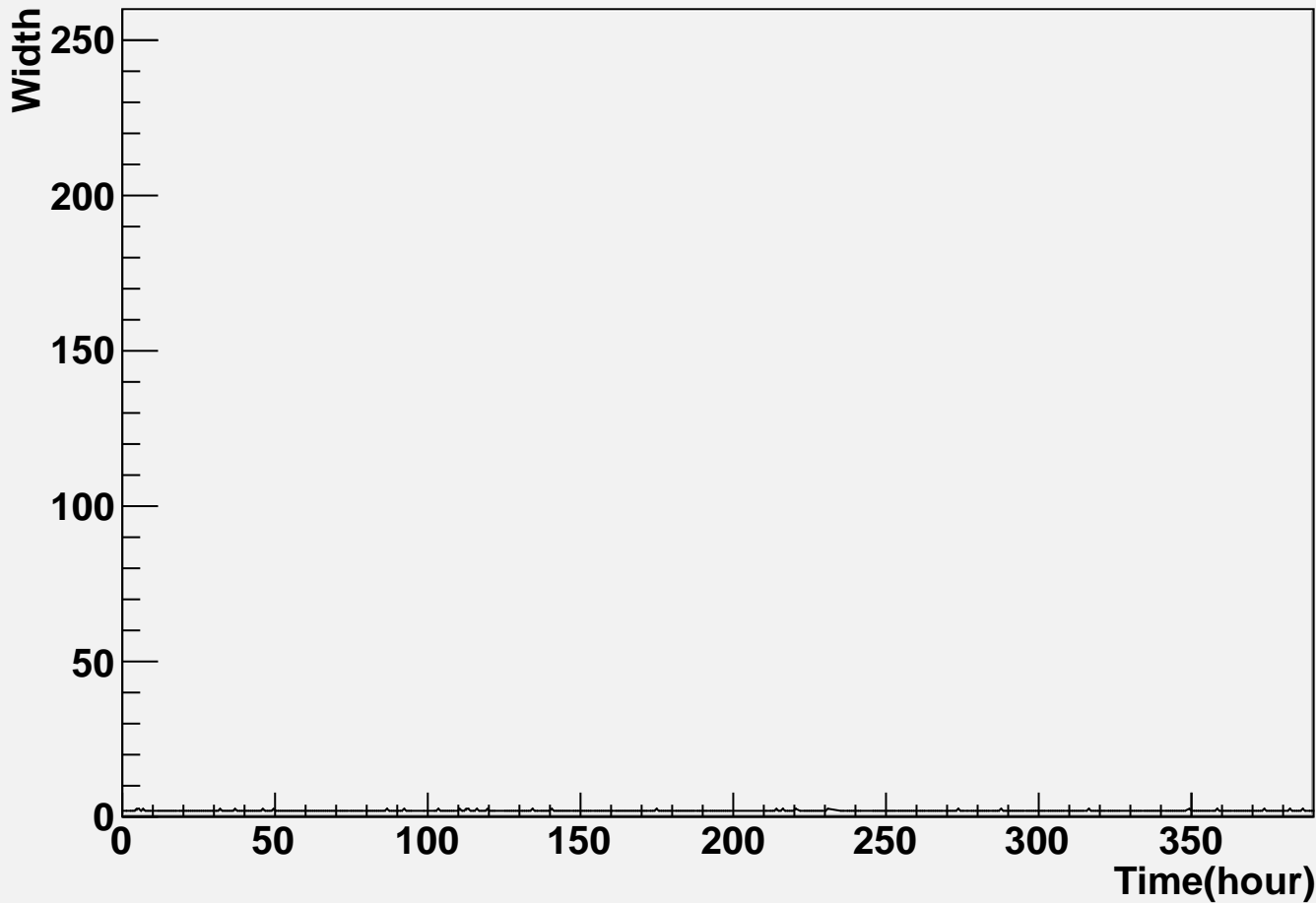
width-S0-SFEC10-Ch6



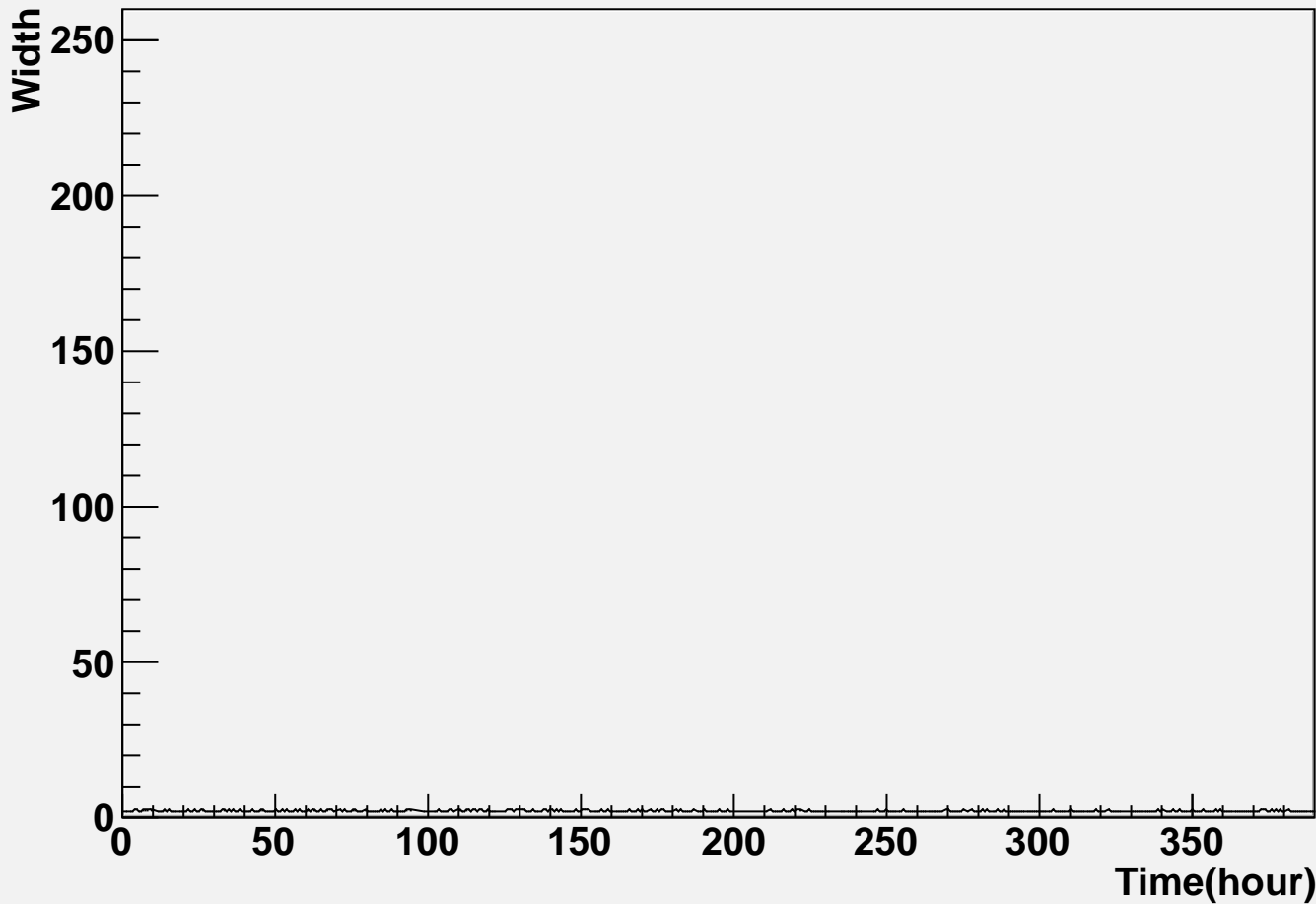
width-S0-SFEC10-Ch7



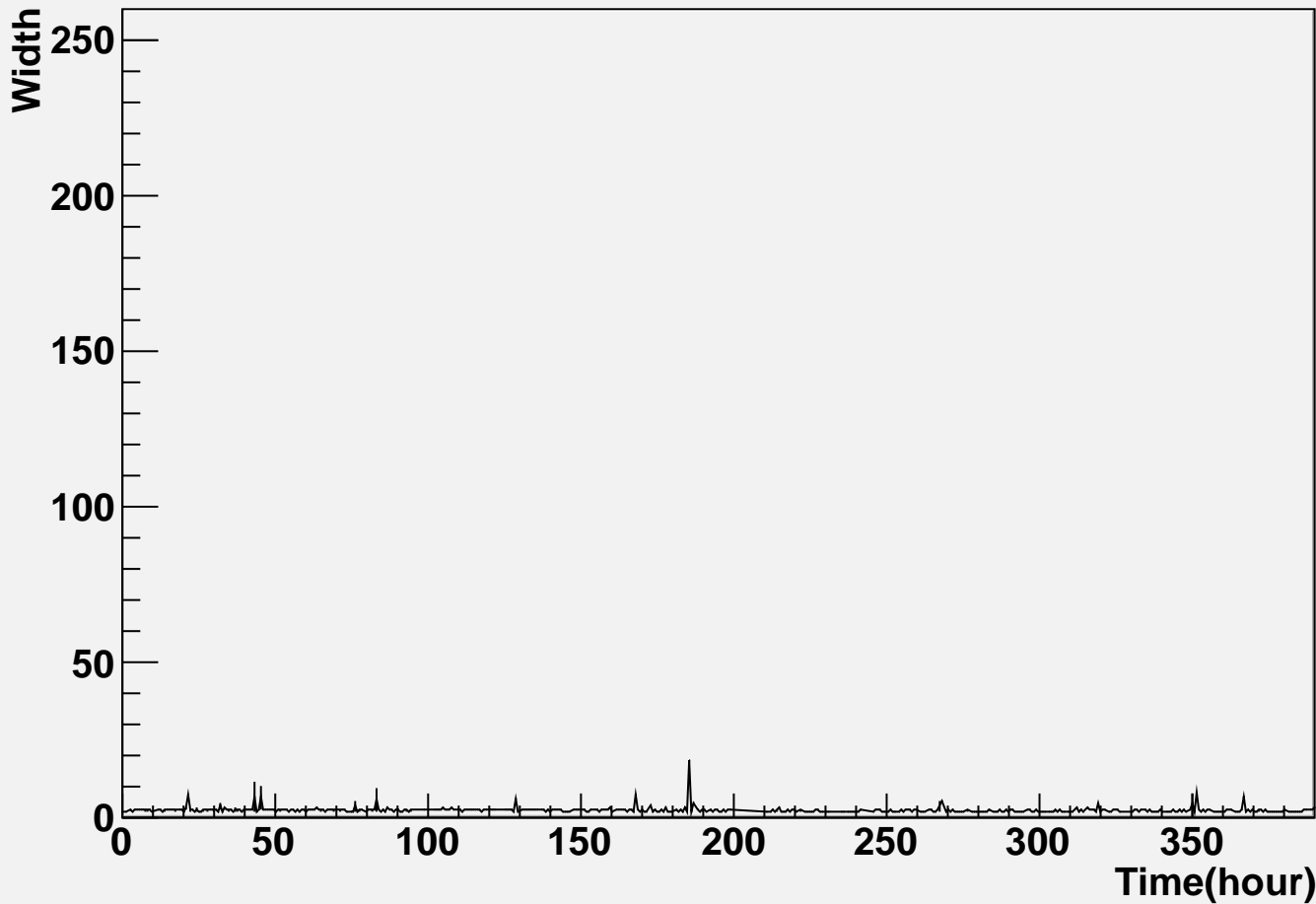
width-S0-SFEC10-Ch8



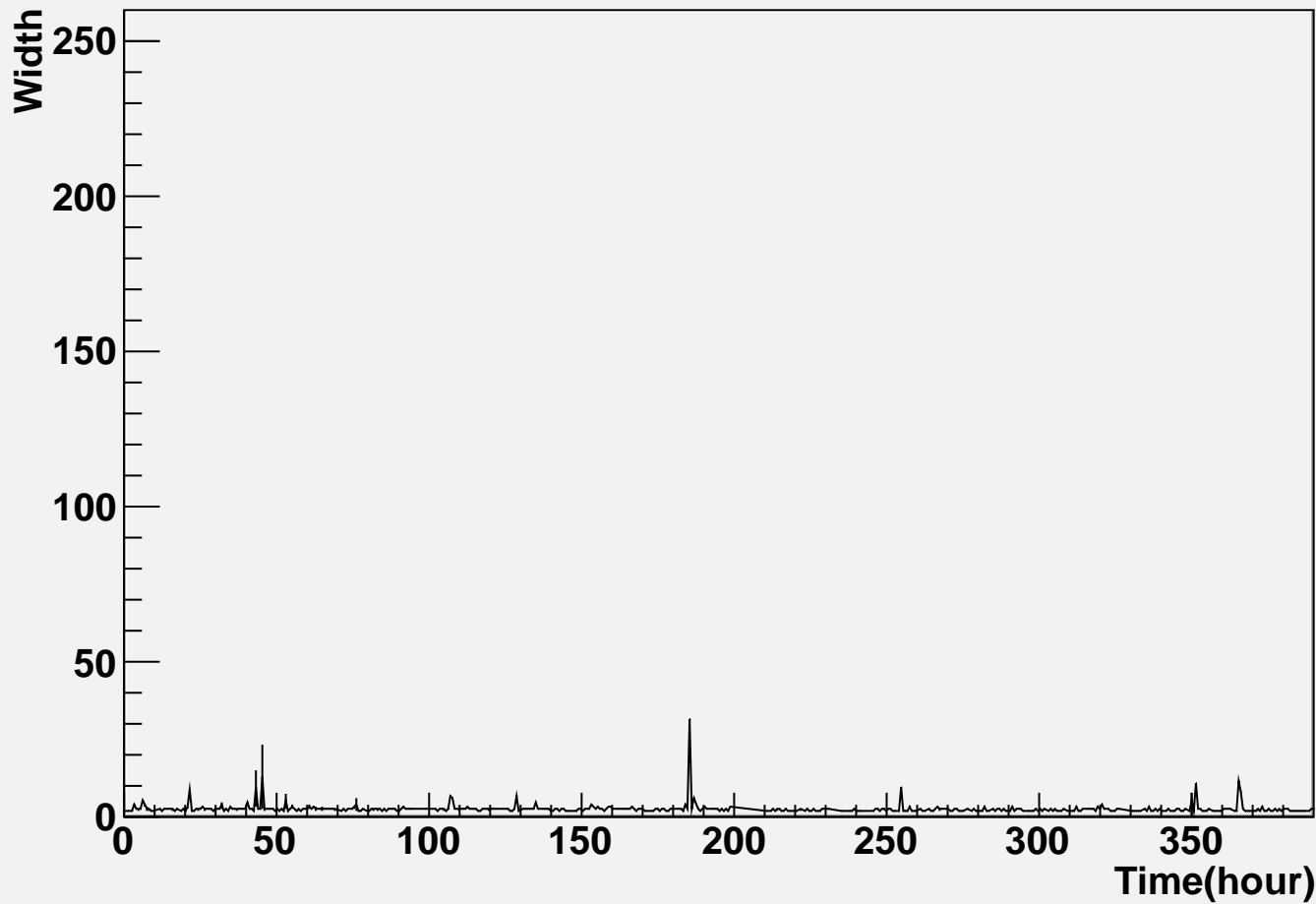
width-S0-SFEC10-Ch9



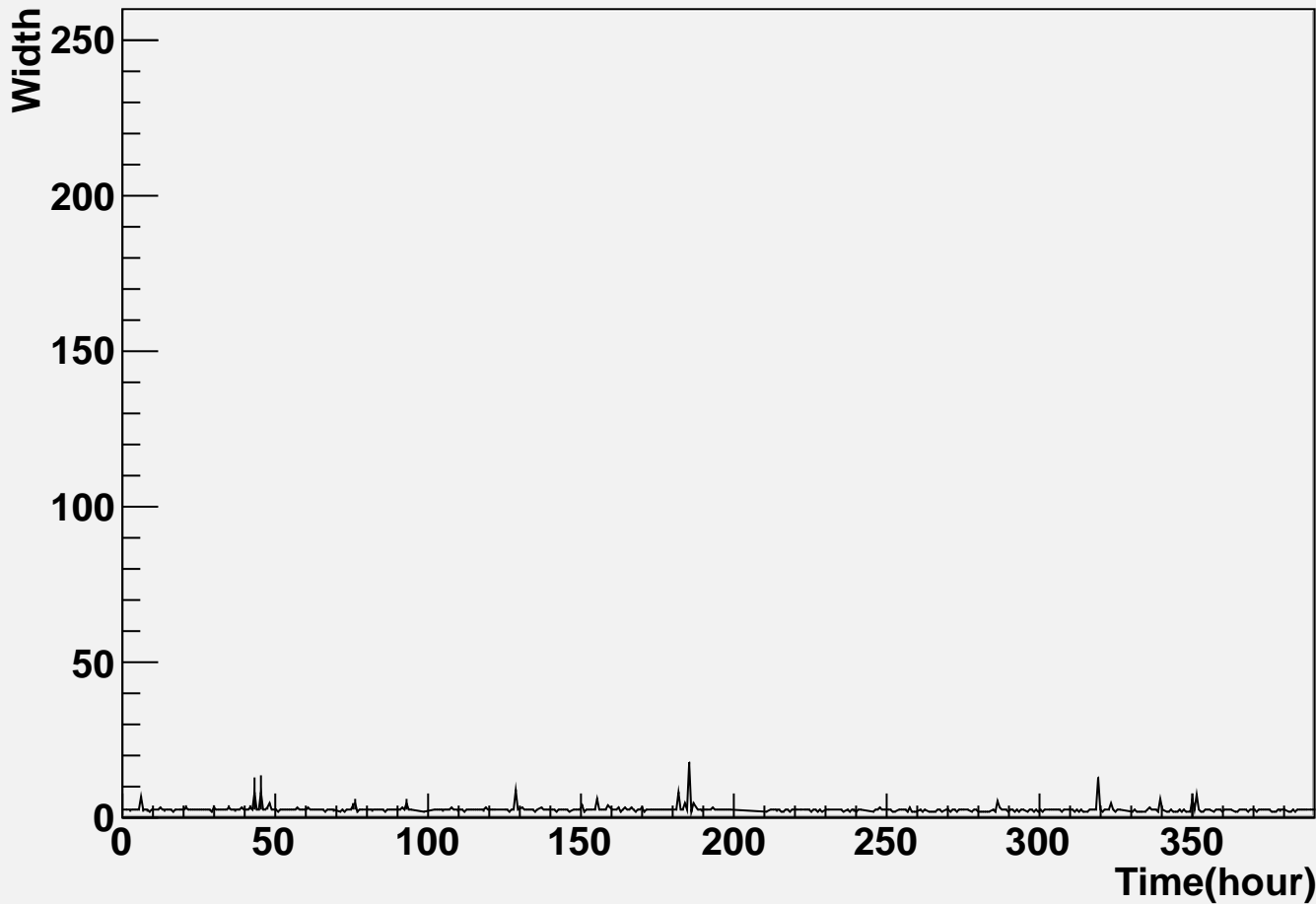
width-S0-SFEC11-Ch0



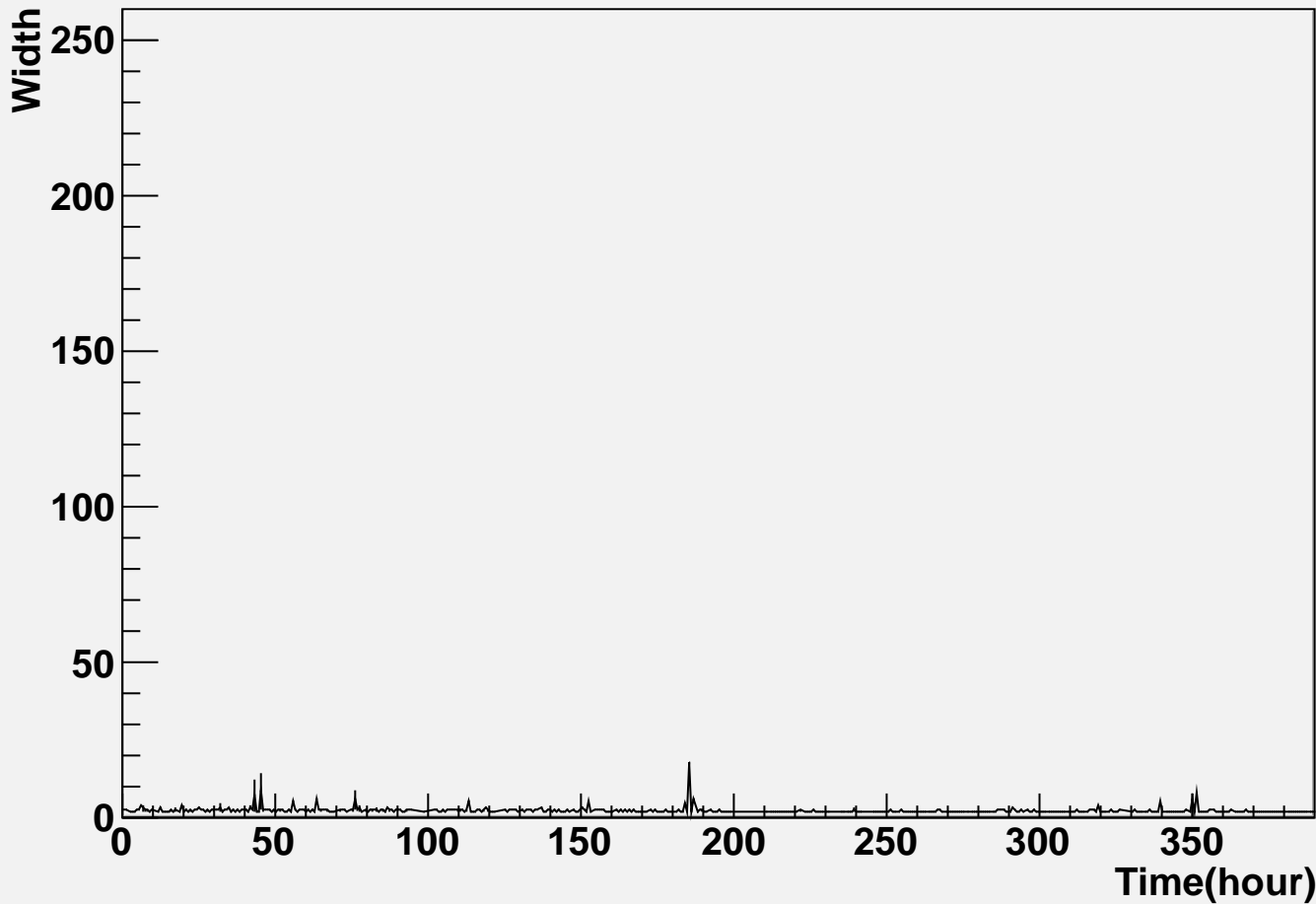
width-S0-SFEC11-Ch1



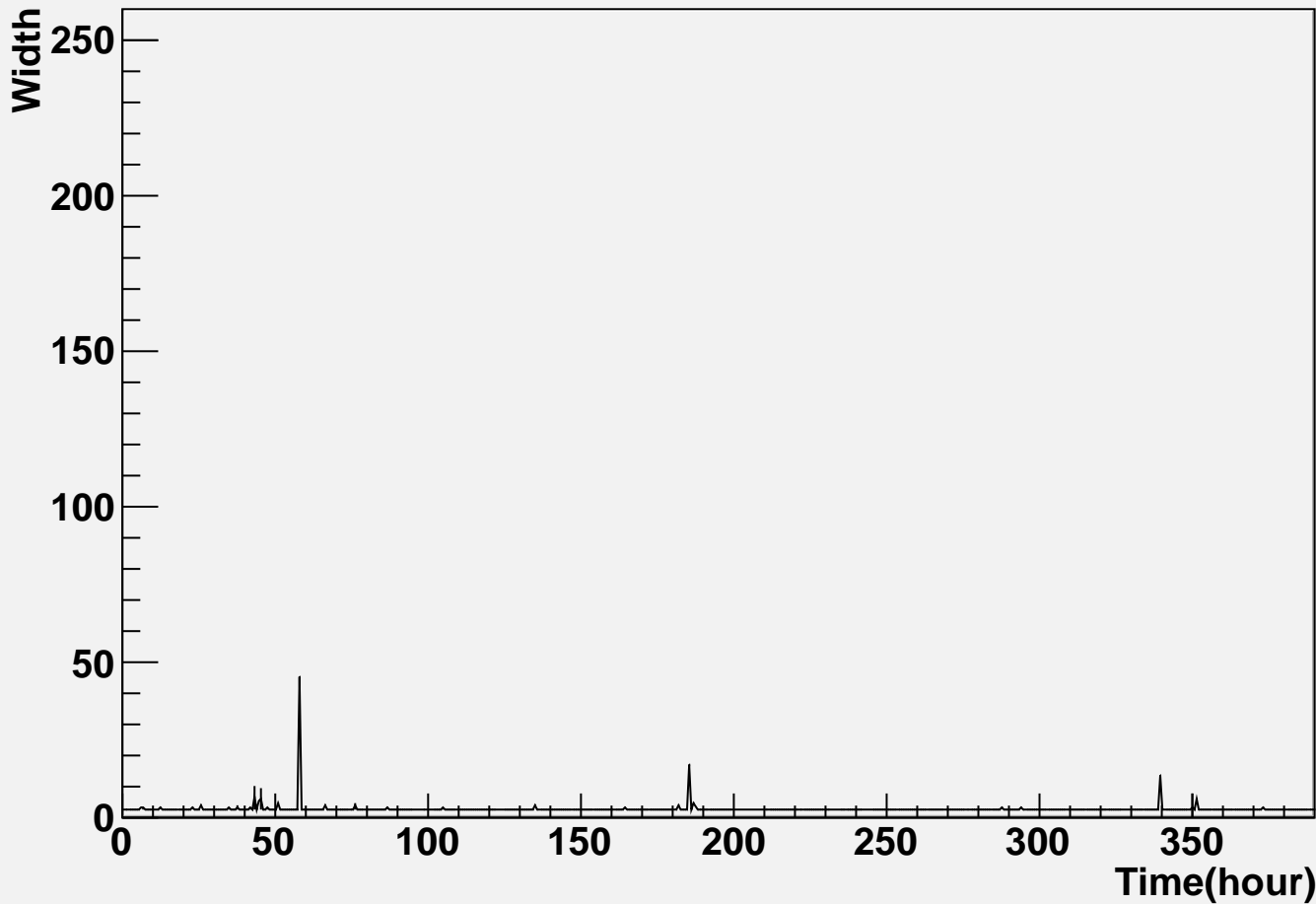
width-S0-SFEC11-Ch2



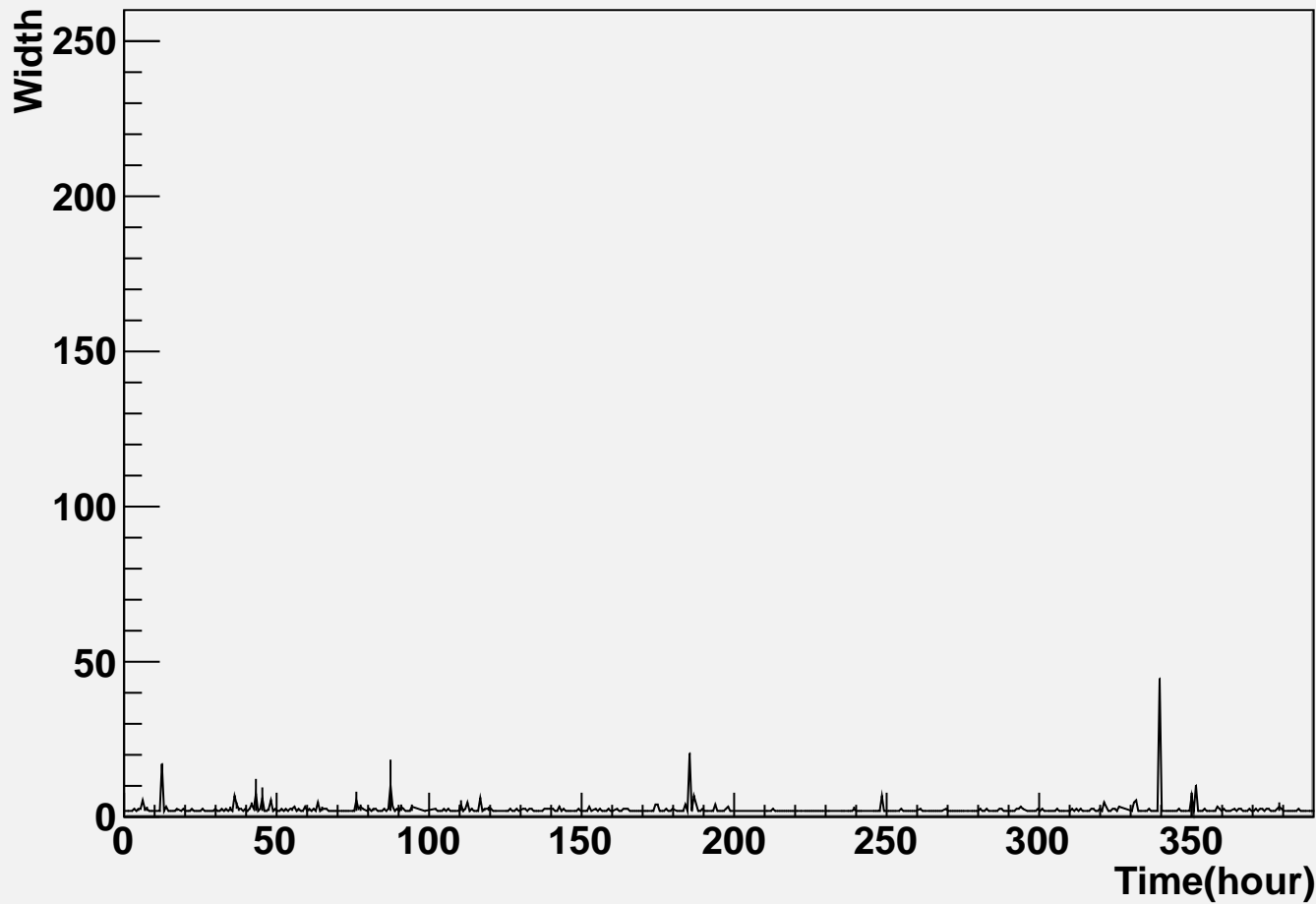
width-S0-SFEC11-Ch3



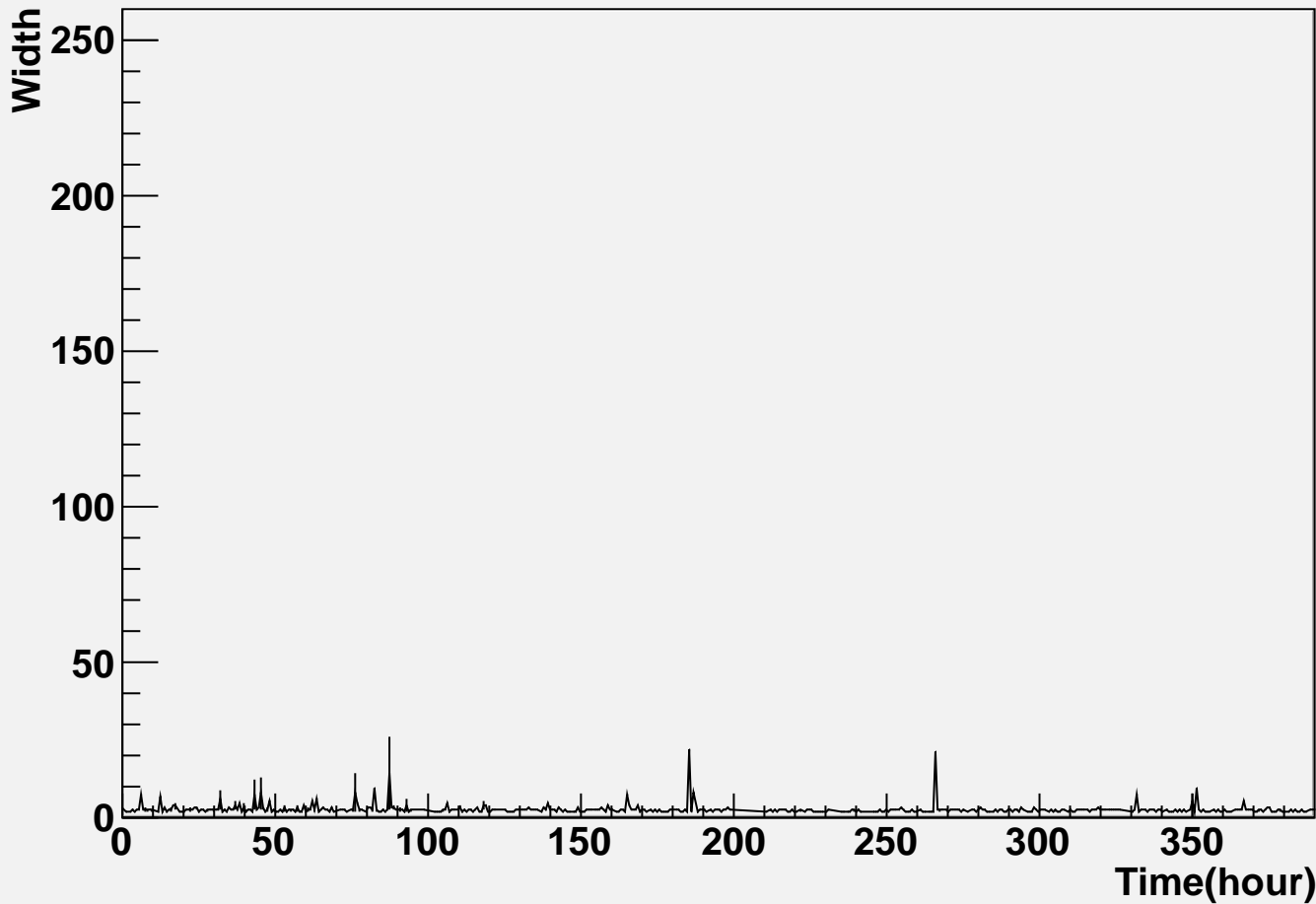
width-S0-SFEC11-Ch4



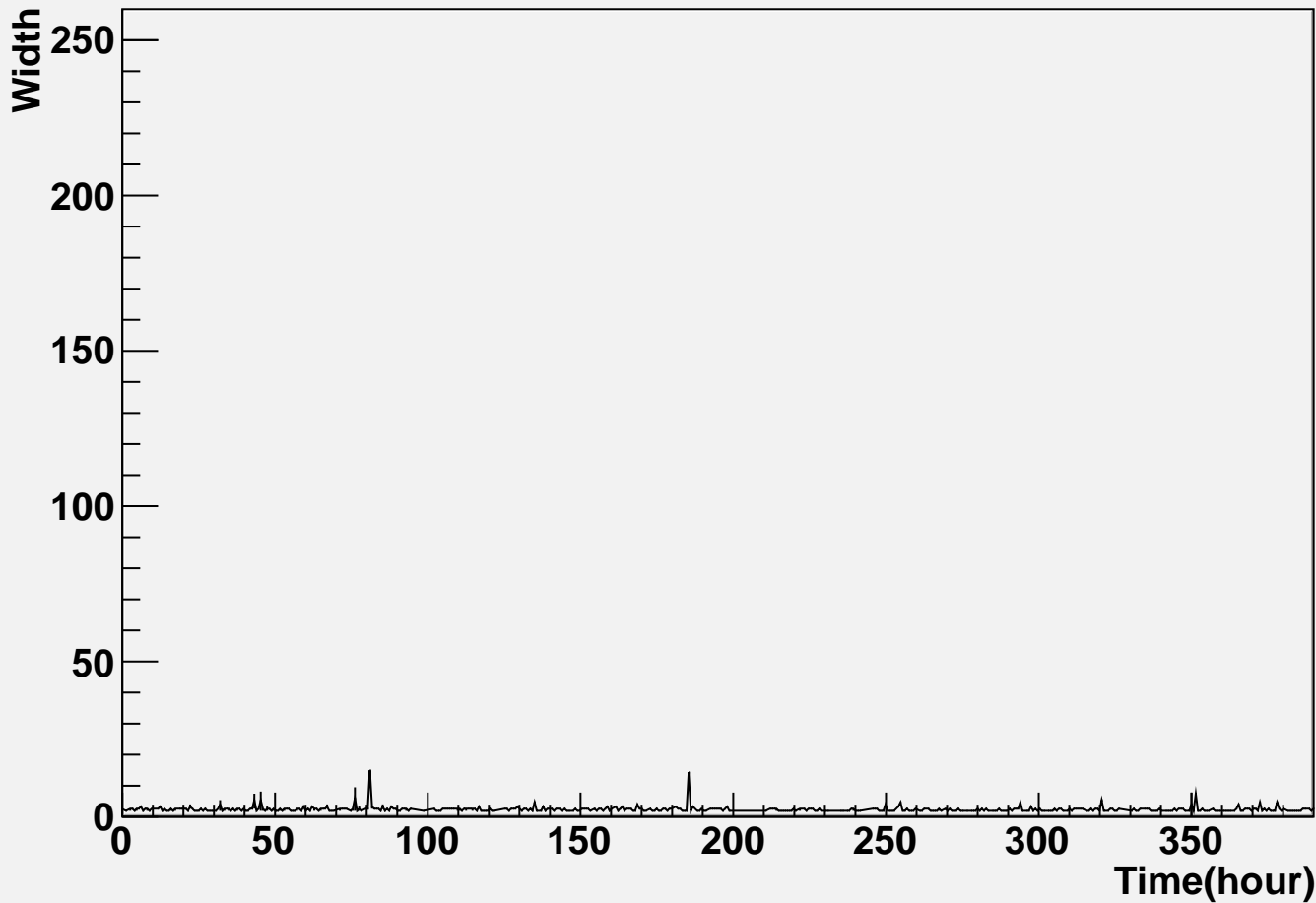
width-S0-SFEC11-Ch5



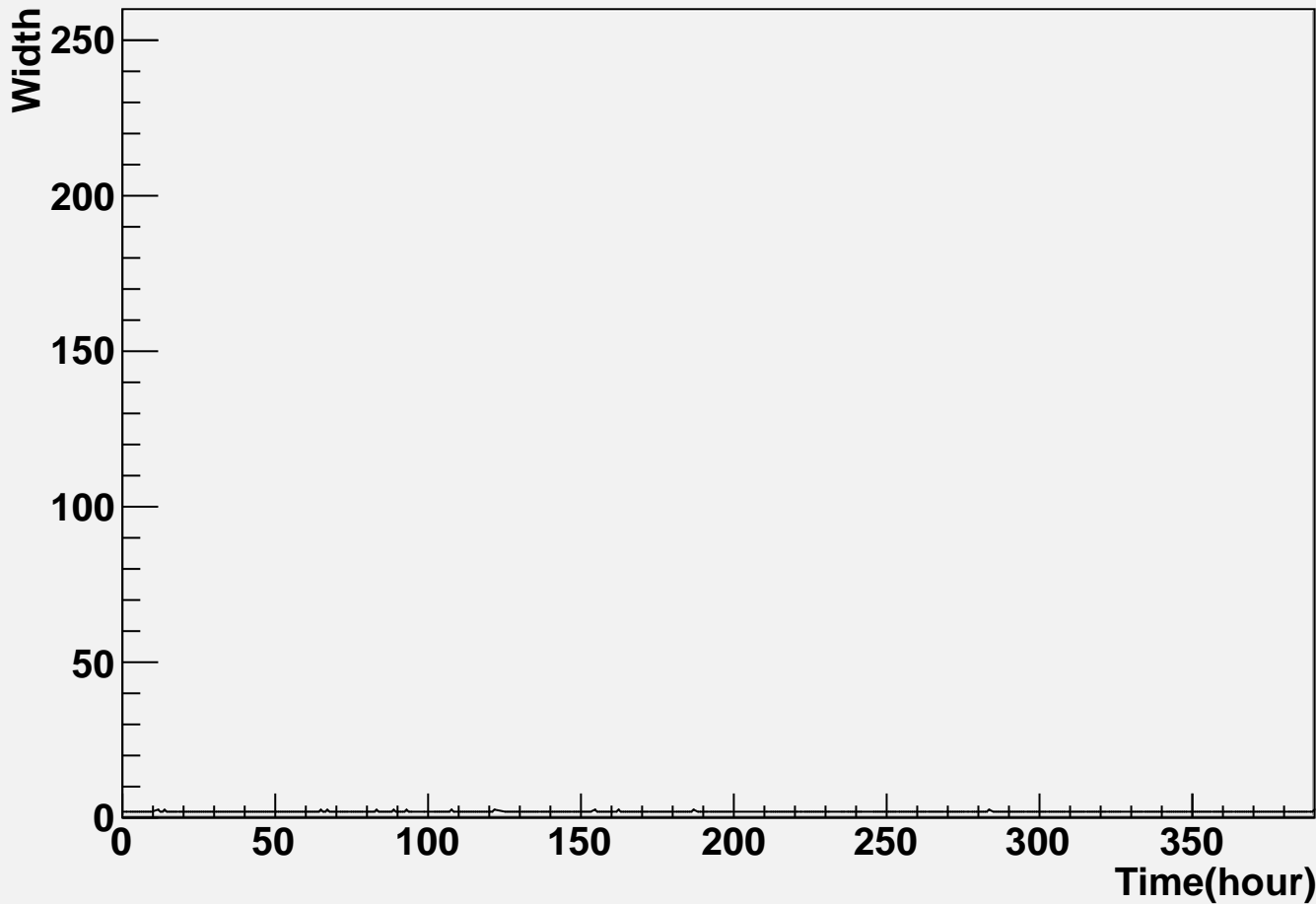
width-S0-SFEC11-Ch6



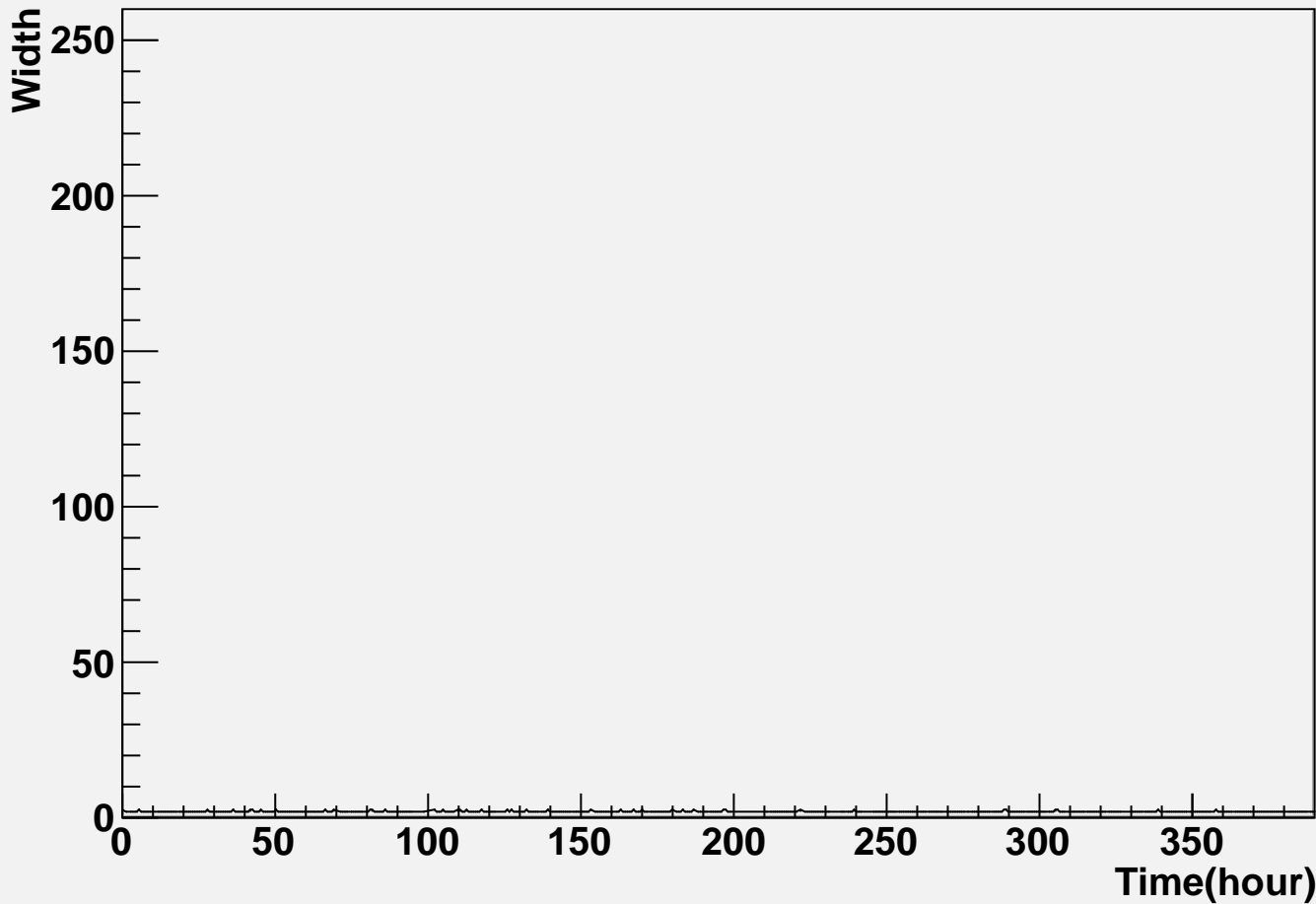
width-S0-SFEC11-Ch7



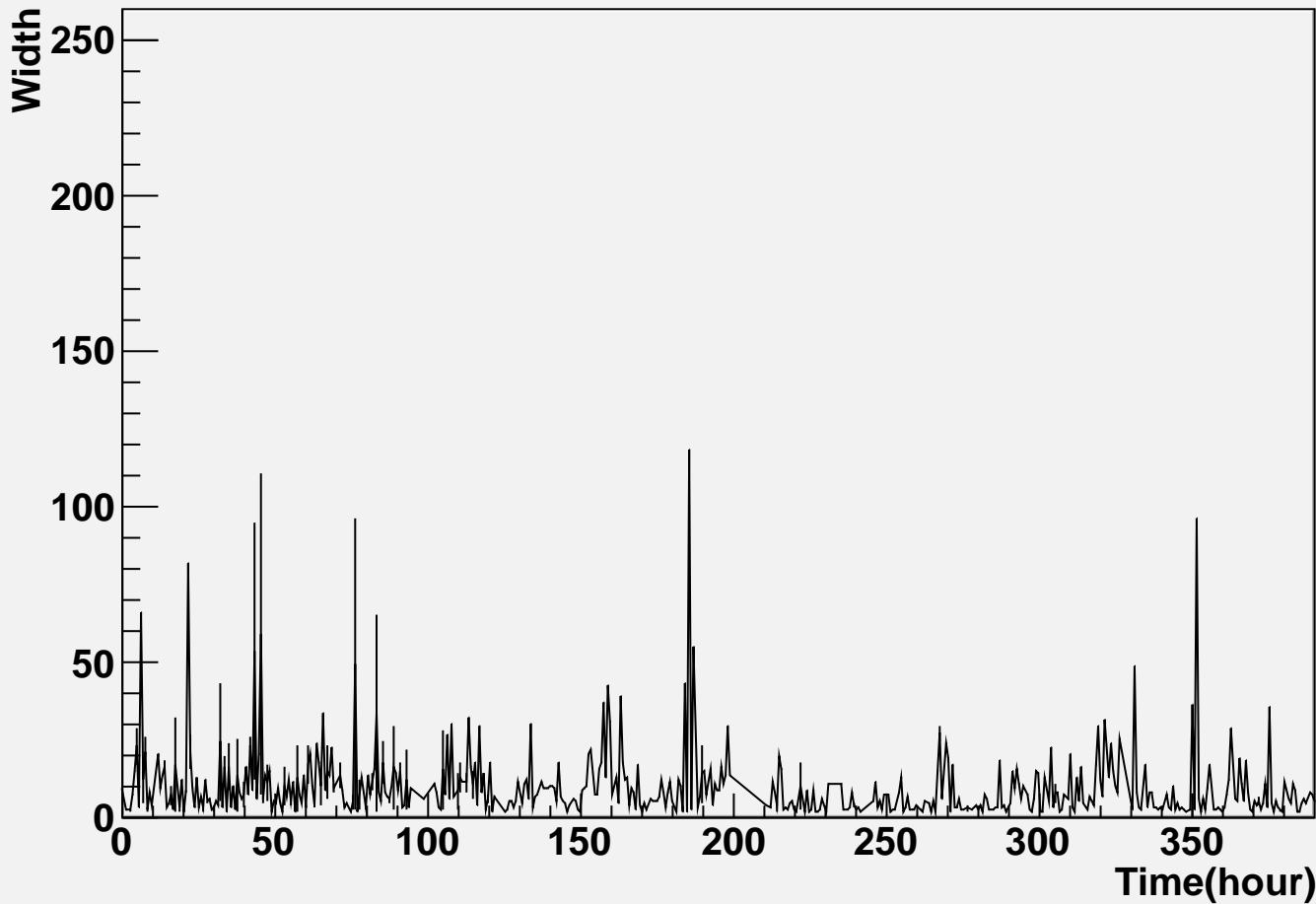
width-S0-SFEC11-Ch8



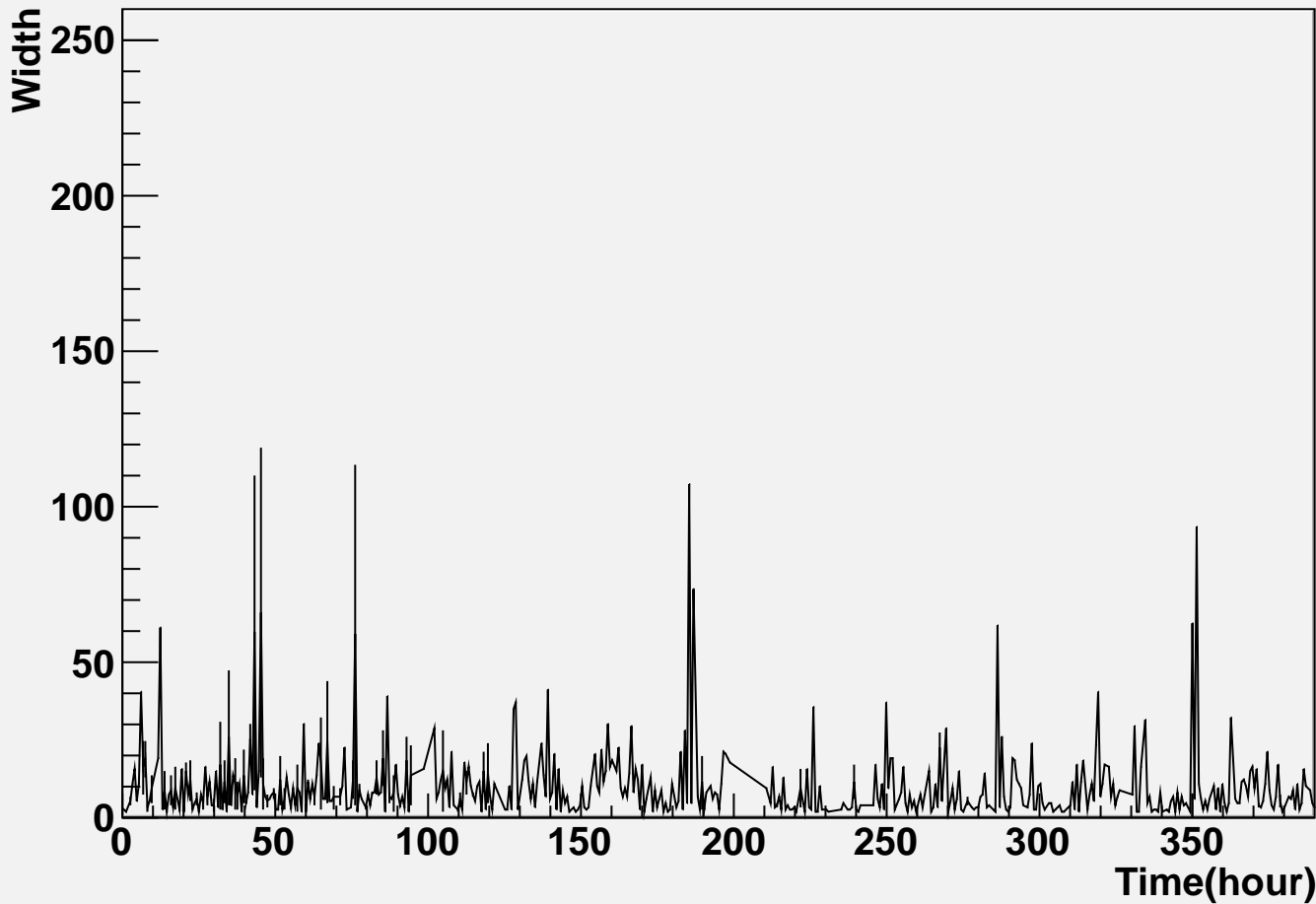
width-S0-SFEC11-Ch9



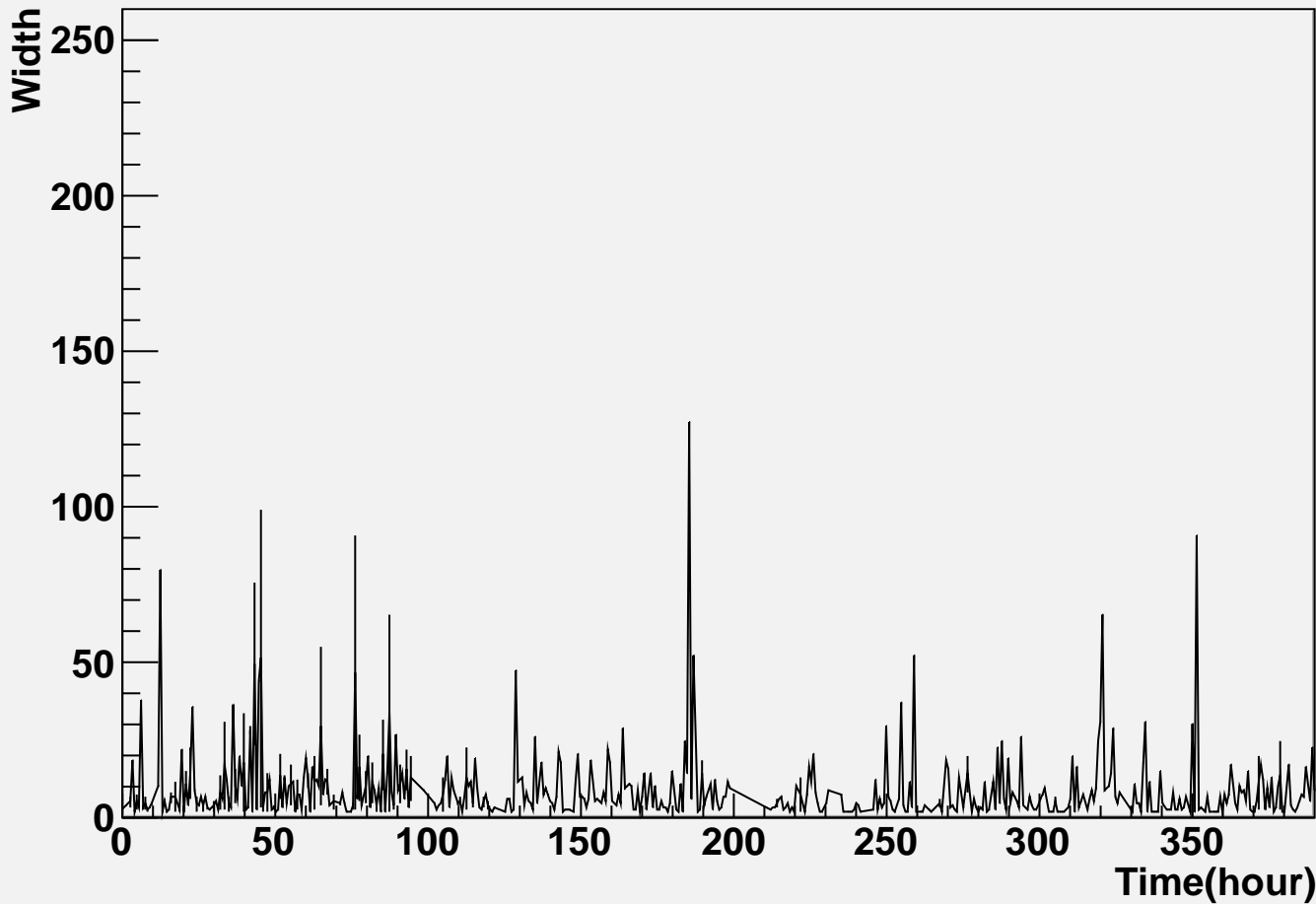
width-S1-SFET2A-Ch0



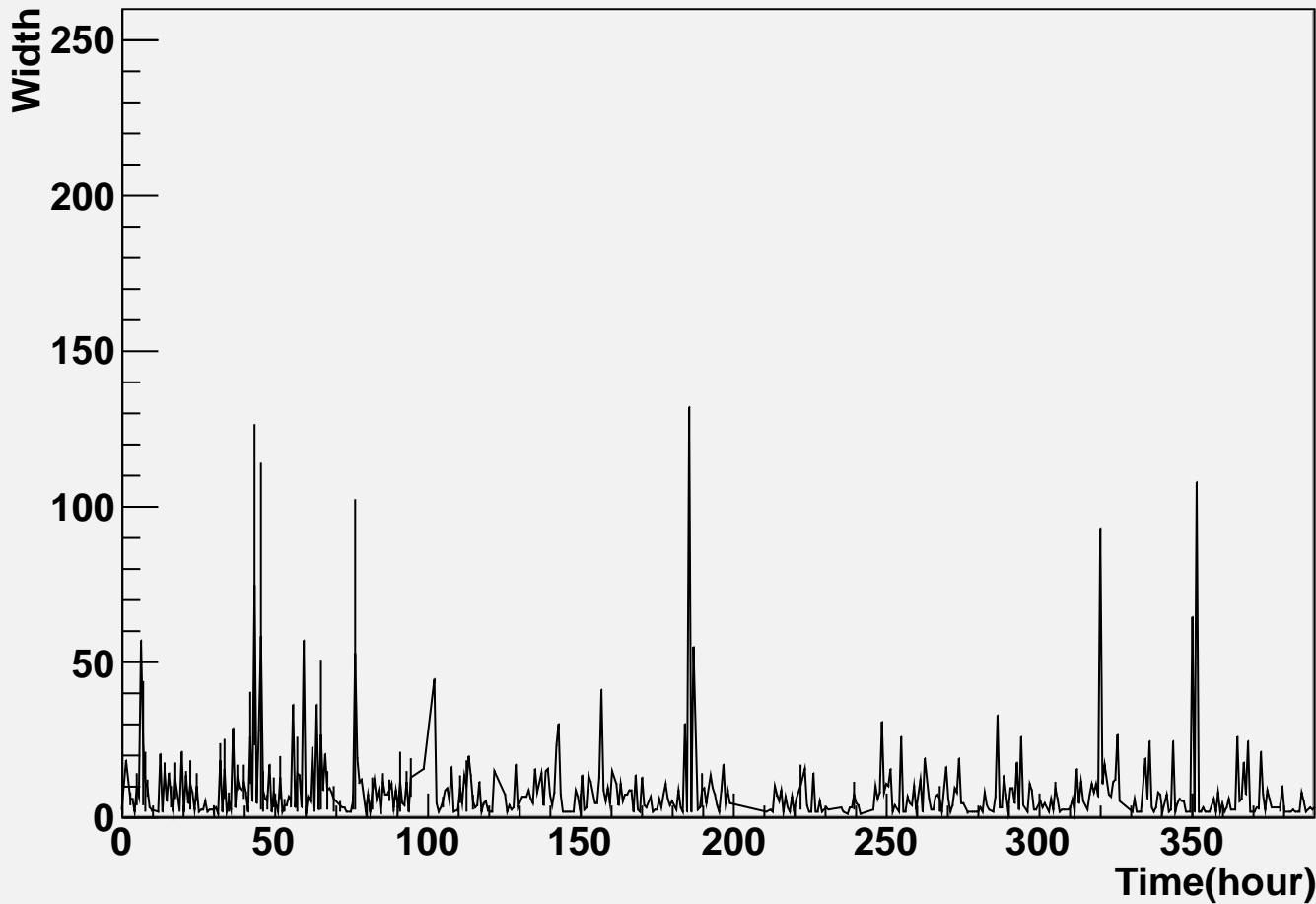
width-S1-SFET2A-Ch1



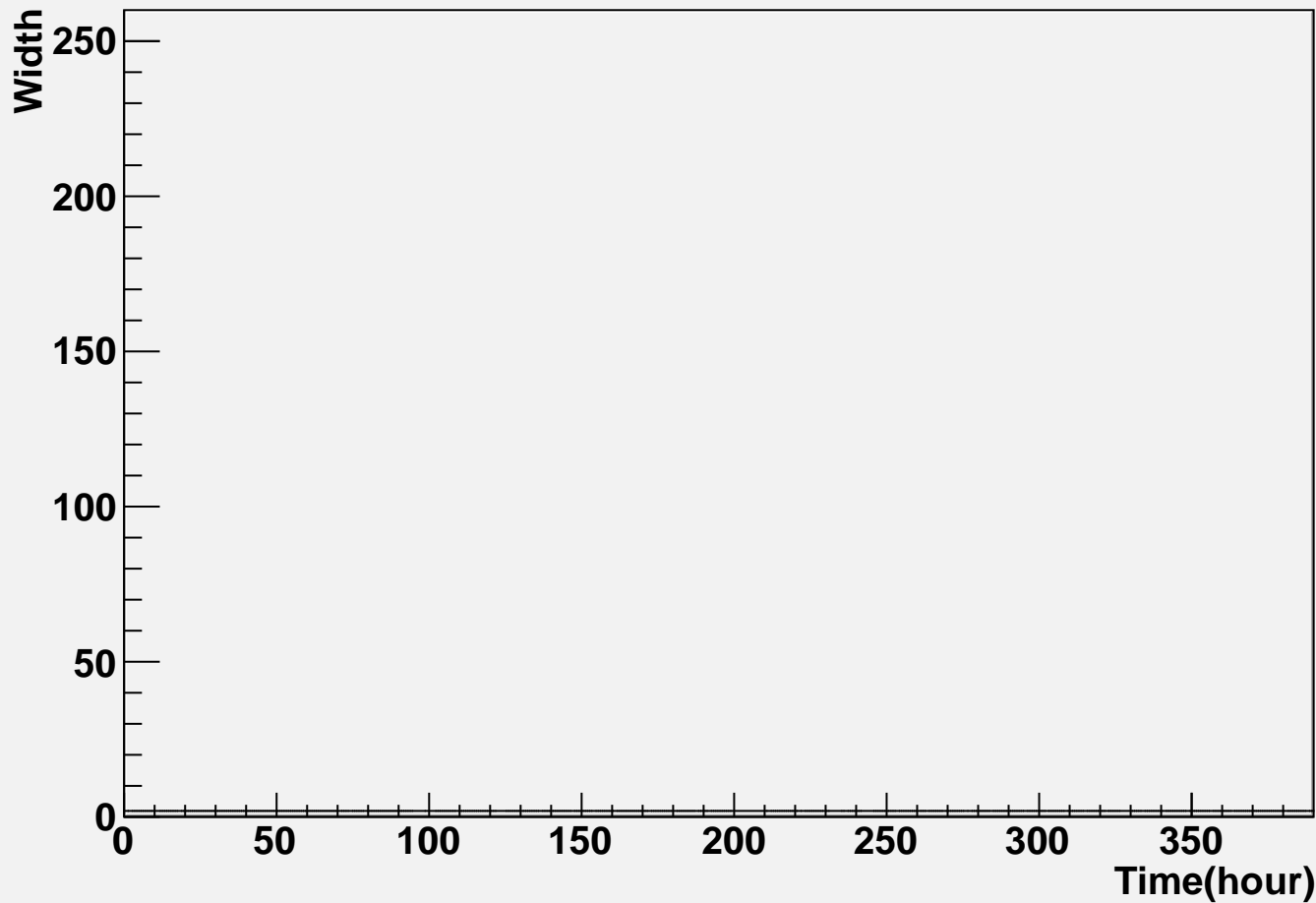
width-S1-SFET2A-Ch2



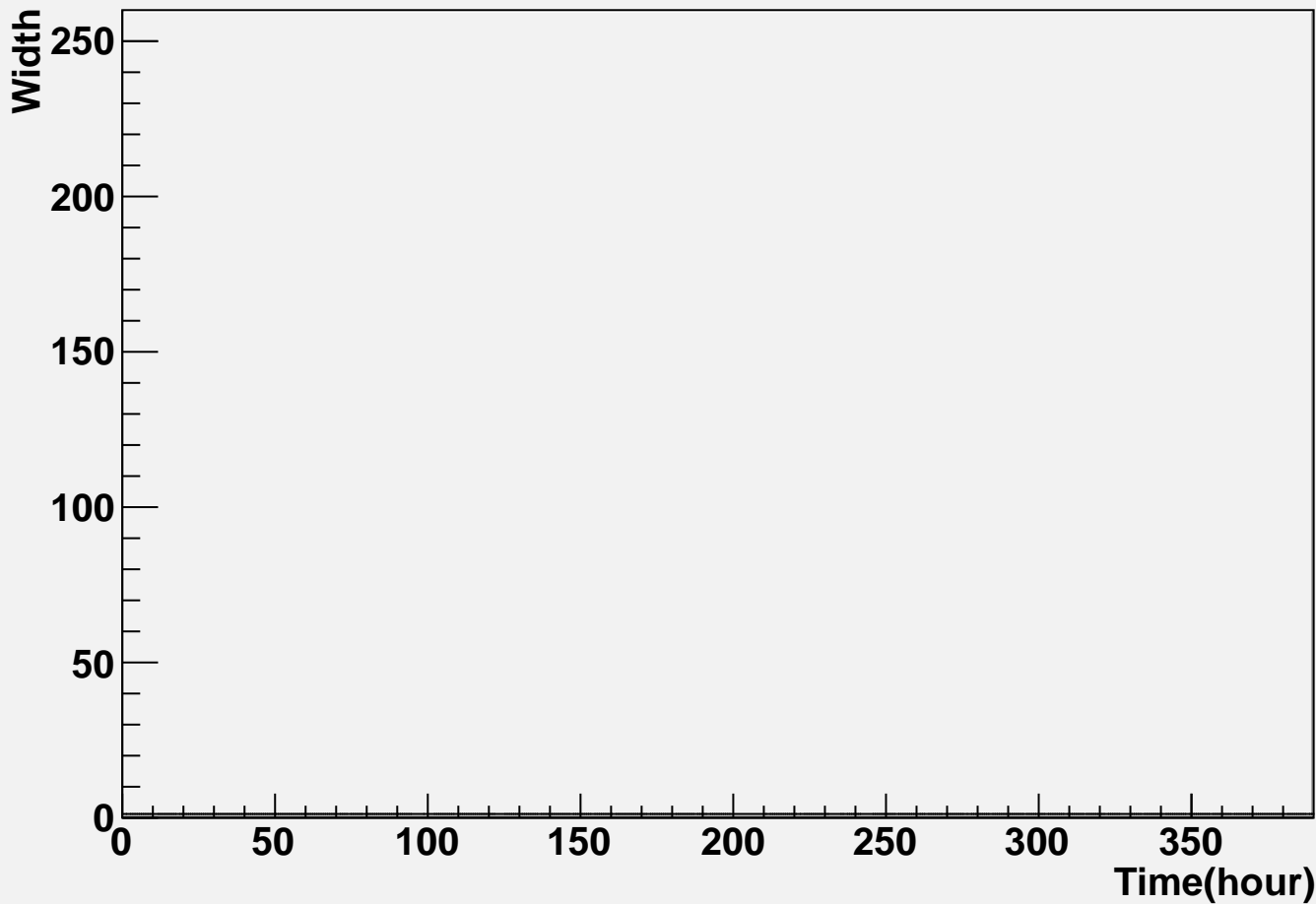
width-S1-SFET2A-Ch3



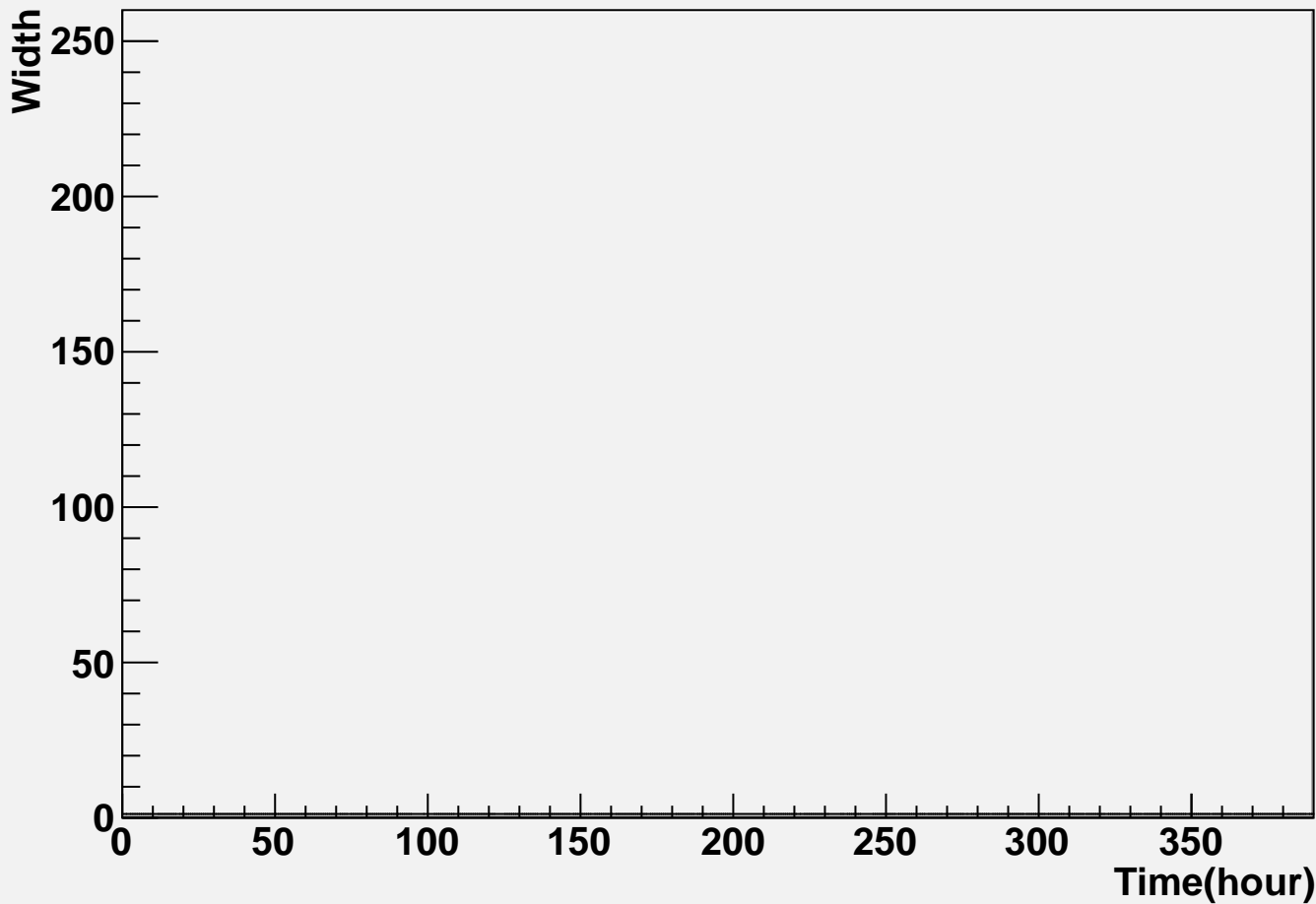
width-S1-SFET2A-Ch4



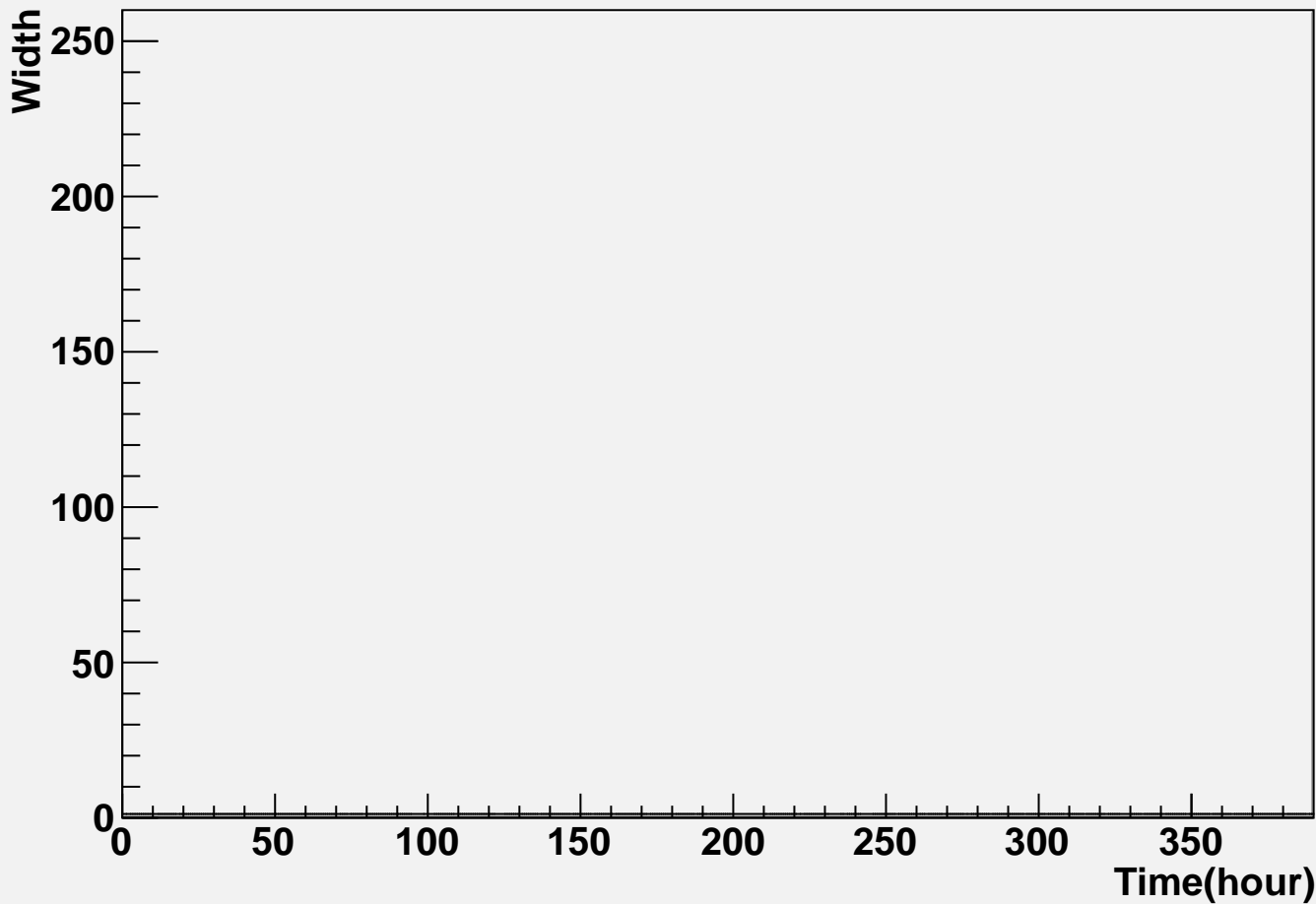
width-S1-SFET2A-Ch5



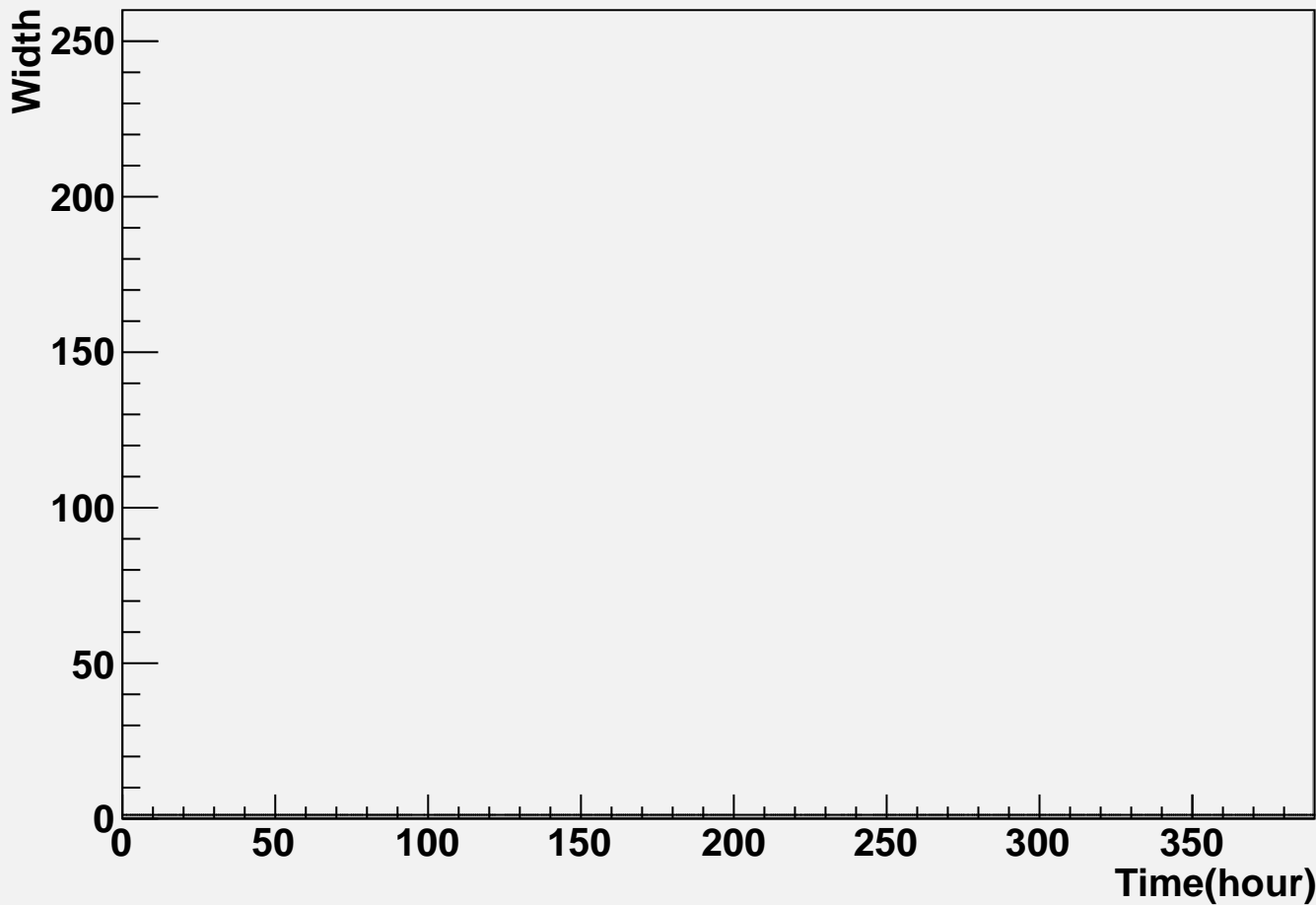
width-S1-SFET2A-Ch6



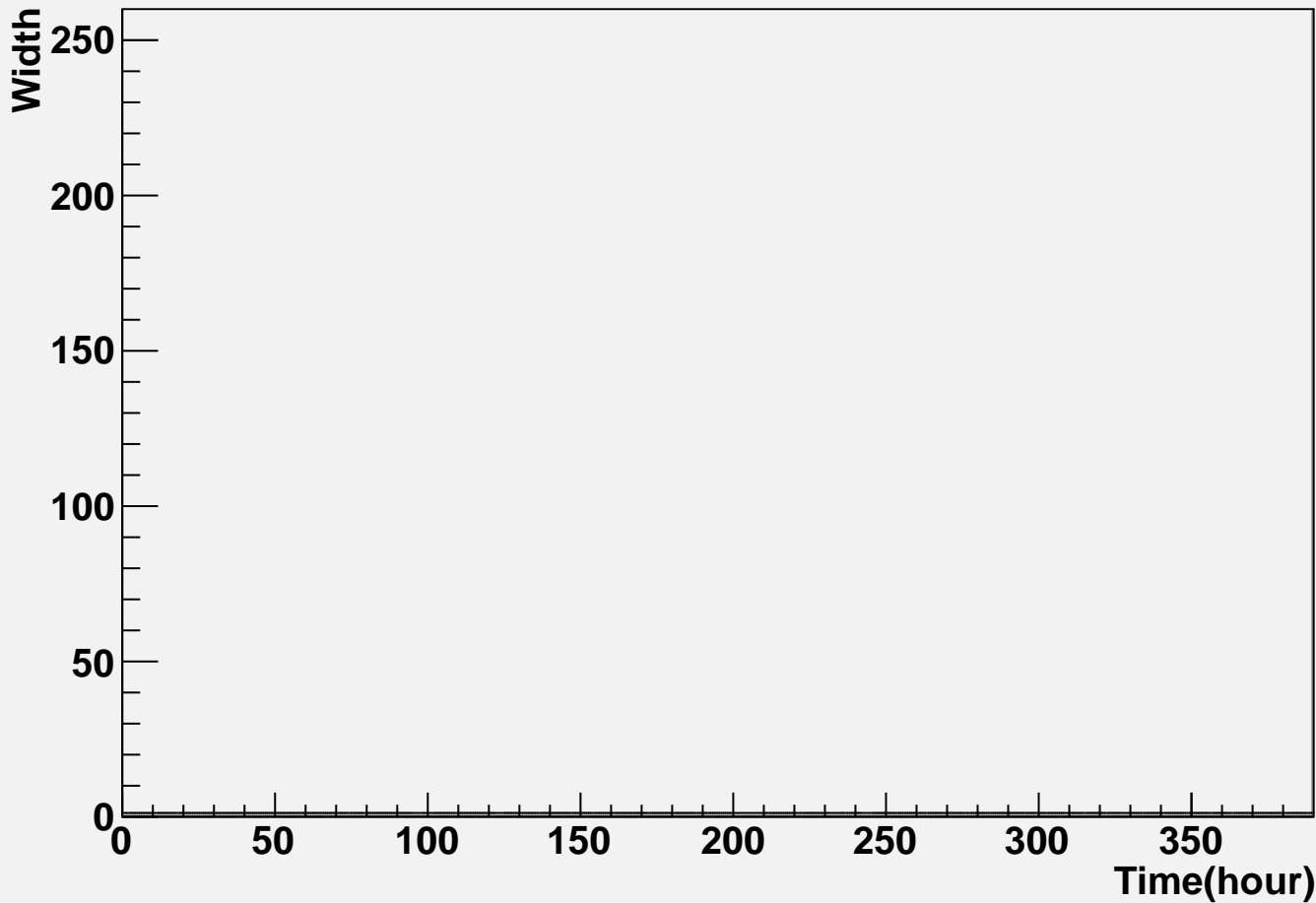
width-S1-SFET2A-Ch7



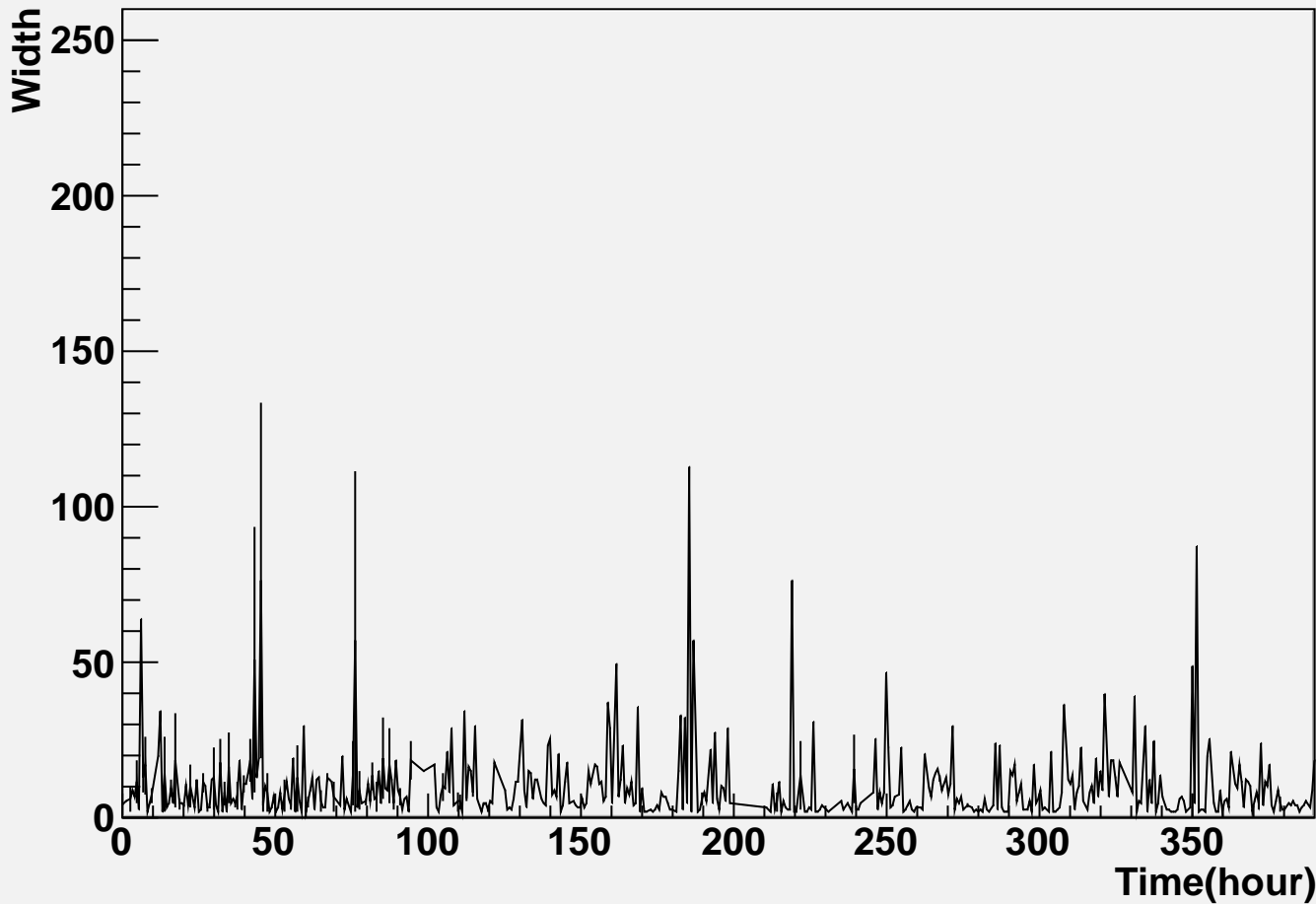
width-S1-SFET2A-Ch8



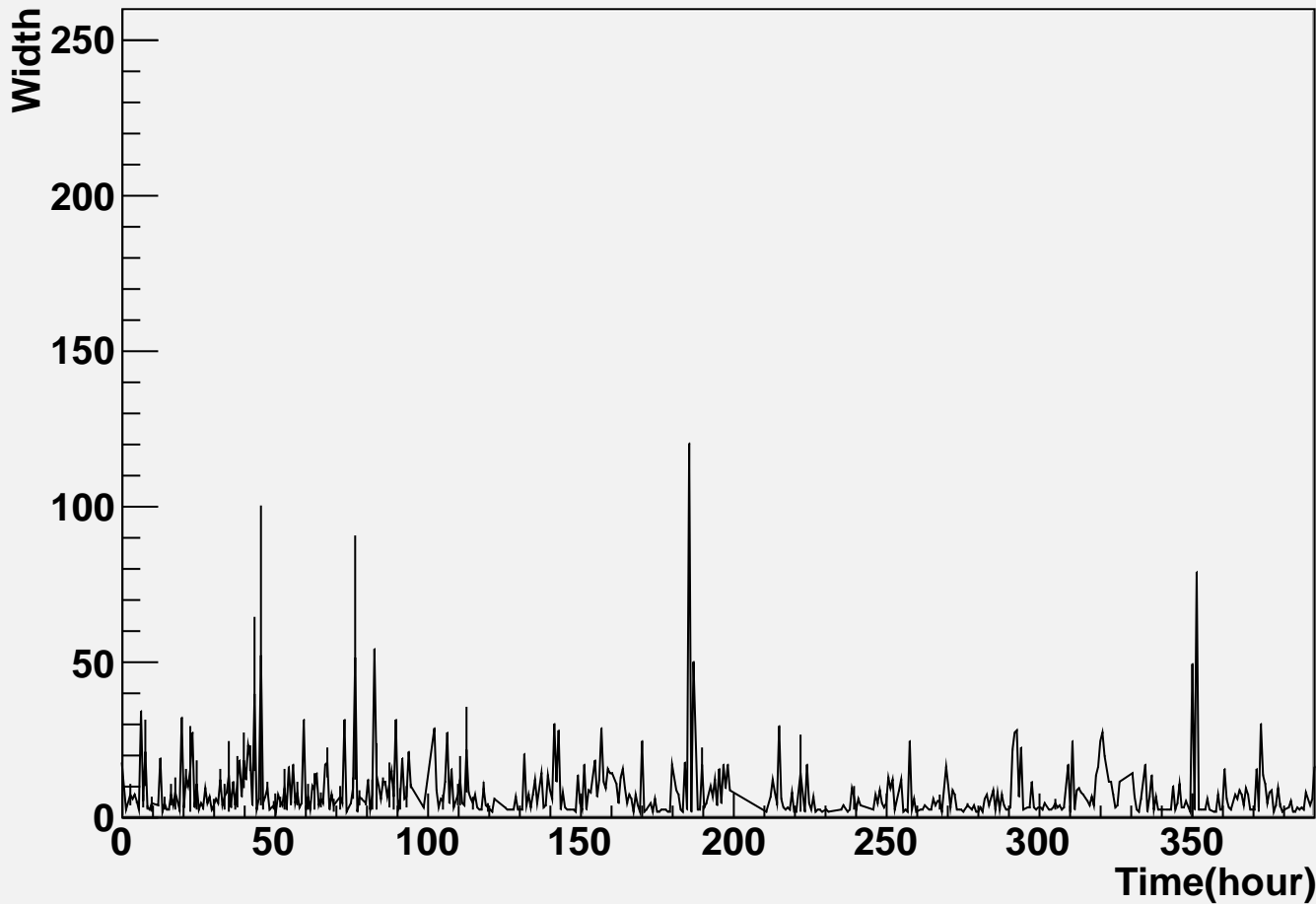
width-S1-SFET2A-Ch9



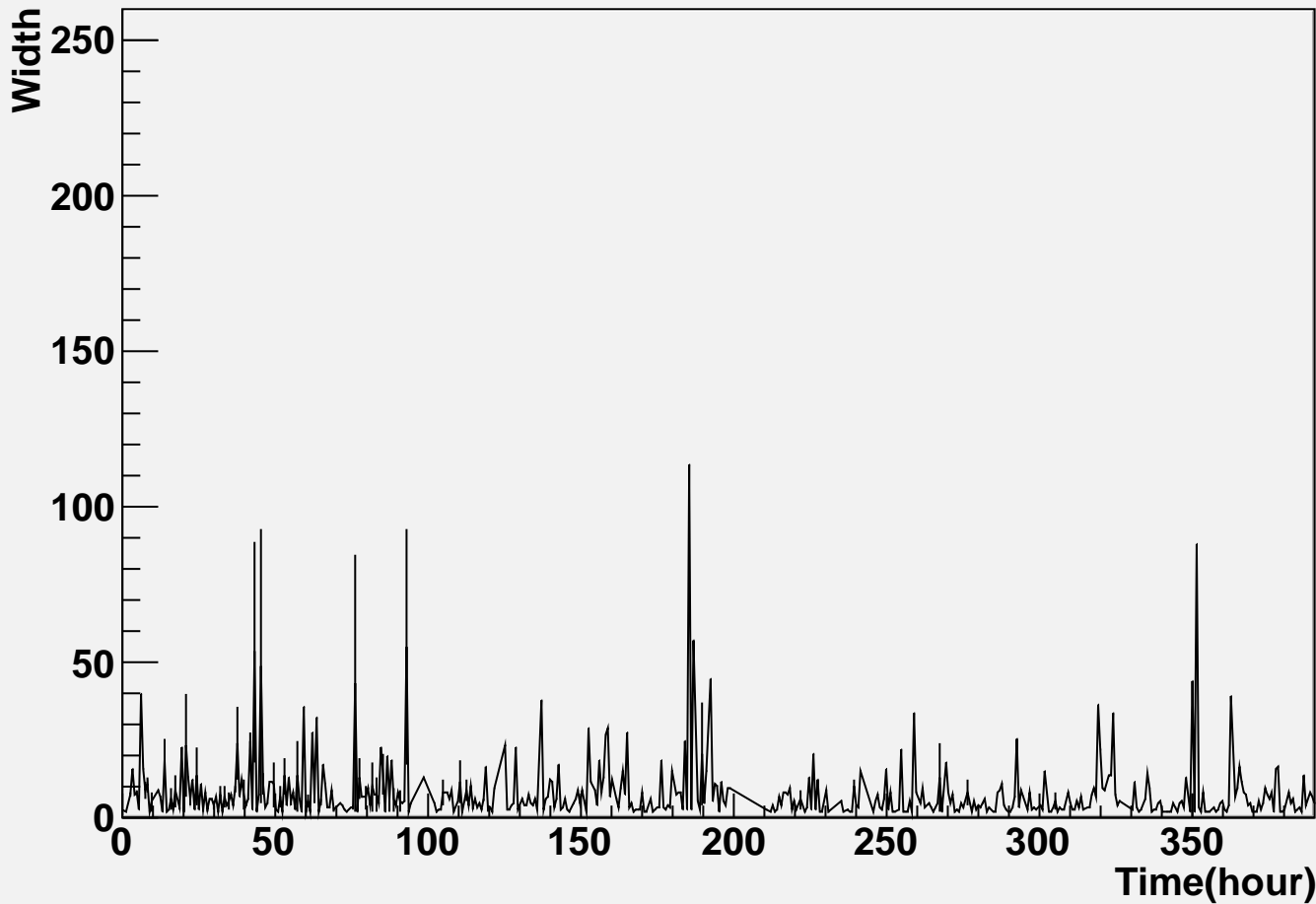
width-S1-SFET2B-Ch0



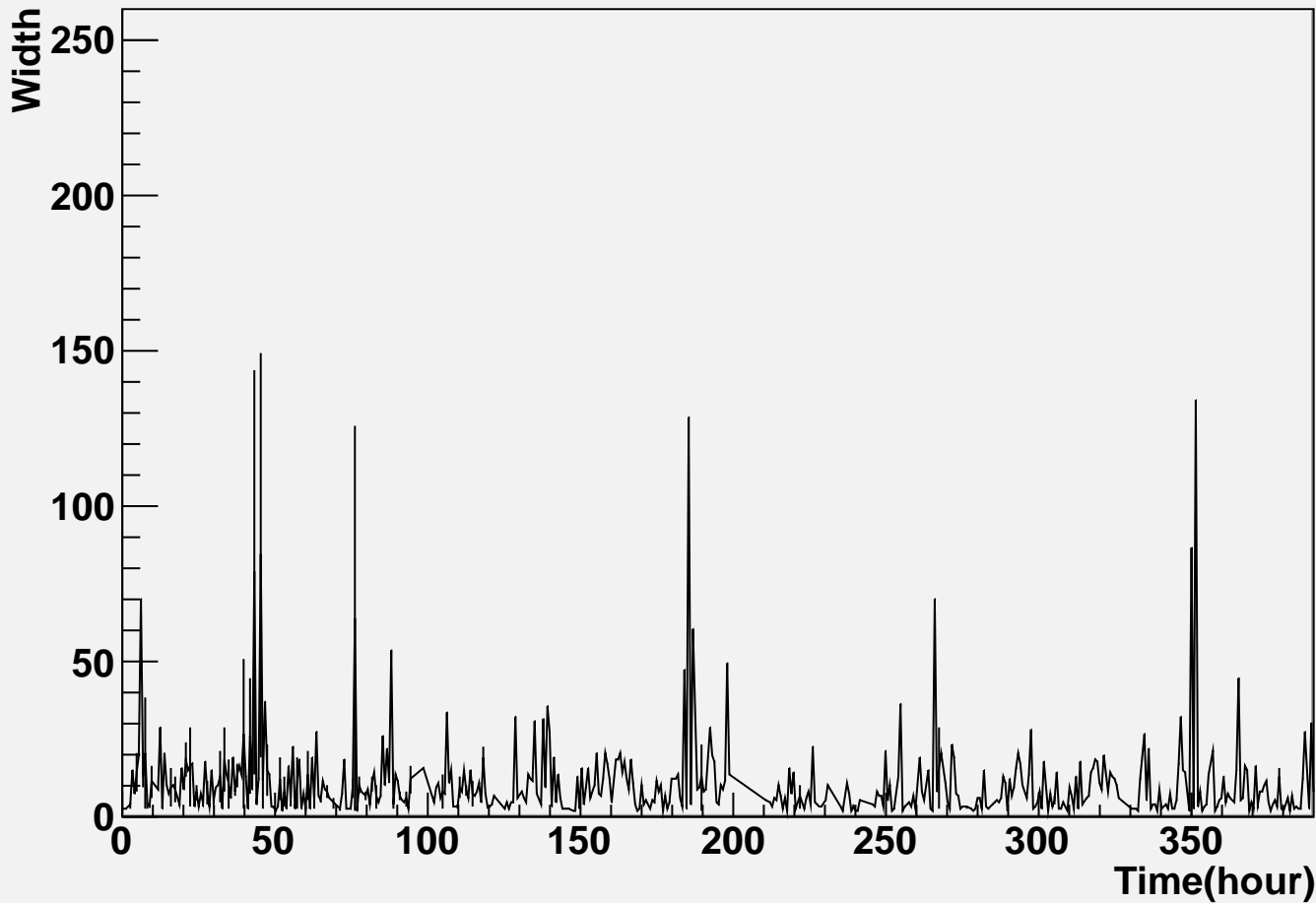
width-S1-SFET2B-Ch1



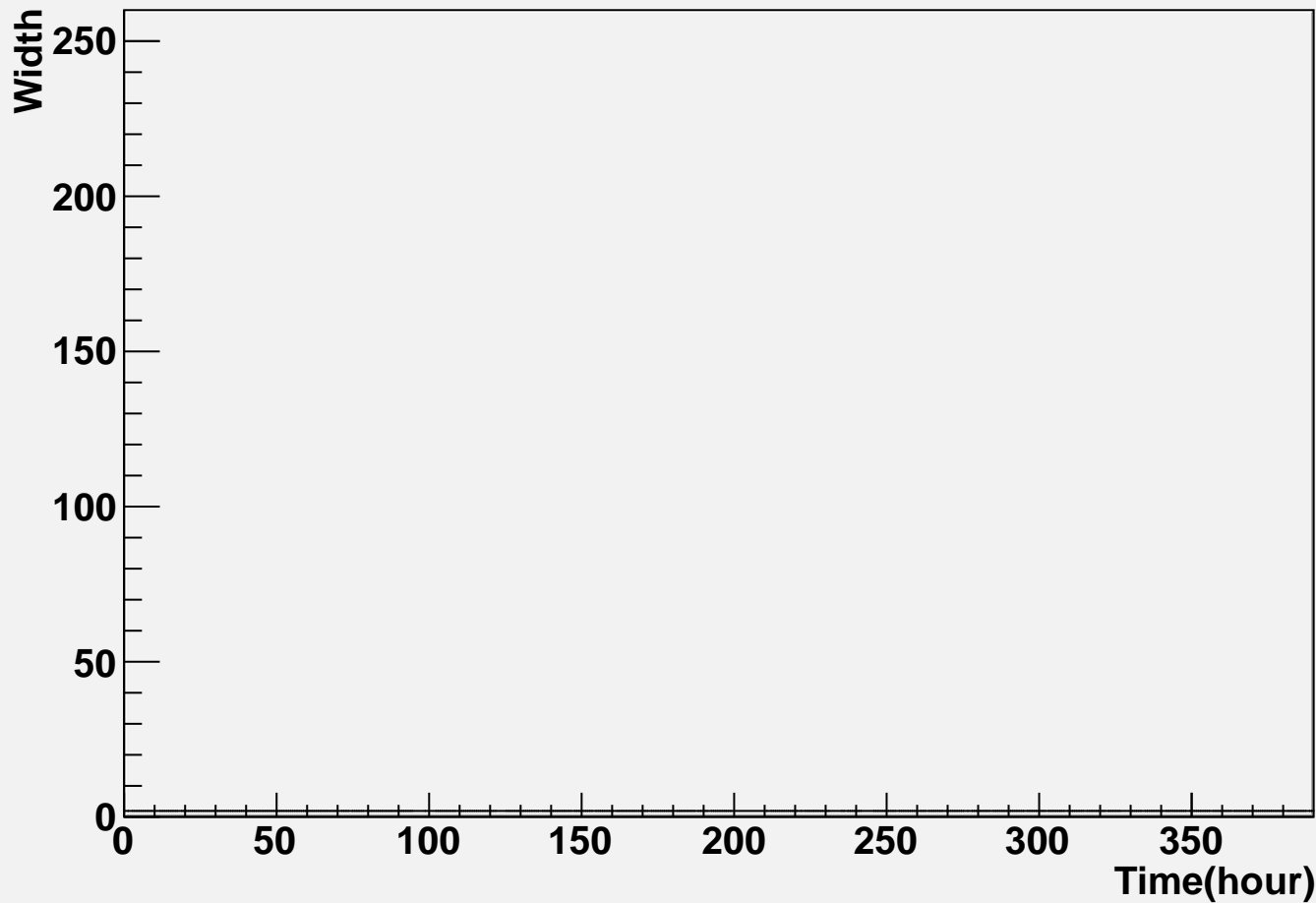
width-S1-SFET2B-Ch2



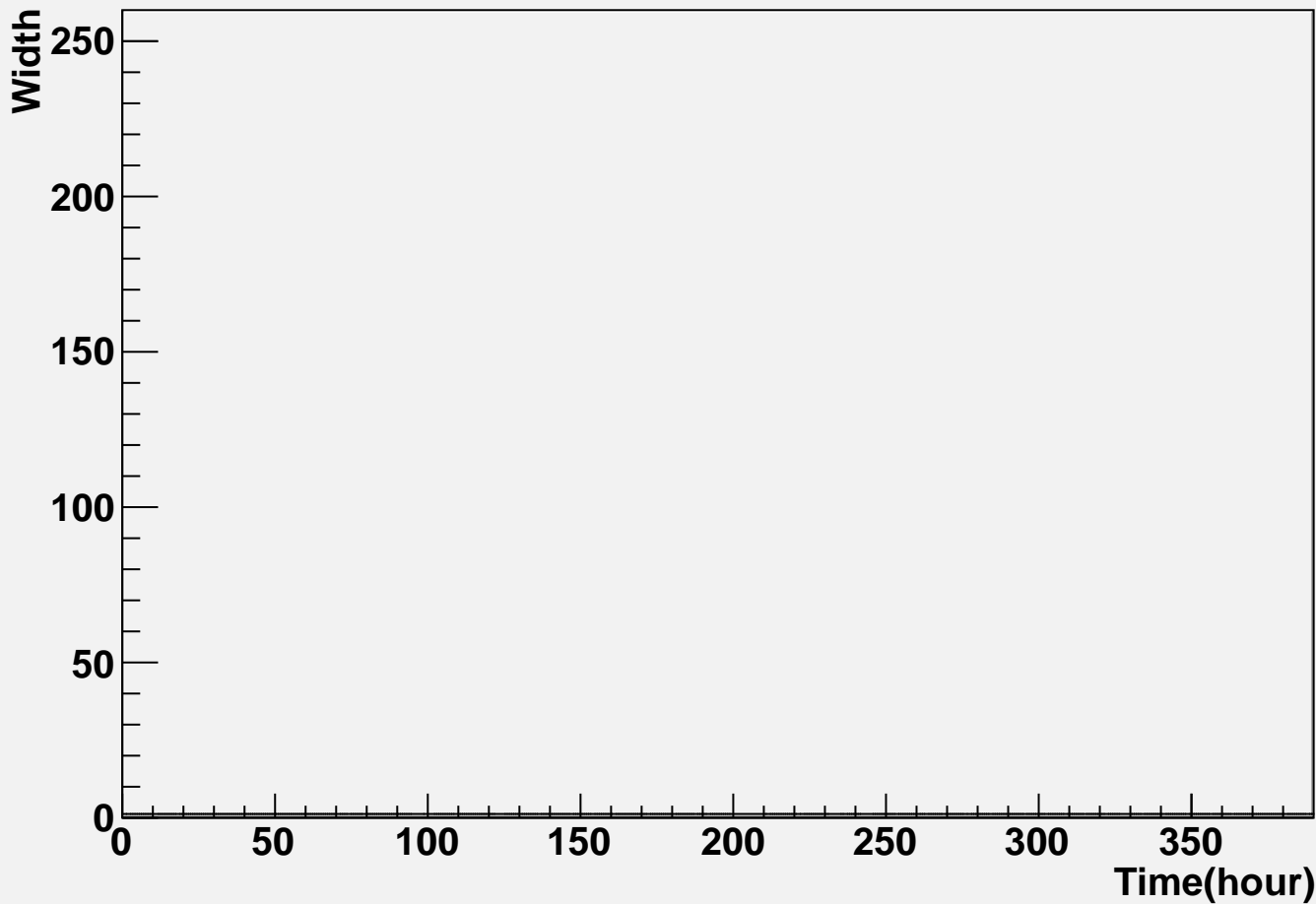
width-S1-SFET2B-Ch3



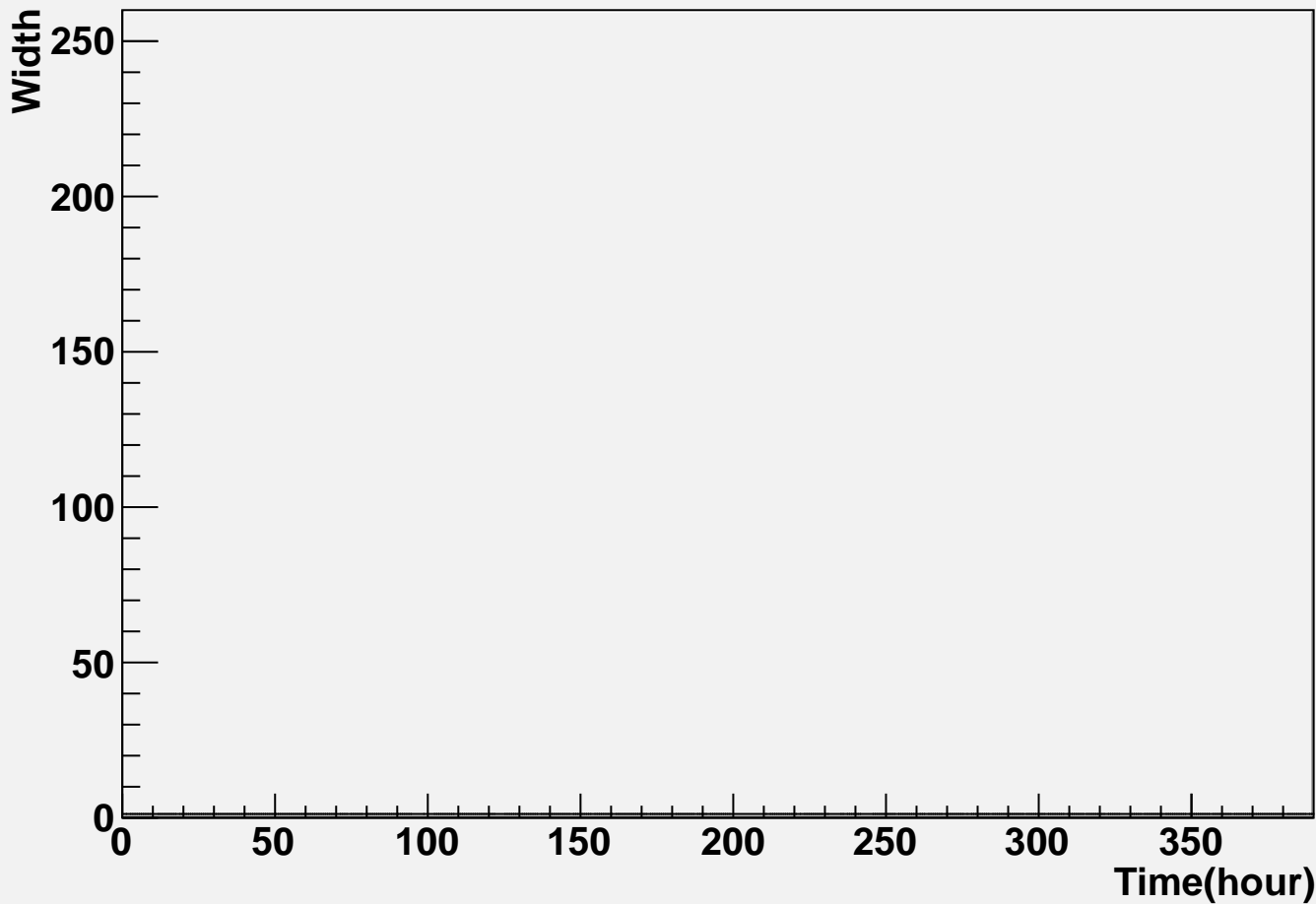
width-S1-SFET2B-Ch4



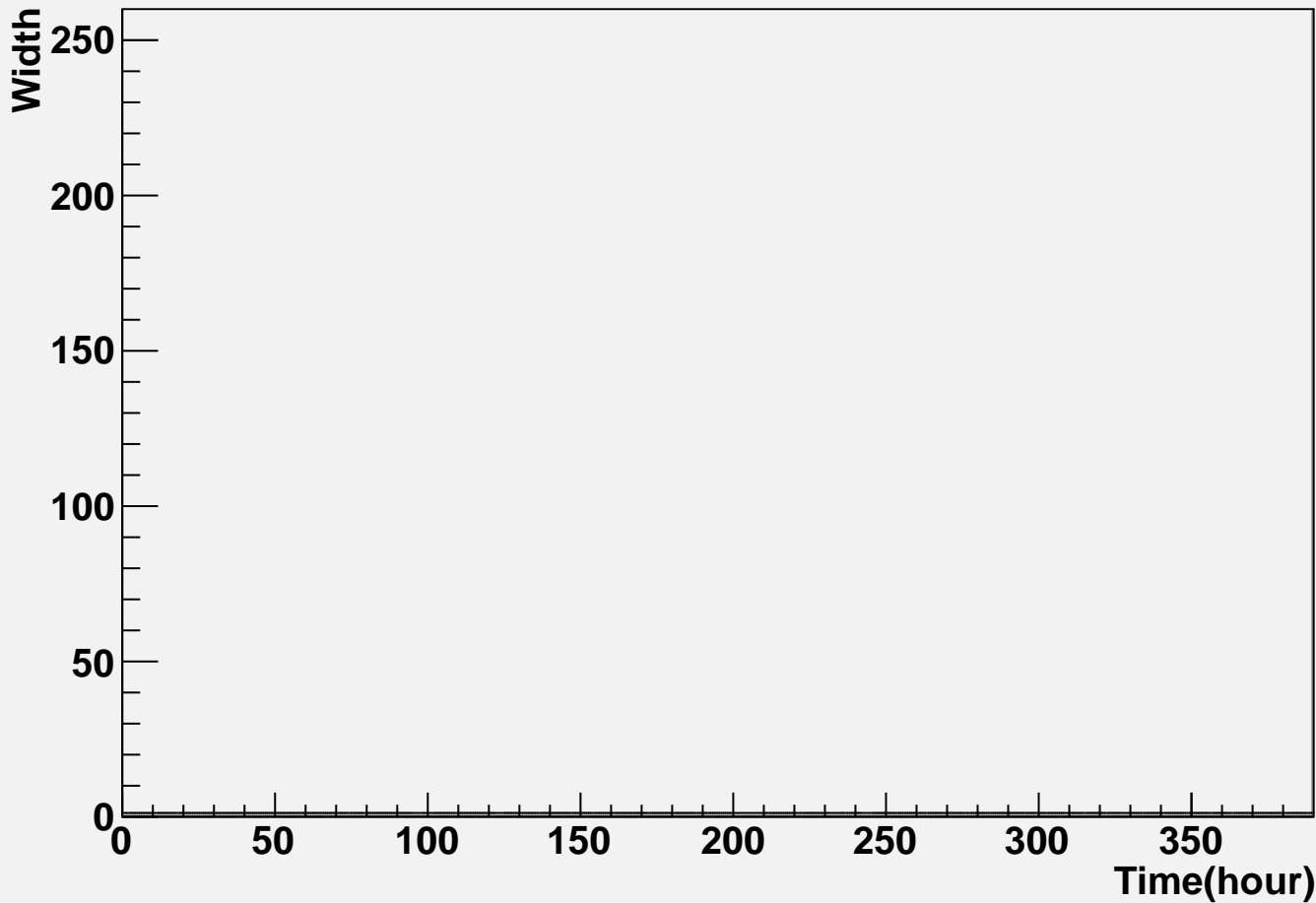
width-S1-SFET2B-Ch5



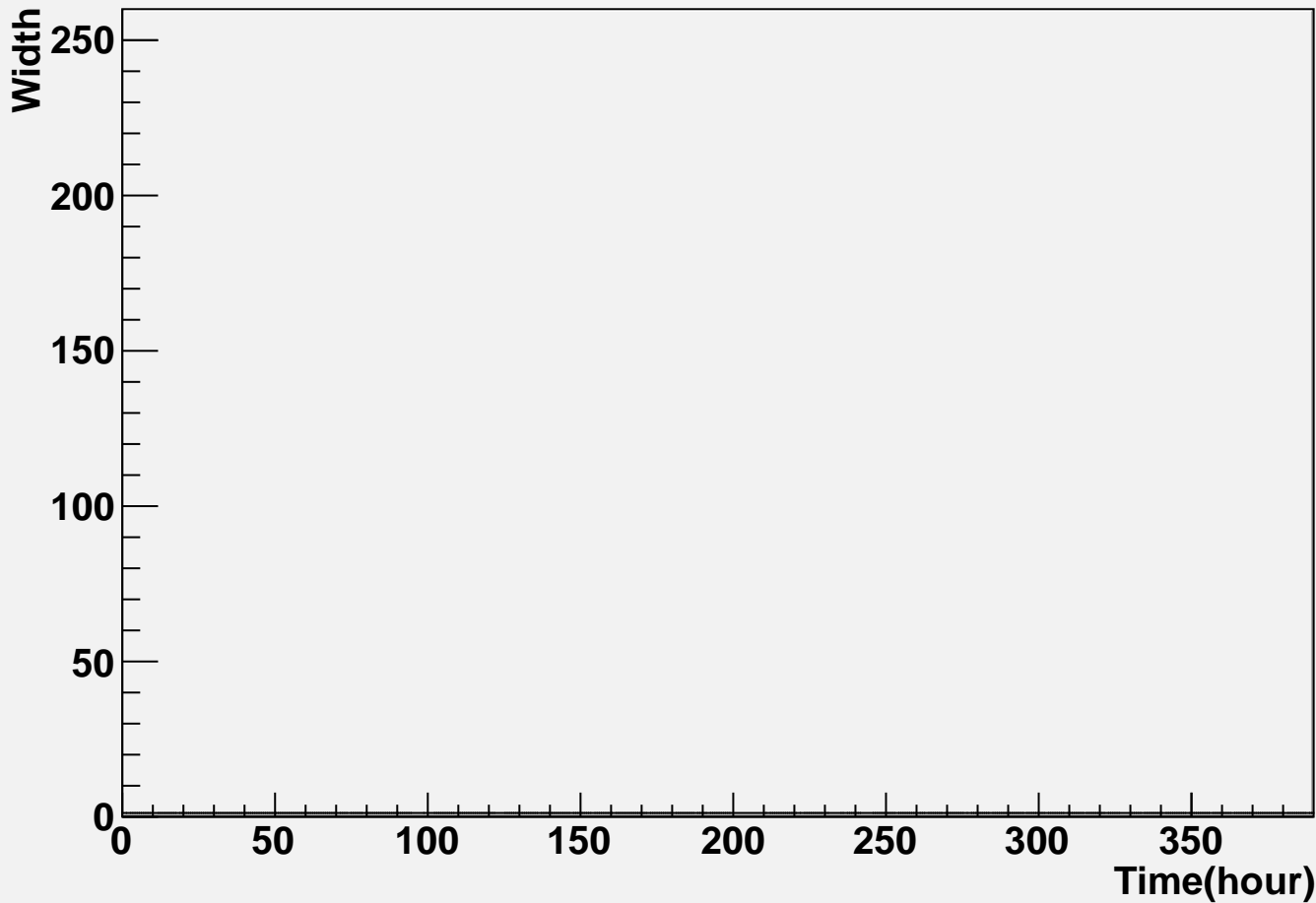
width-S1-SFET2B-Ch6



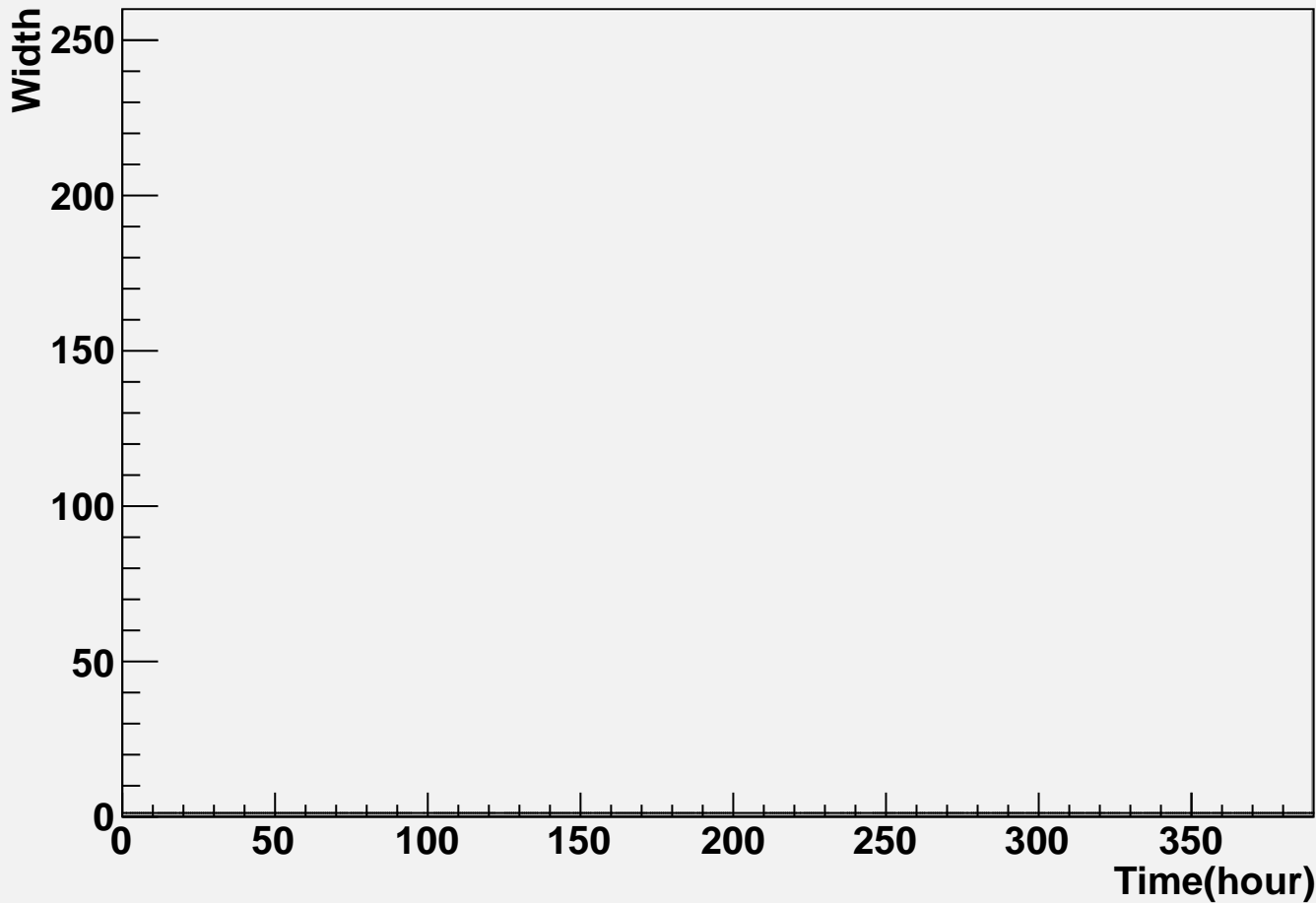
width-S1-SFET2B-Ch7



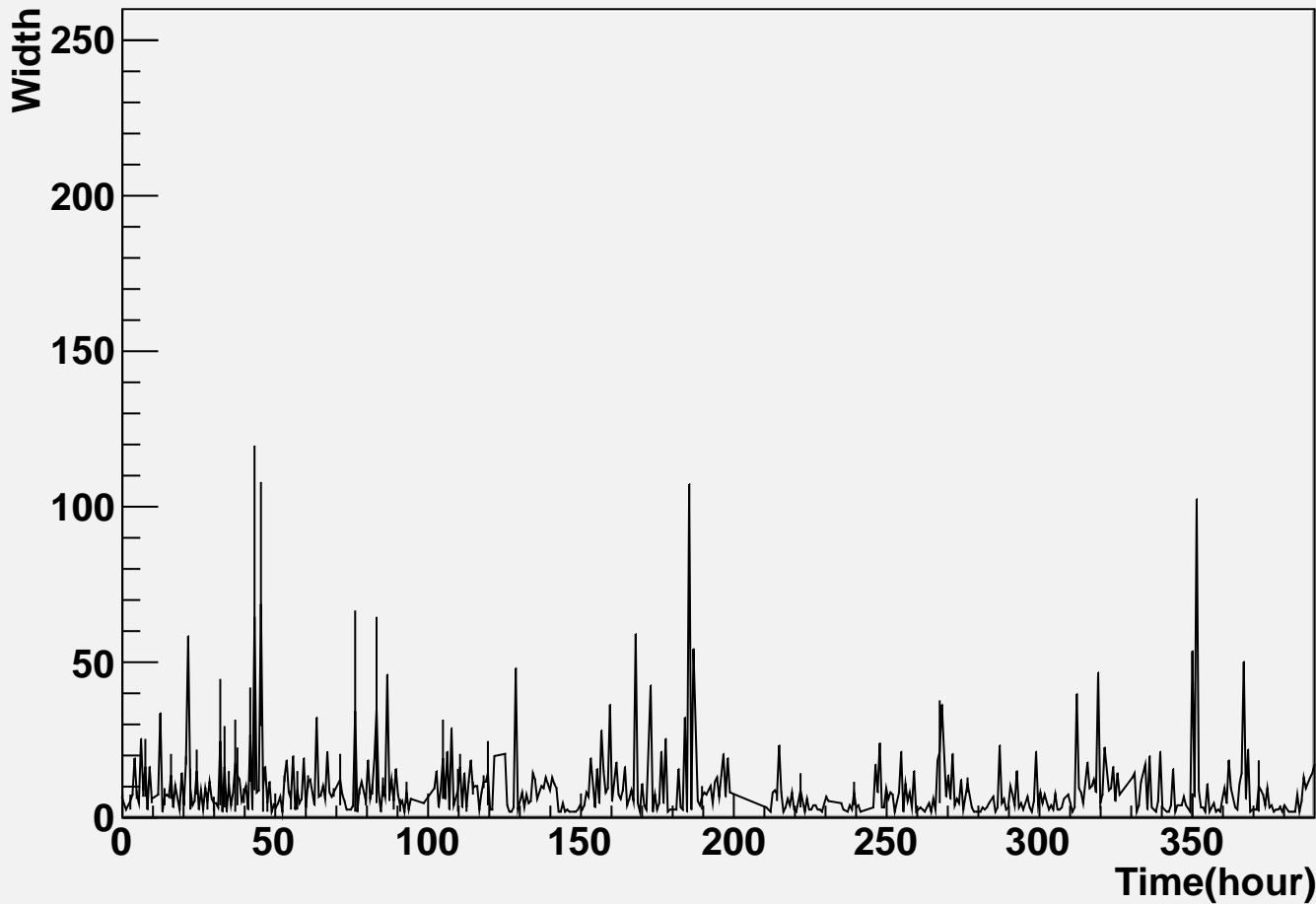
width-S1-SFET2B-Ch8



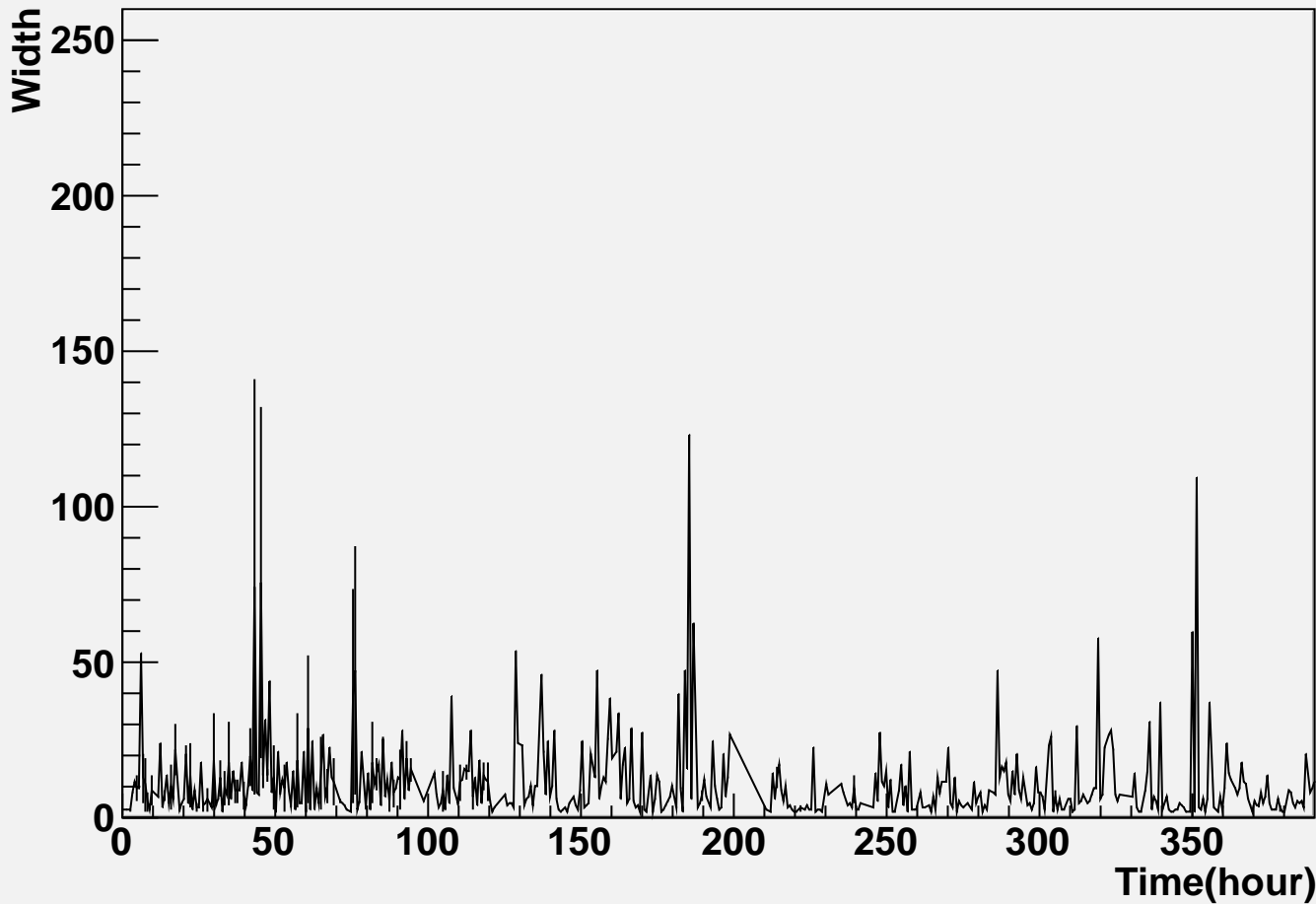
width-S1-SFET2B-Ch9



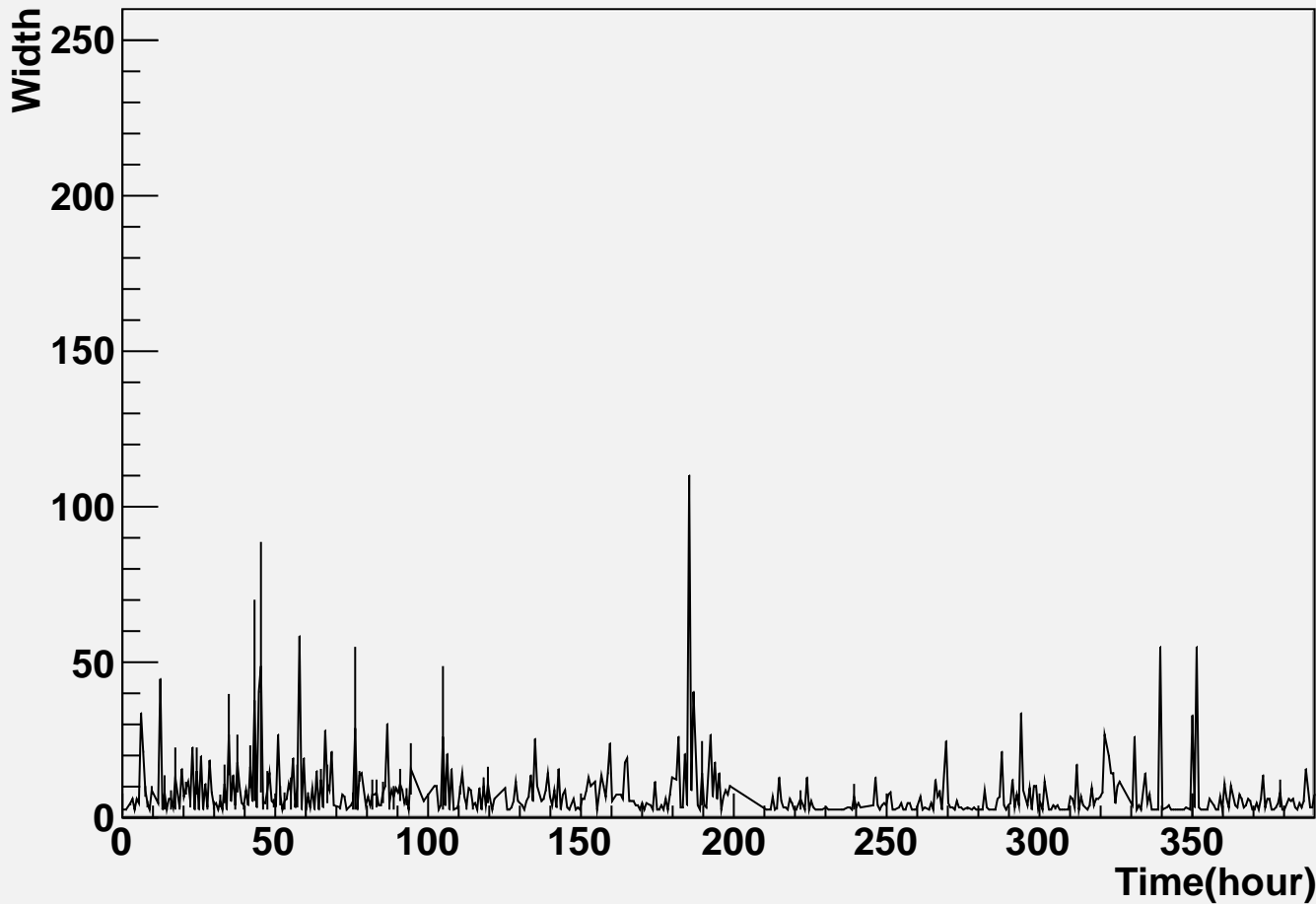
width-S1-SFET2C-Ch0



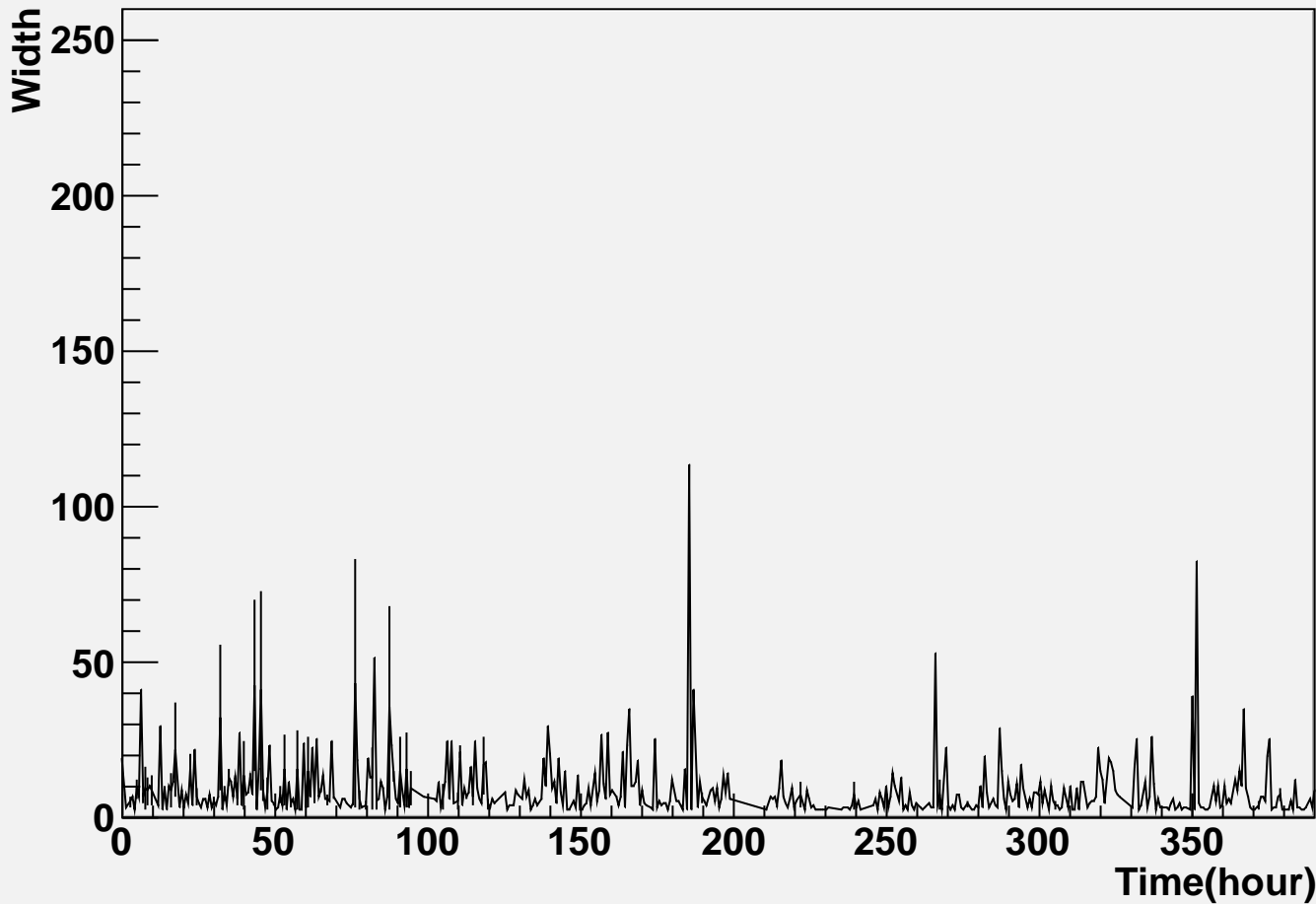
width-S1-SFET2C-Ch1



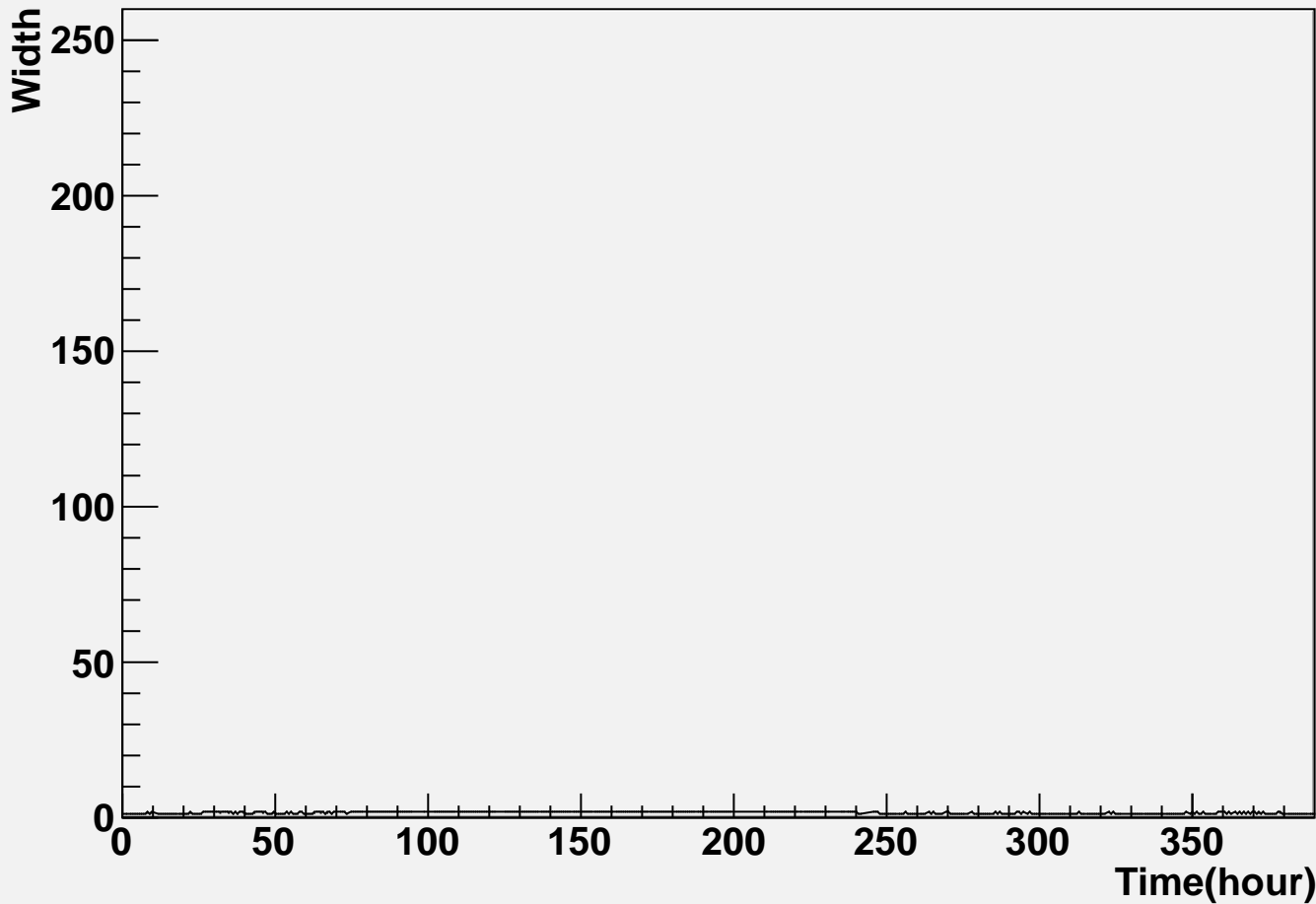
width-S1-SFET2C-Ch2



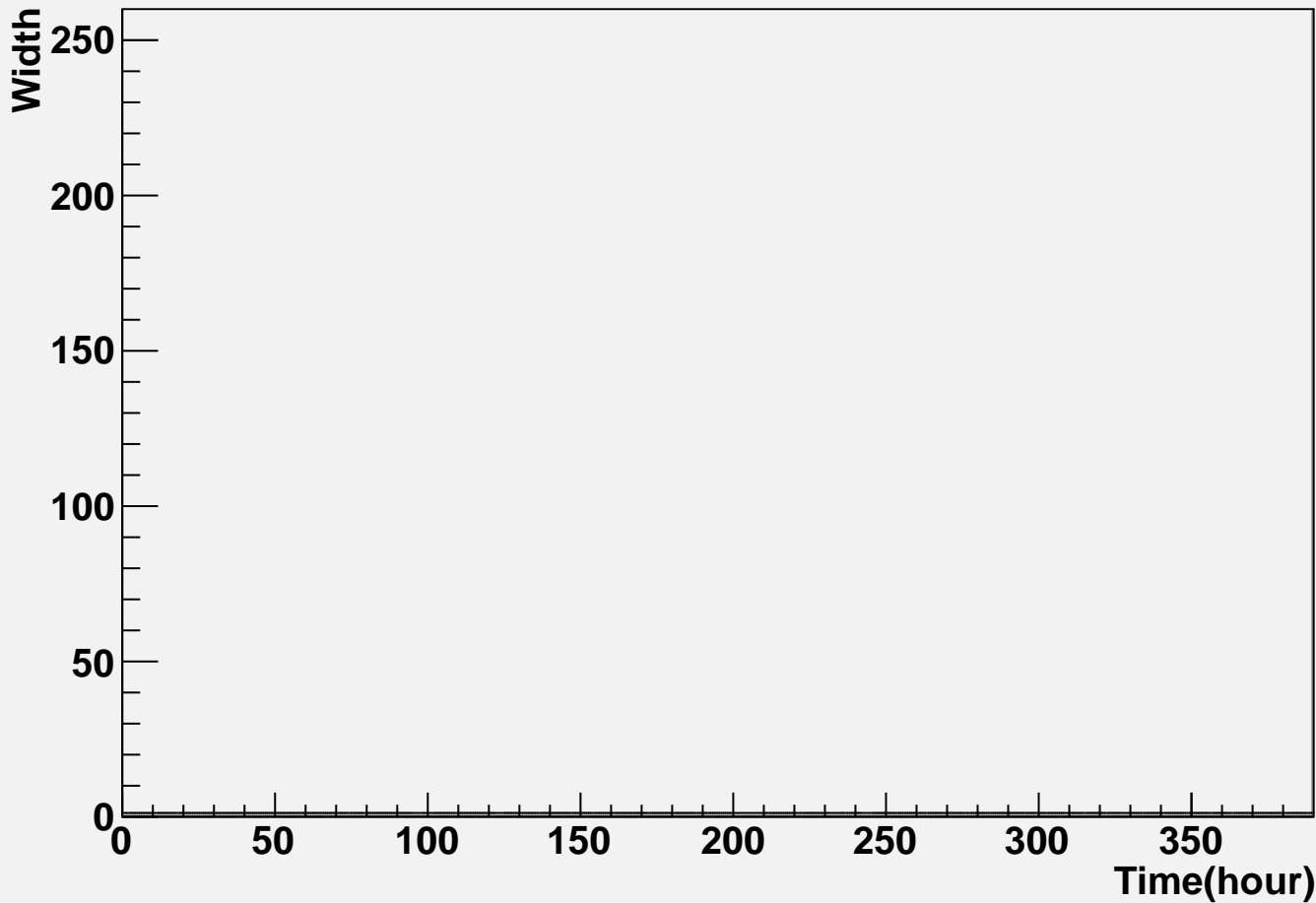
width-S1-SFET2C-Ch3



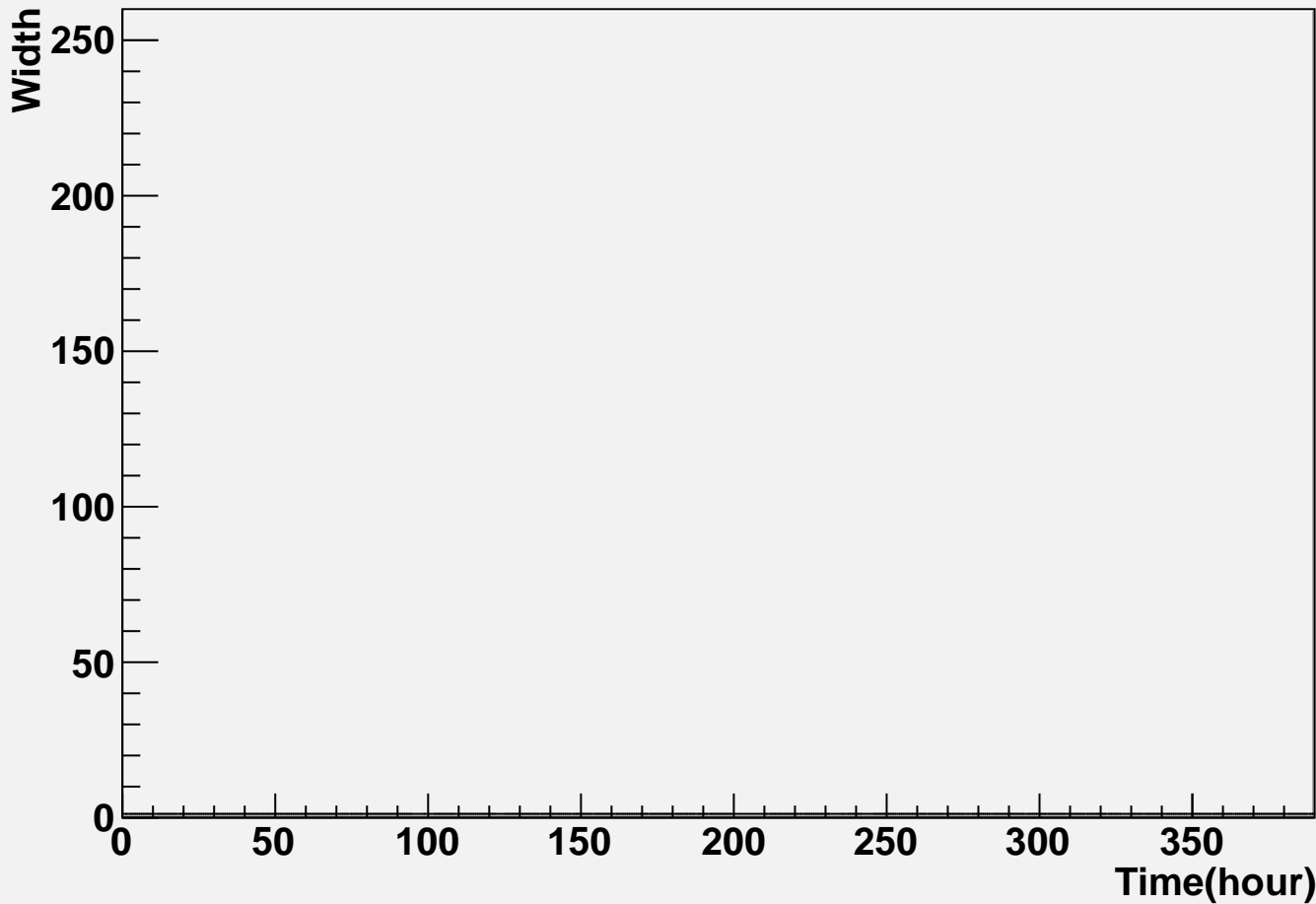
width-S1-SFET2C-Ch4



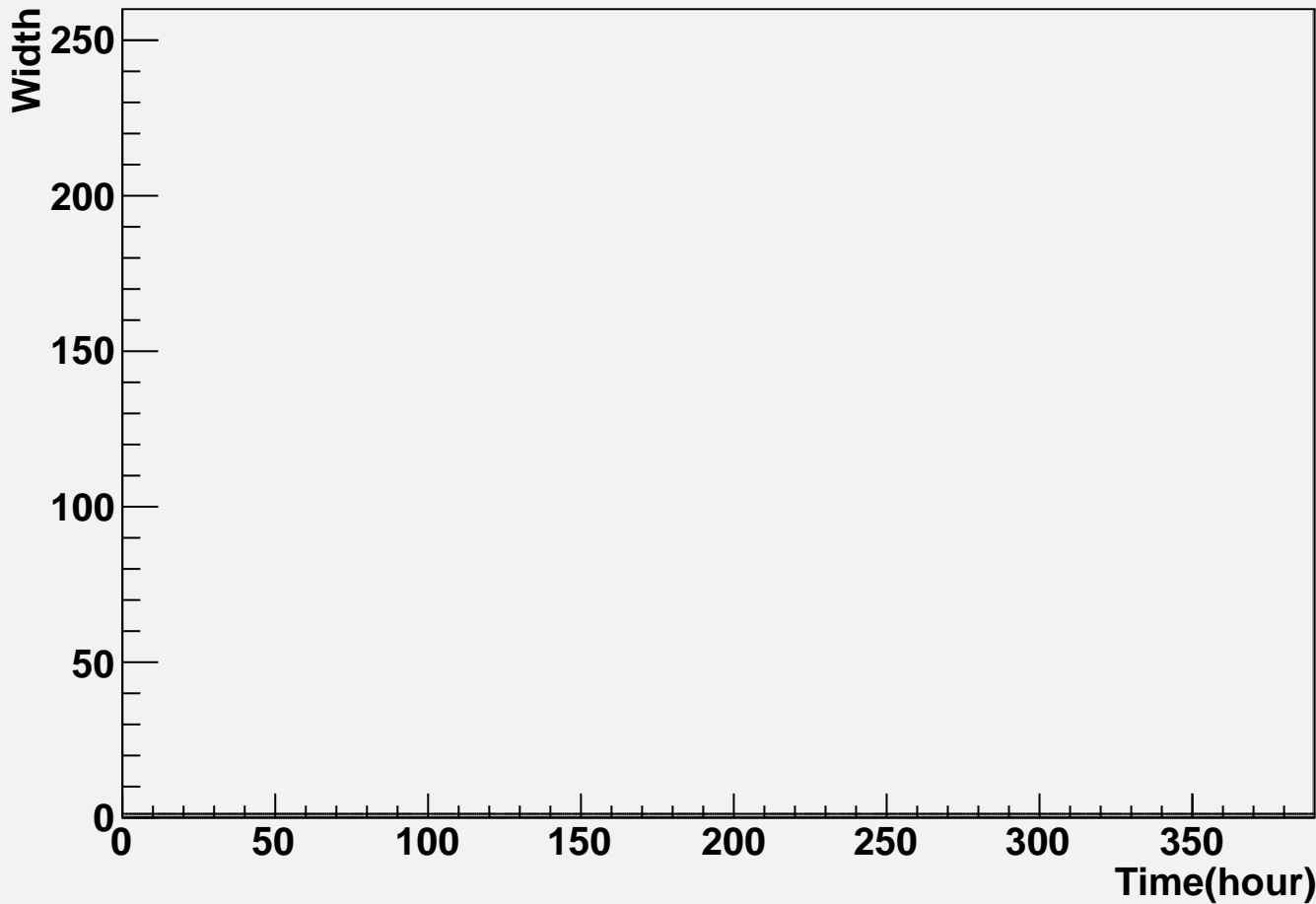
width-S1-SFET2C-Ch5



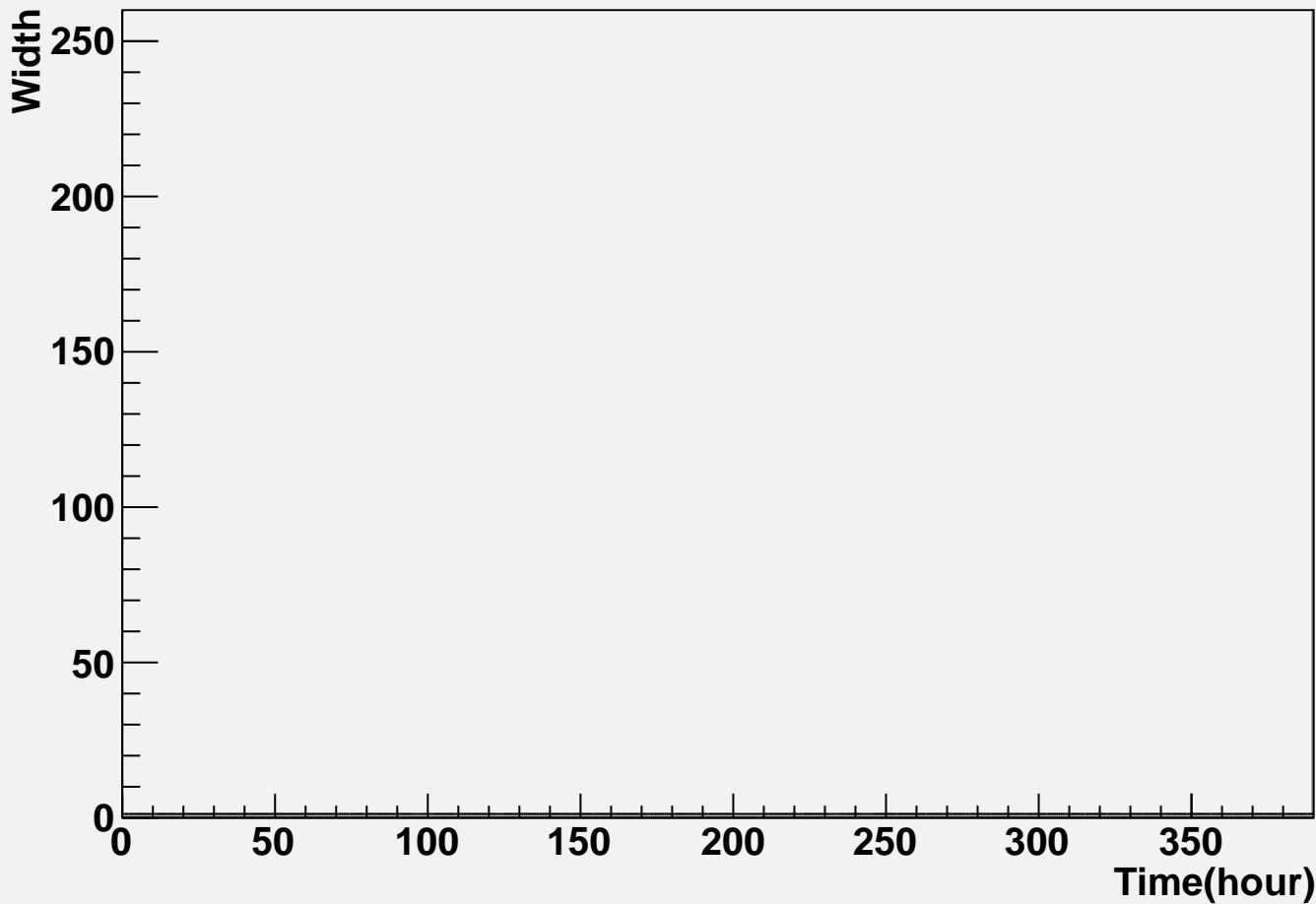
width-S1-SFET2C-Ch6



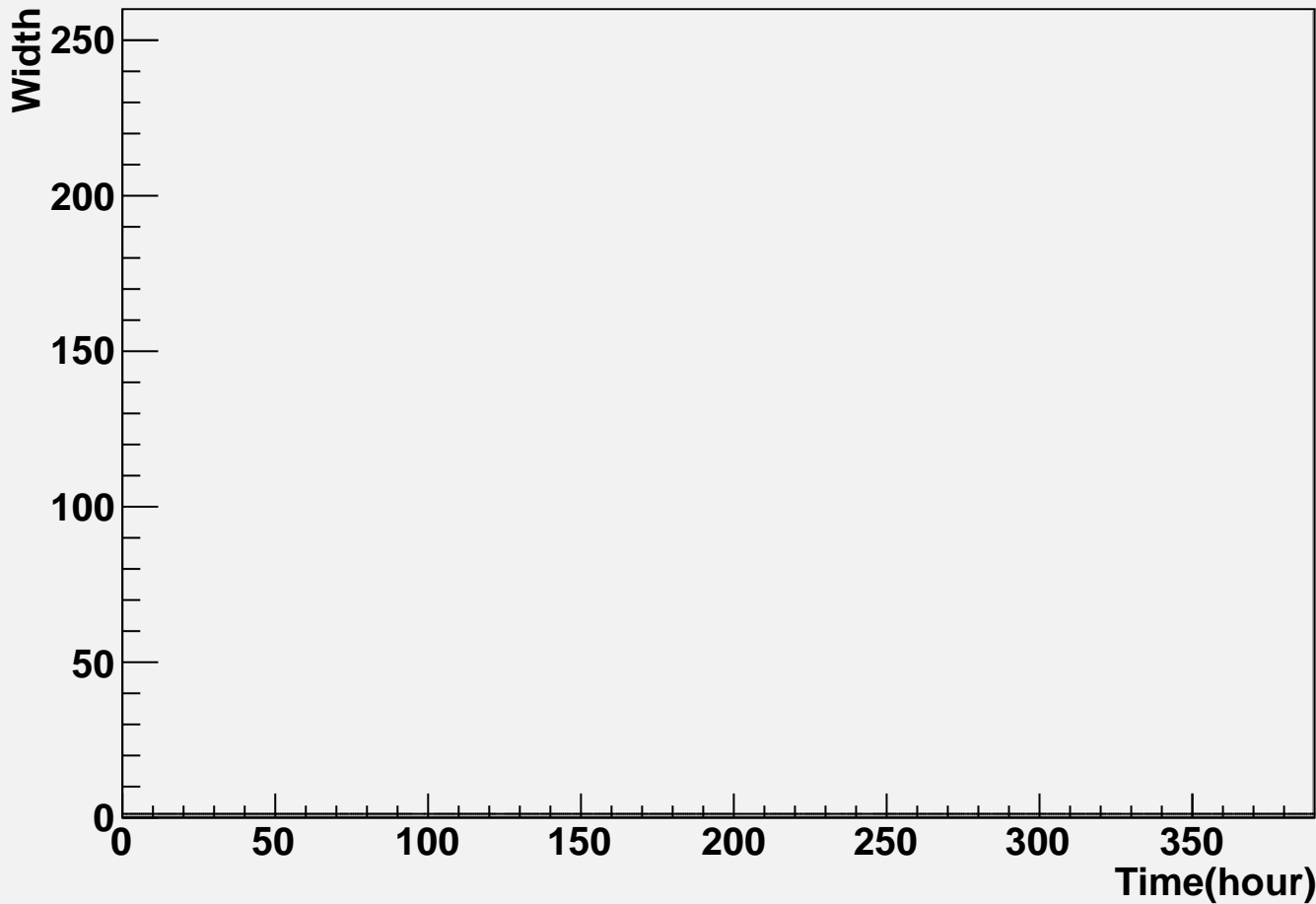
width-S1-SFET2C-Ch7



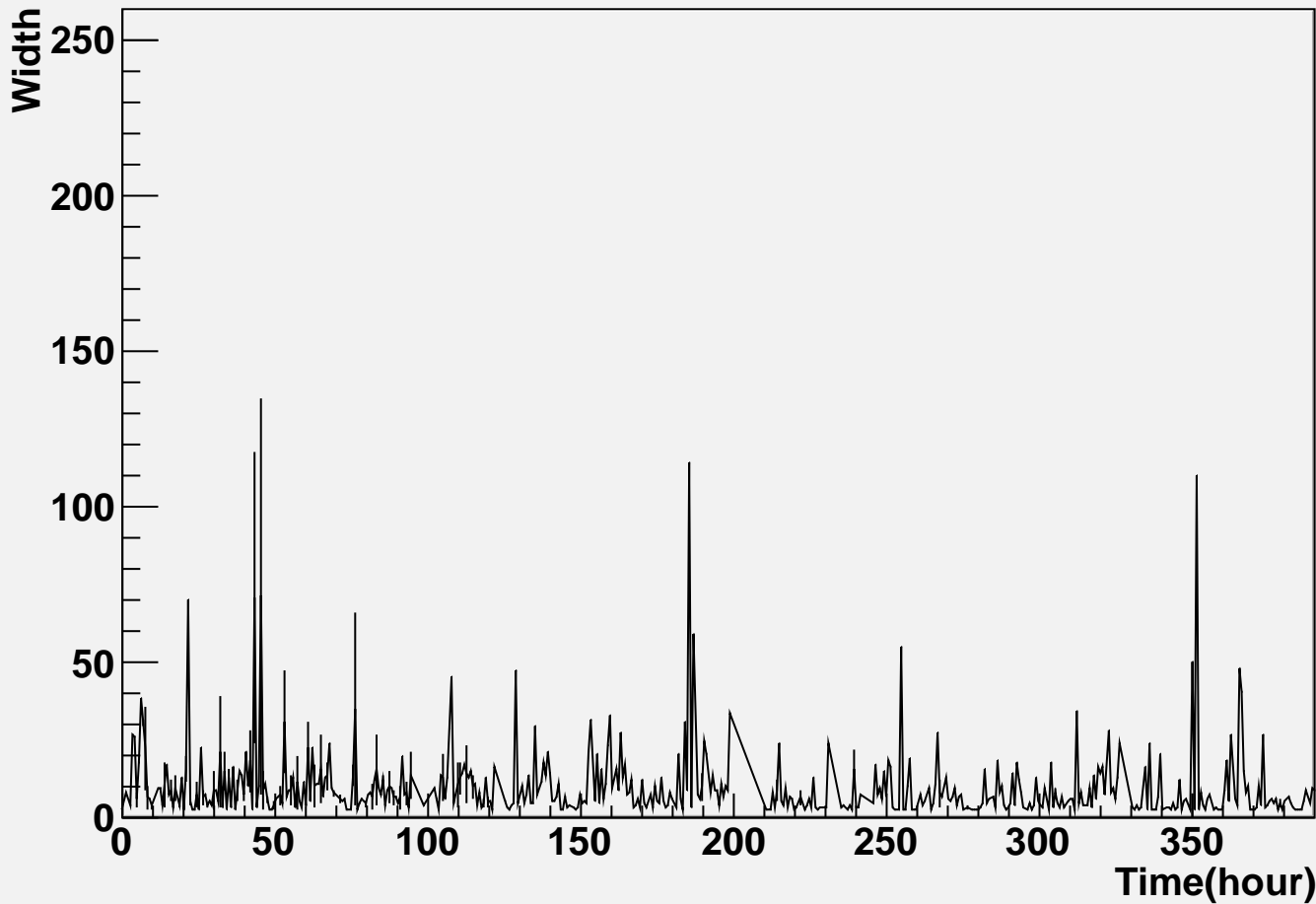
width-S1-SFET2C-Ch8



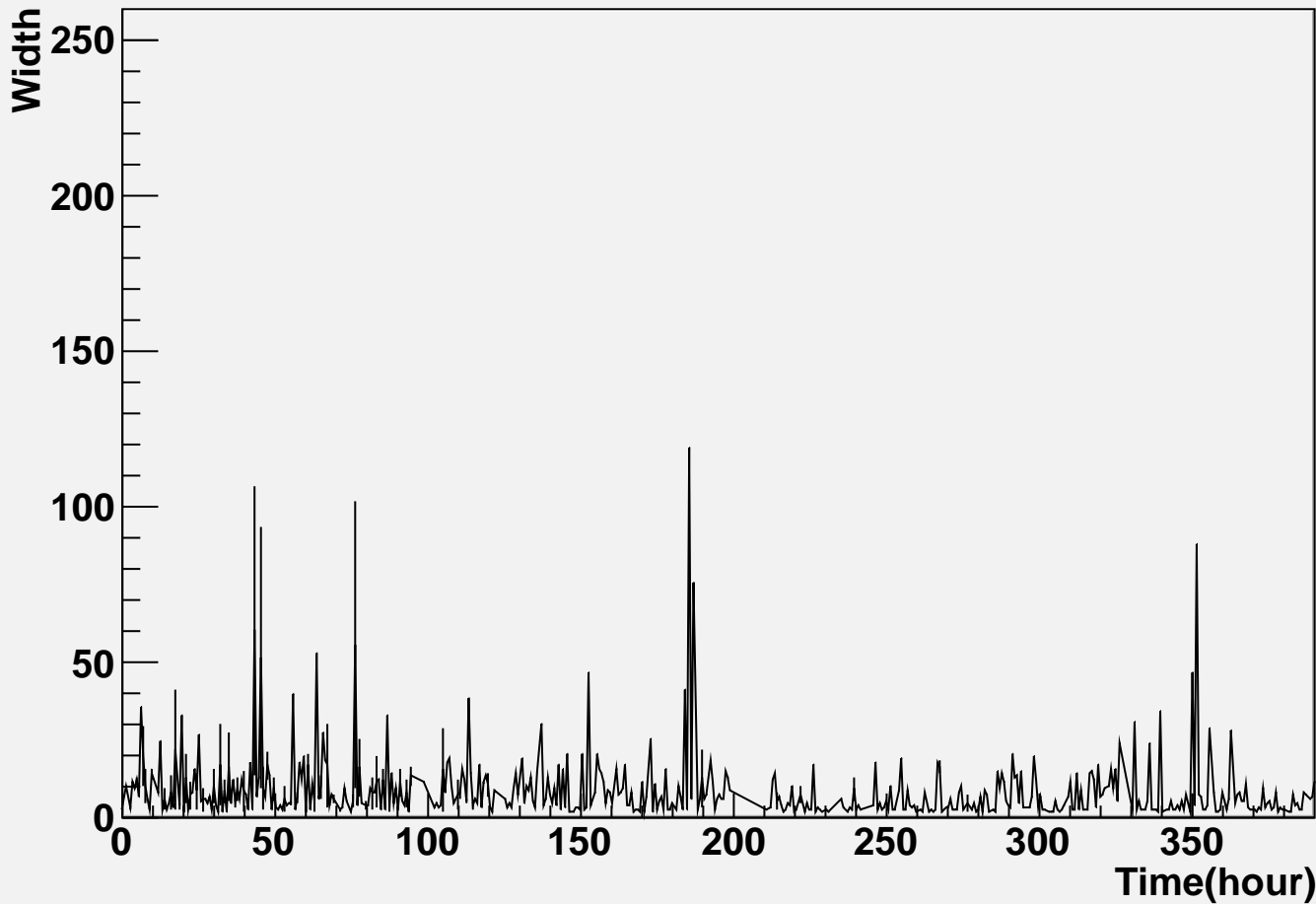
width-S1-SFET2C-Ch9



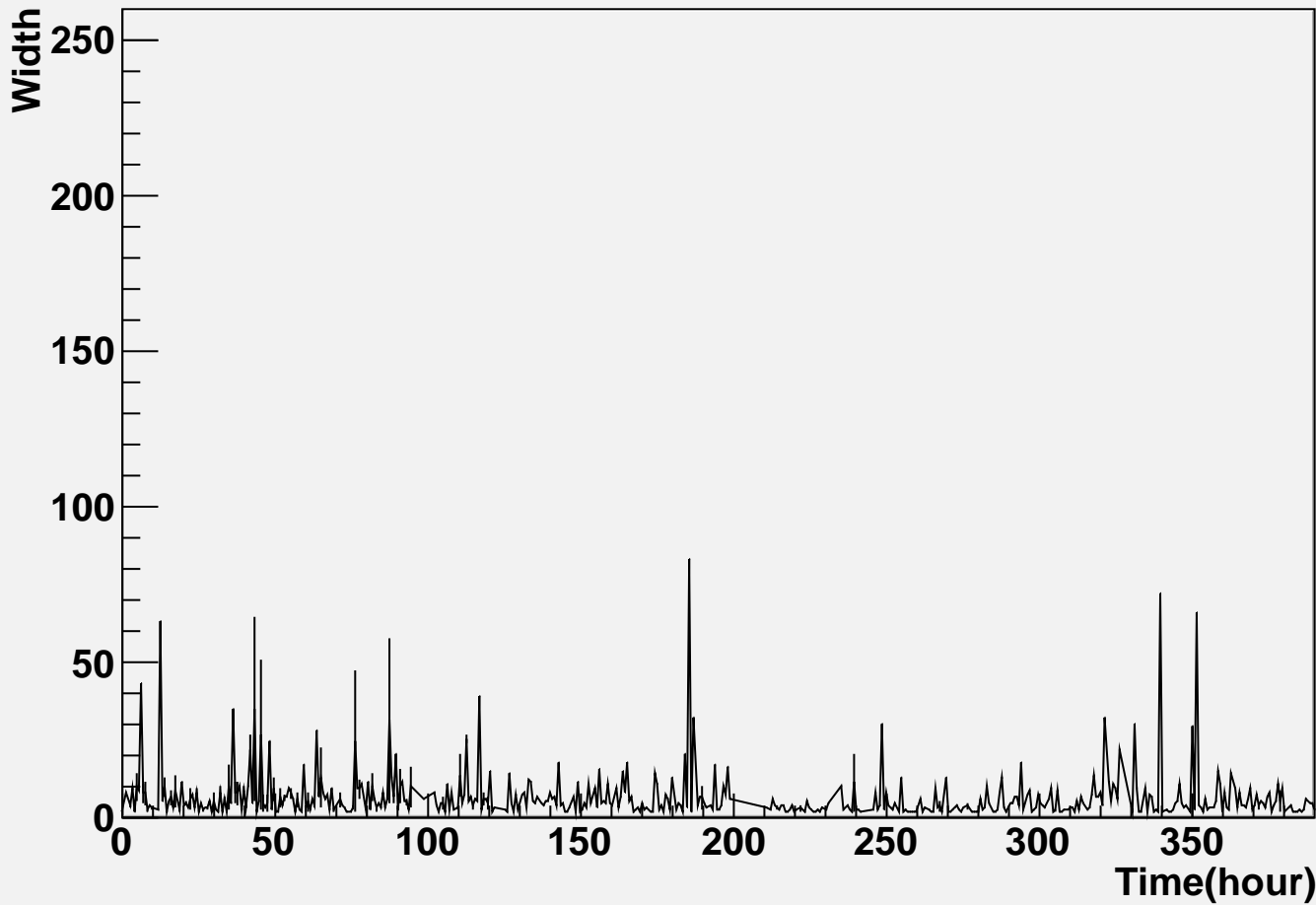
width-S1-SFET2D-Ch0



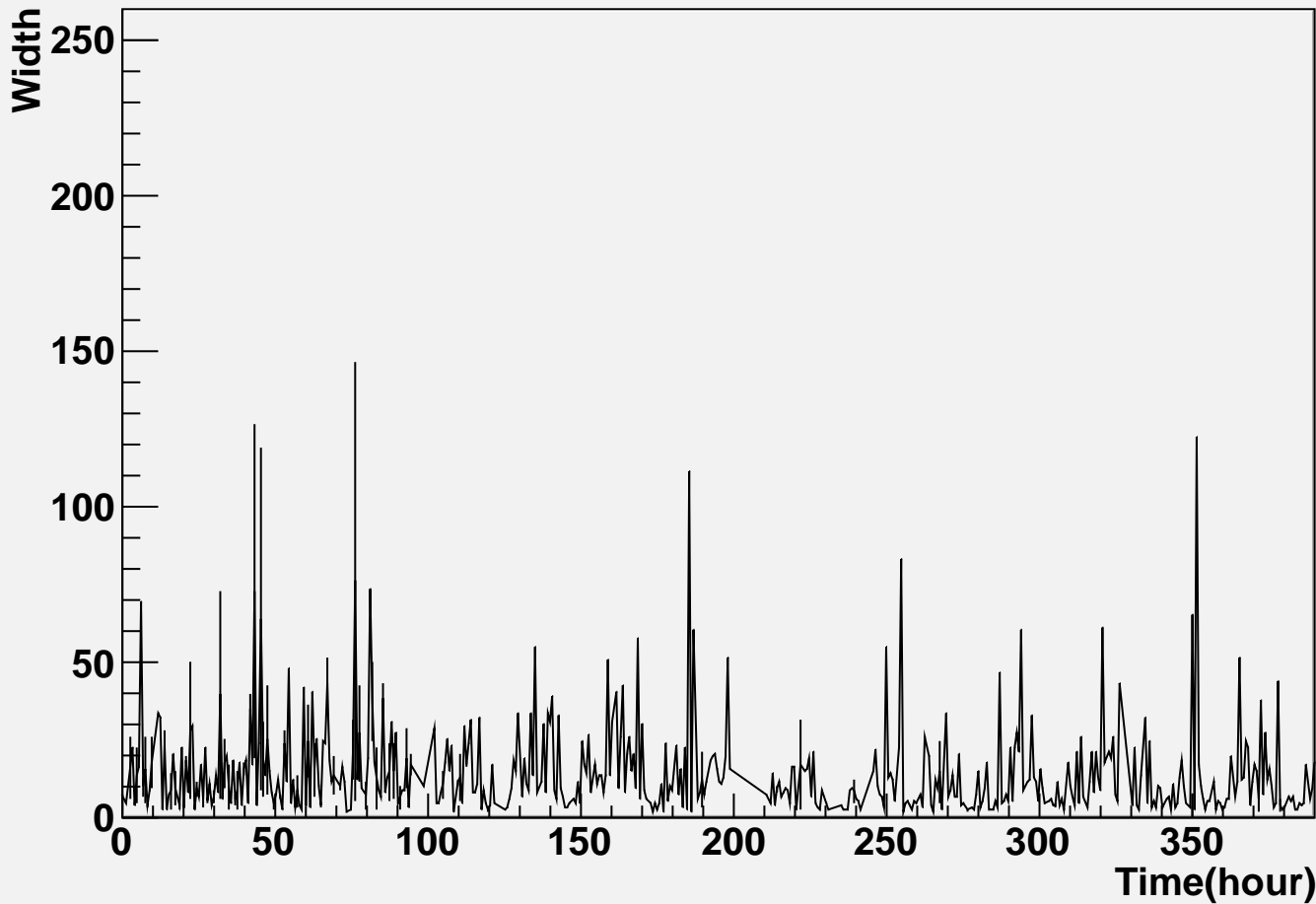
width-S1-SFET2D-Ch1



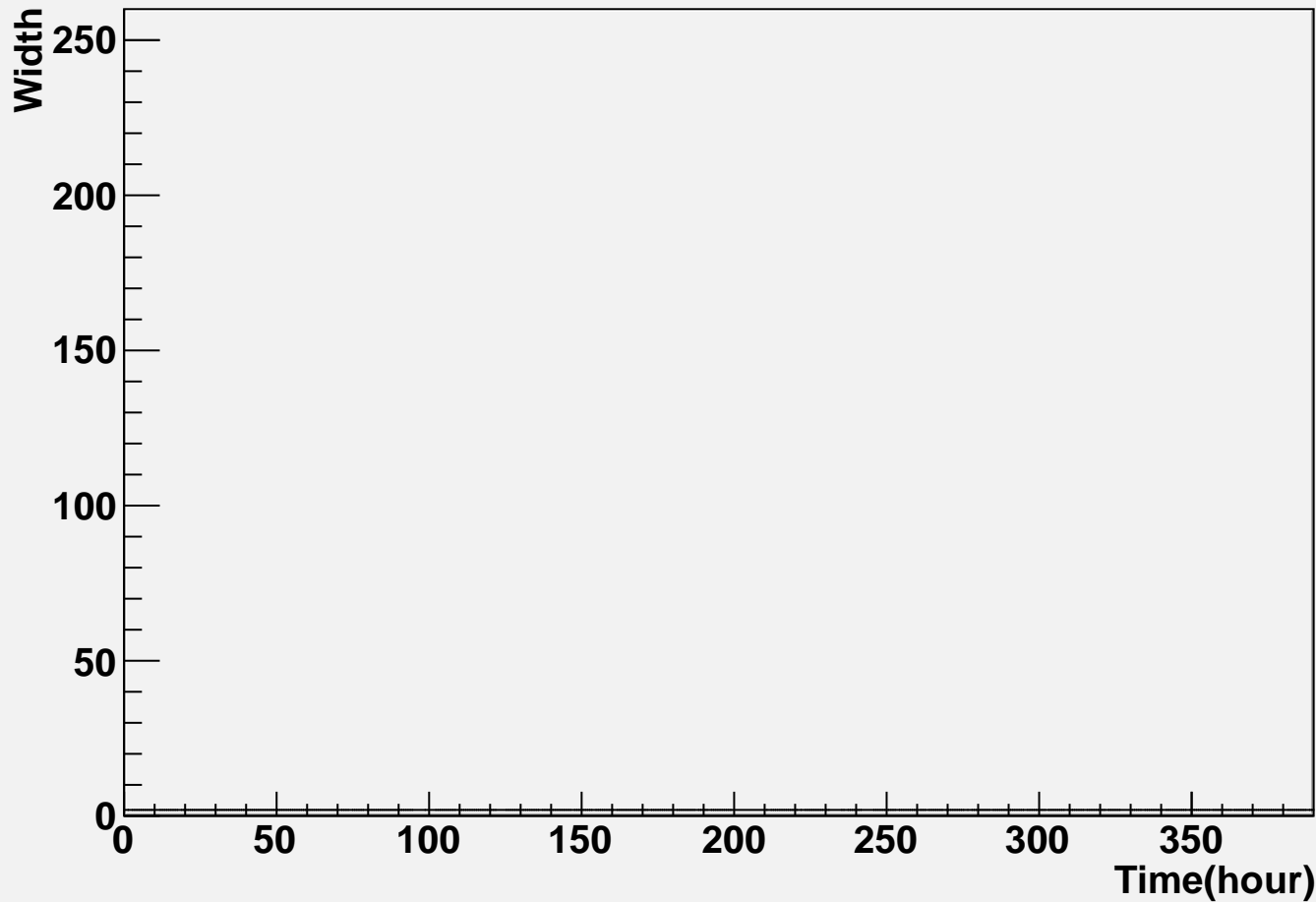
width-S1-SFET2D-Ch2



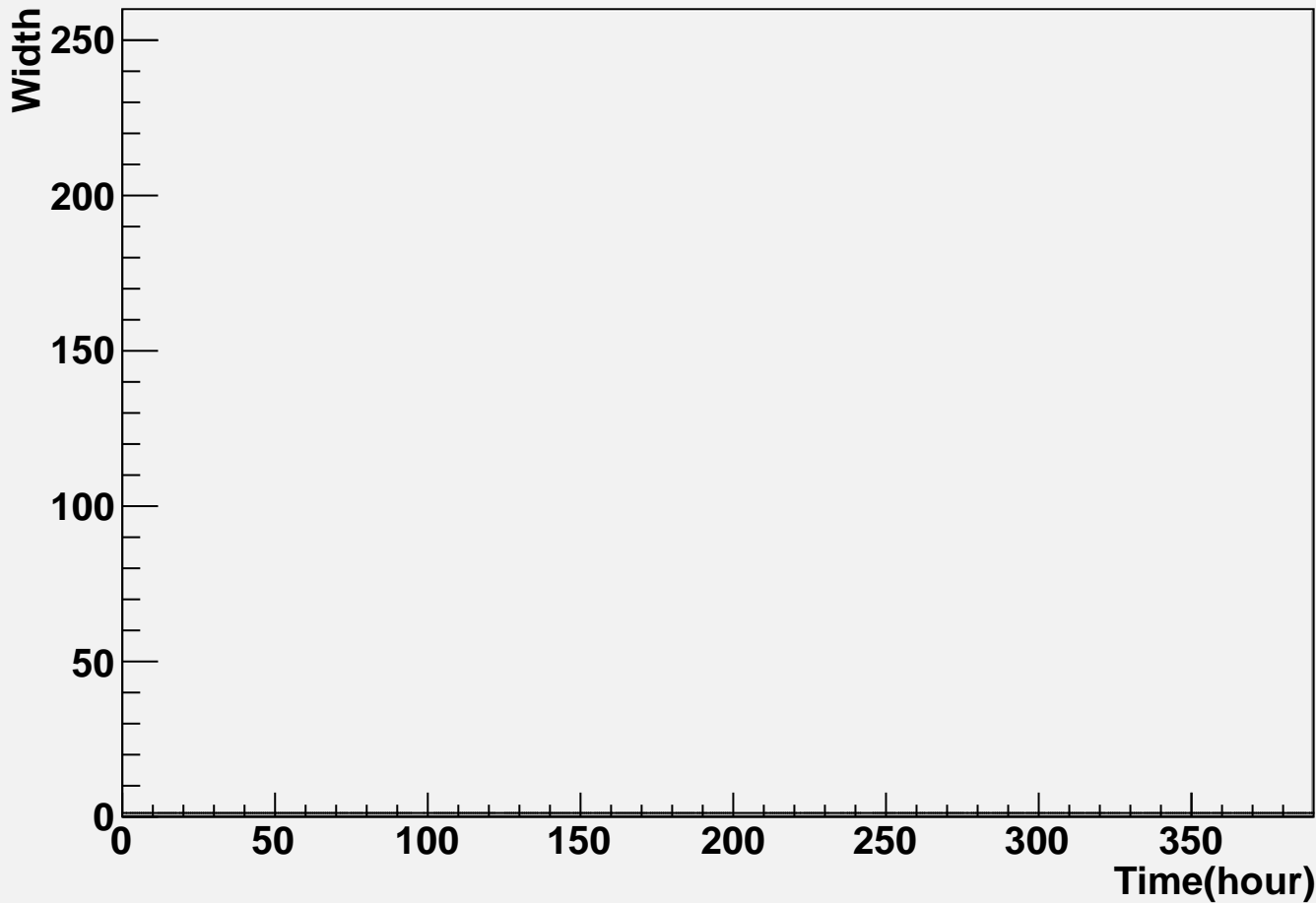
width-S1-SFET2D-Ch3



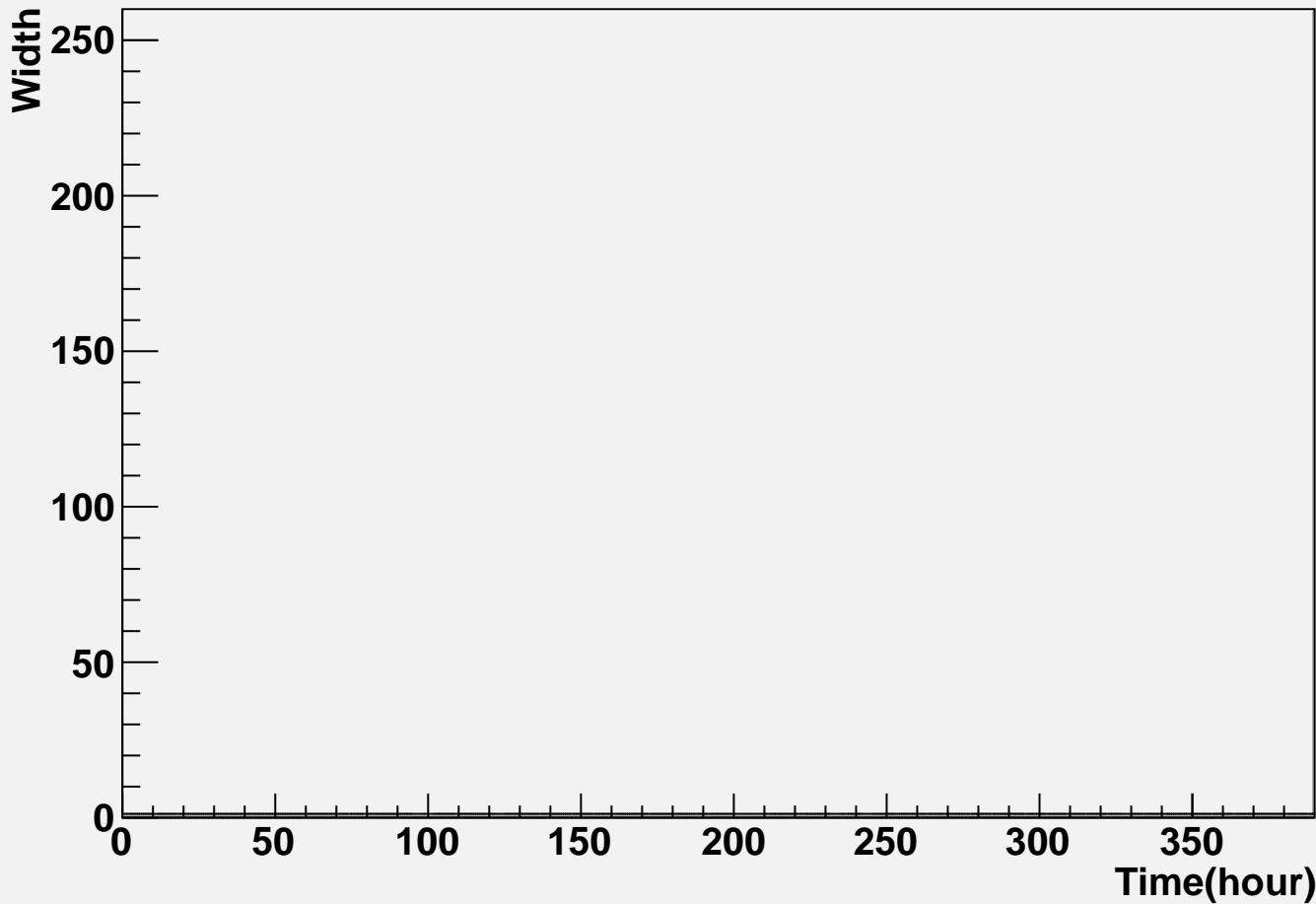
width-S1-SFET2D-Ch4



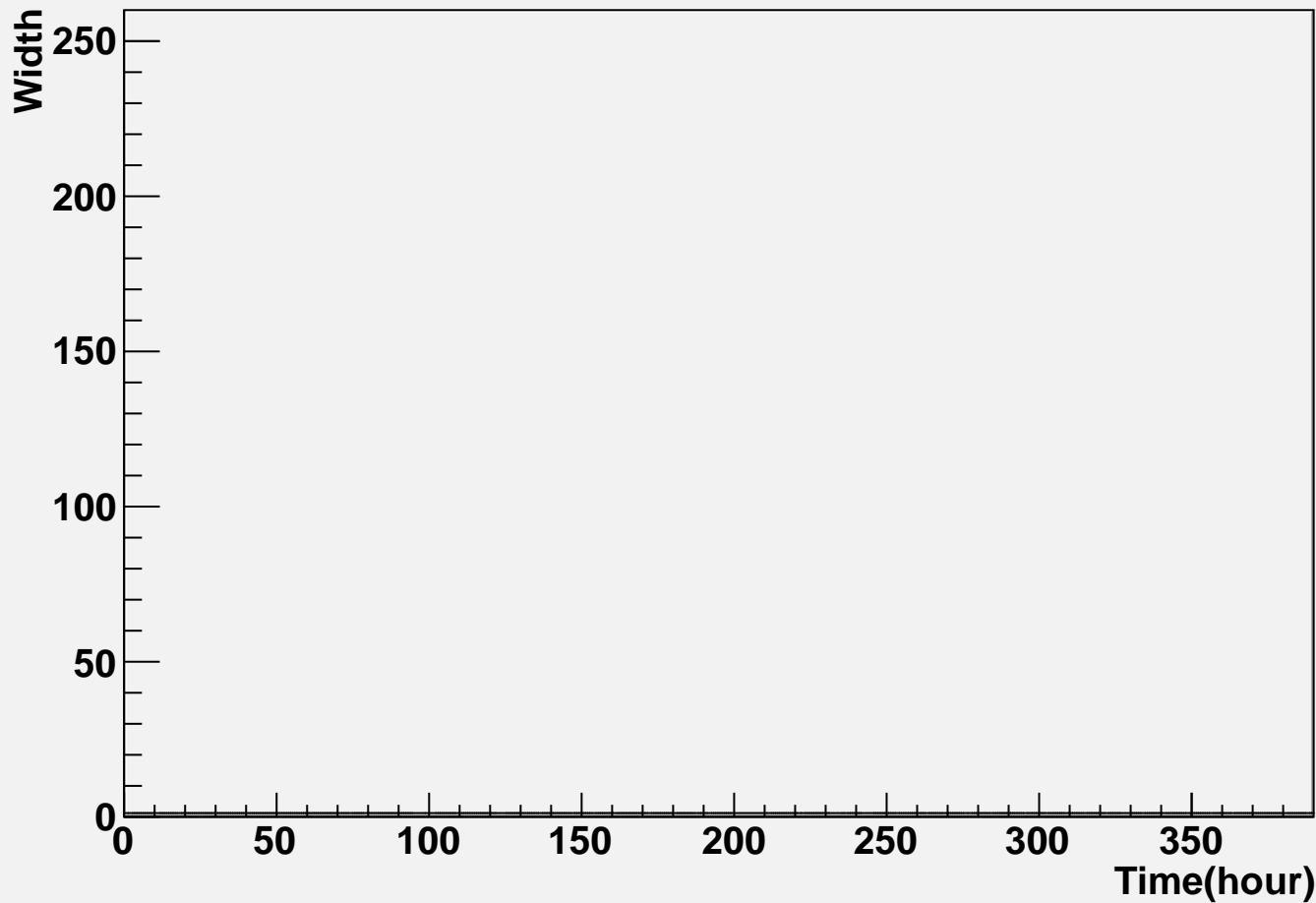
width-S1-SFET2D-Ch5



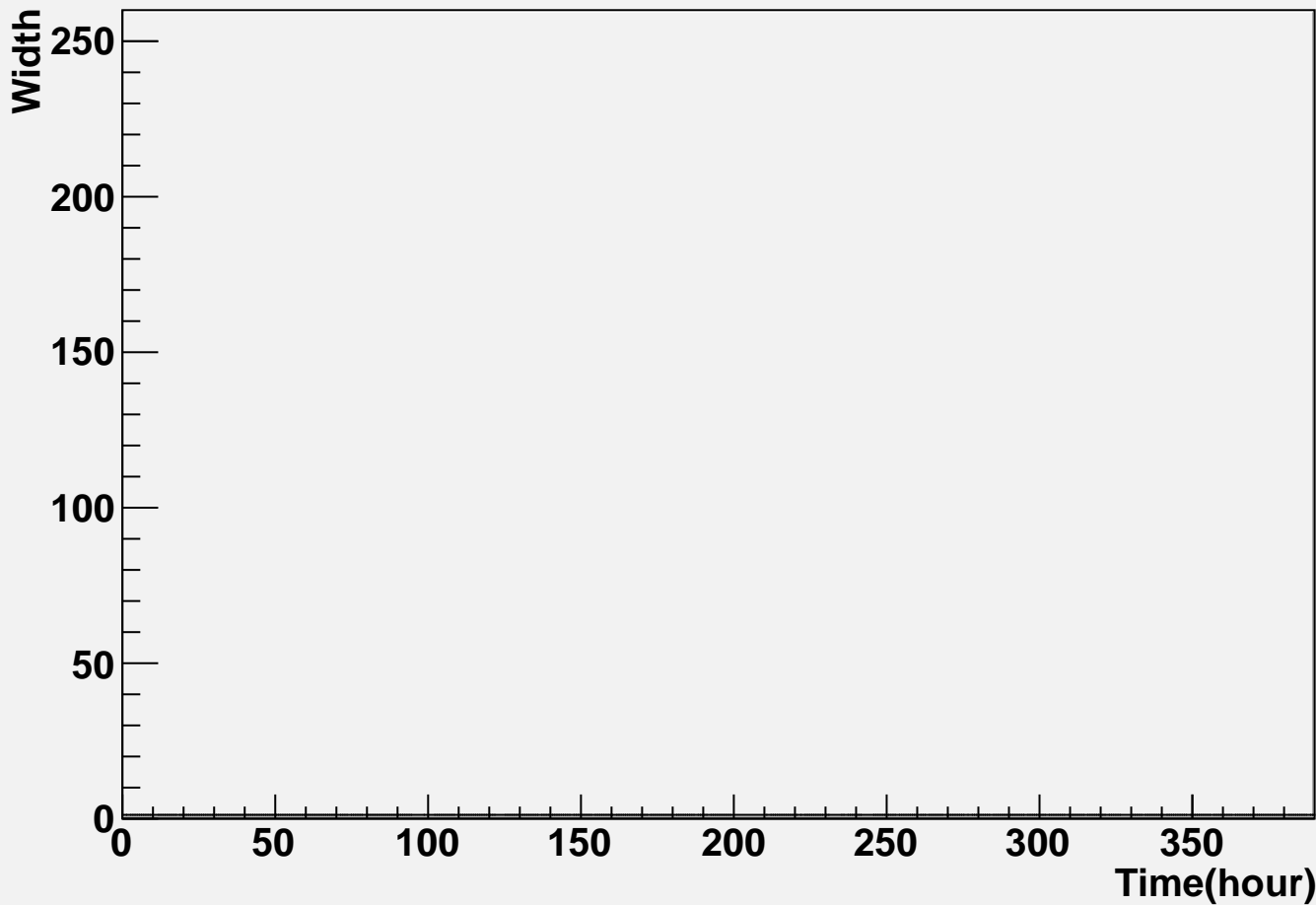
width-S1-SFET2D-Ch6



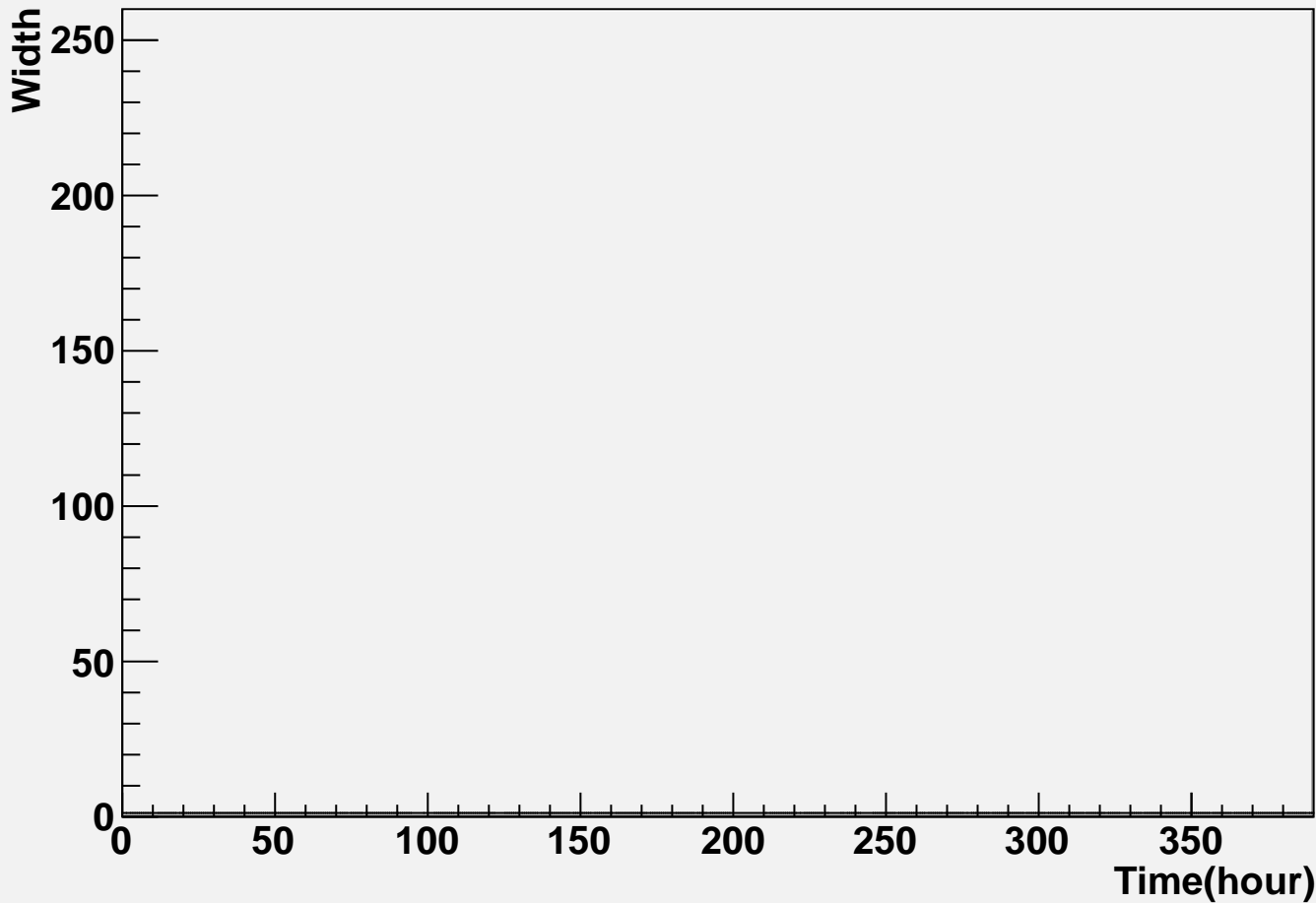
width-S1-SFET2D-Ch7



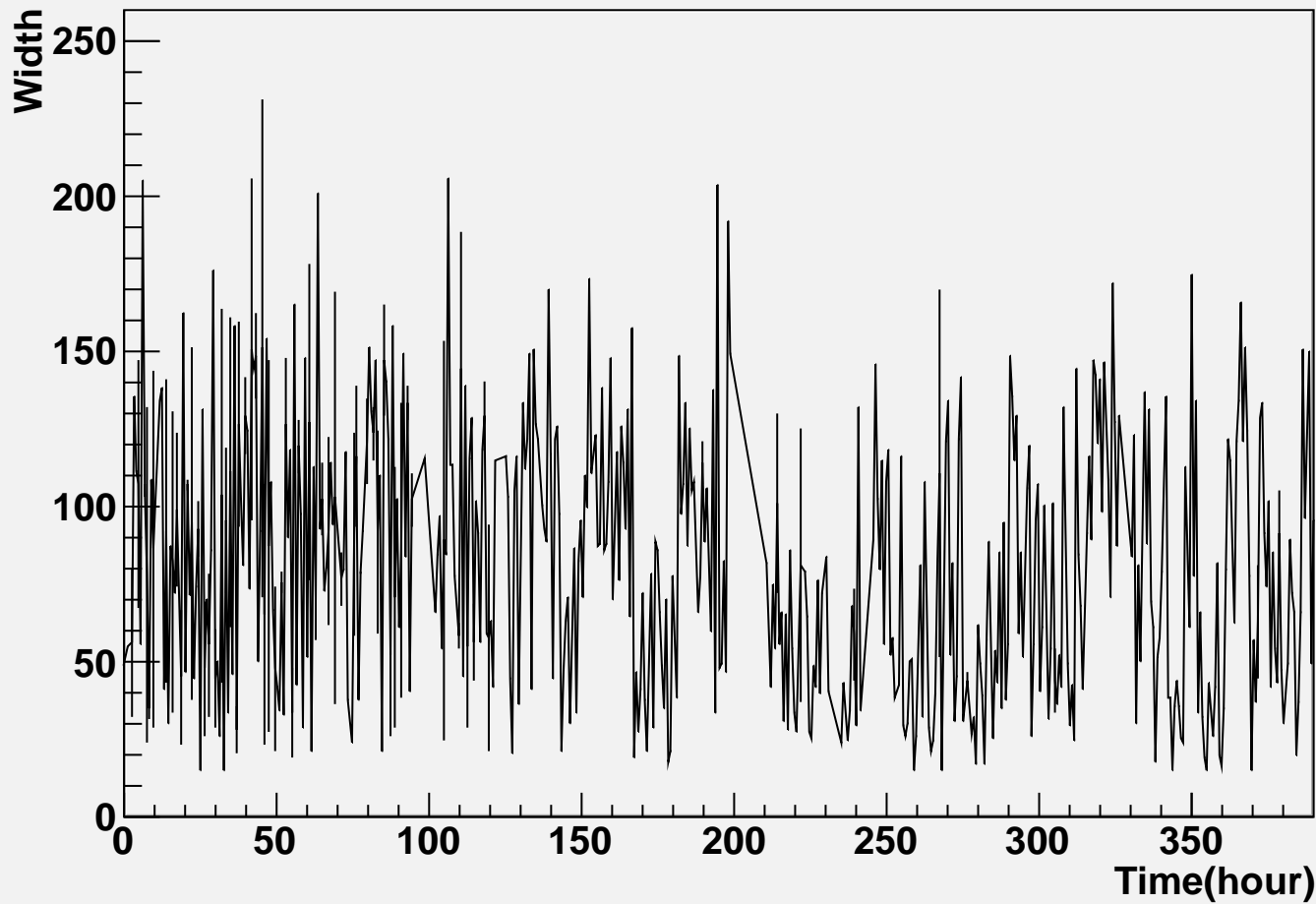
width-S1-SFET2D-Ch8



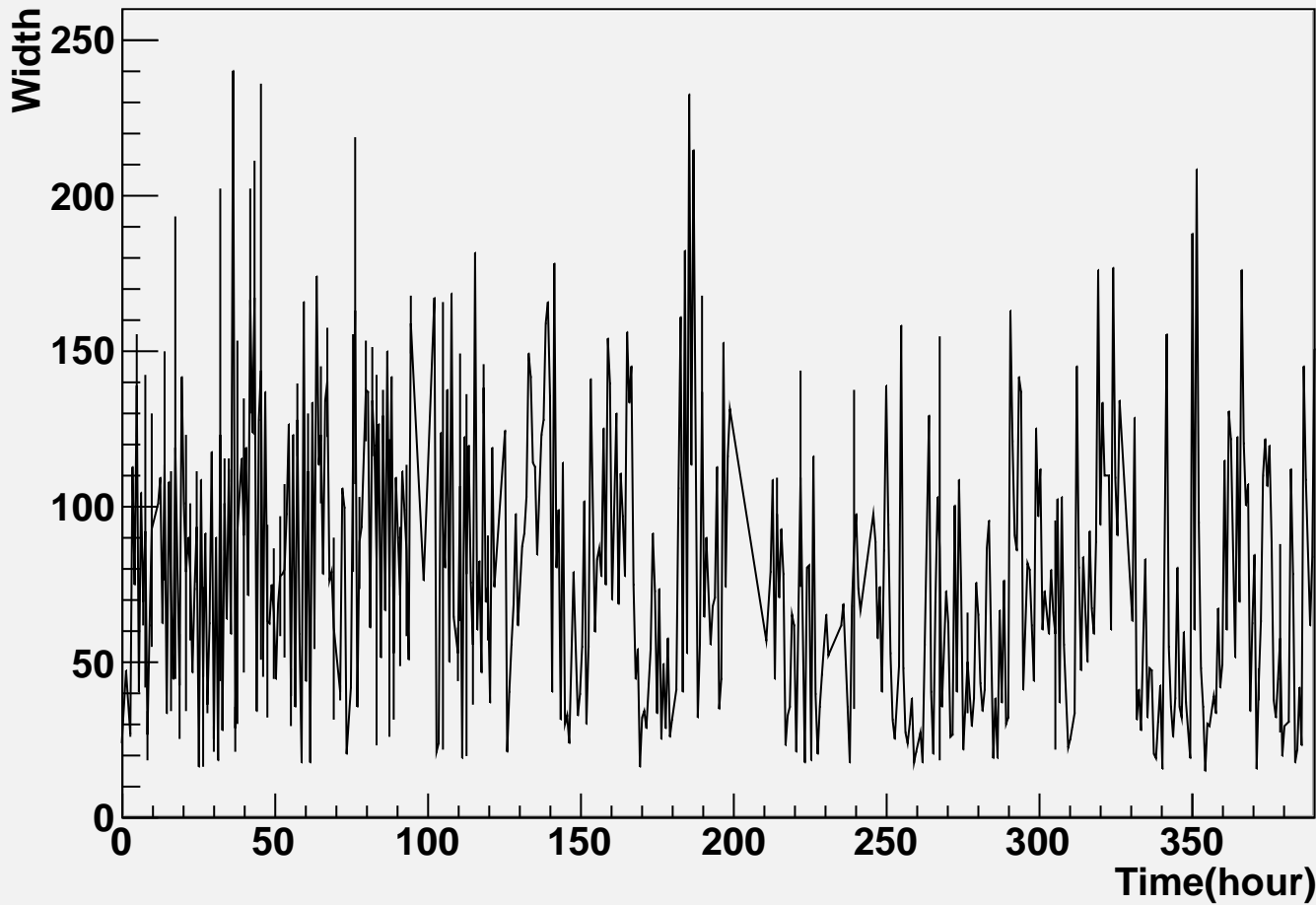
width-S1-SFET2D-Ch9



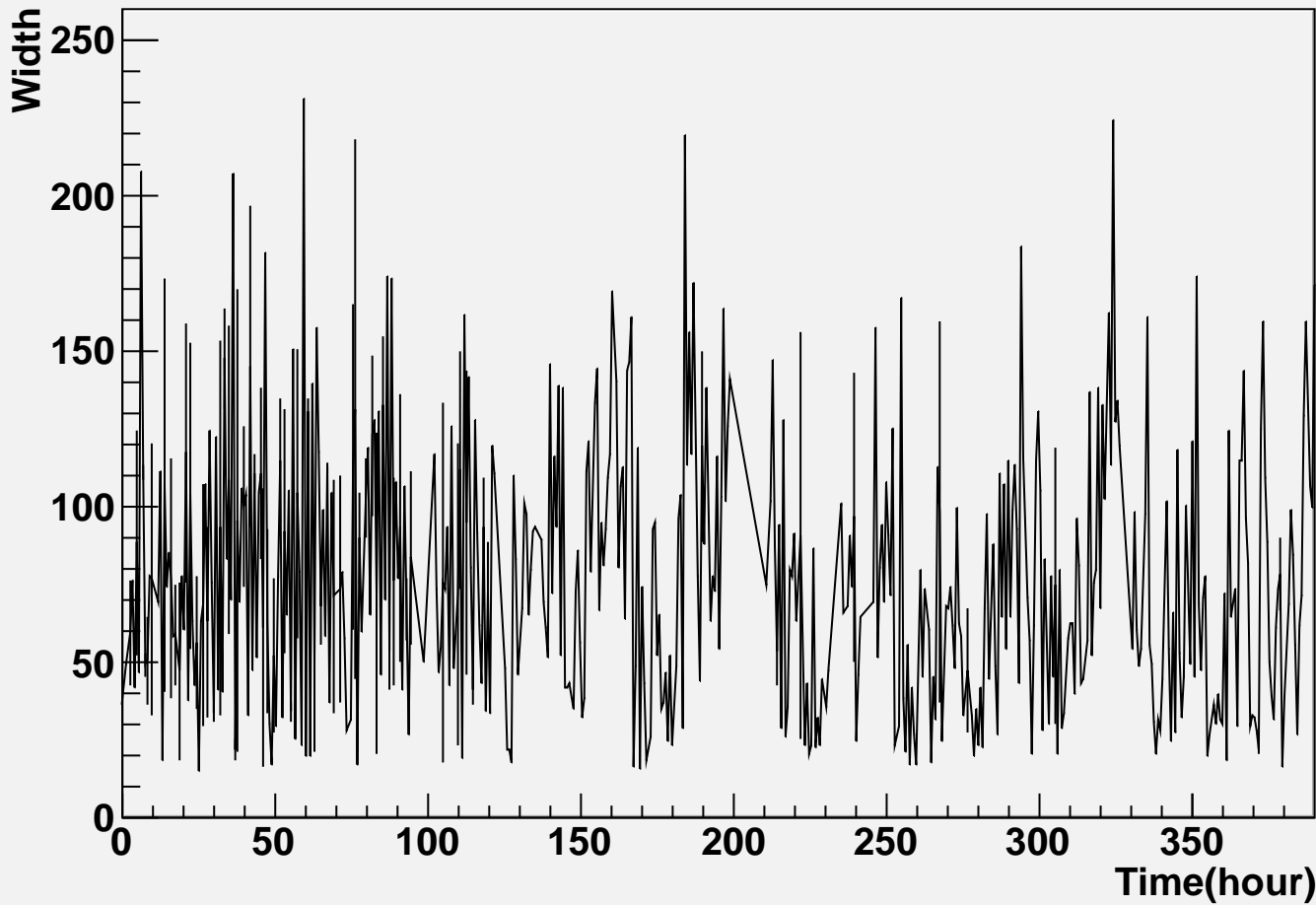
width-S1-SFEA2-Ch0



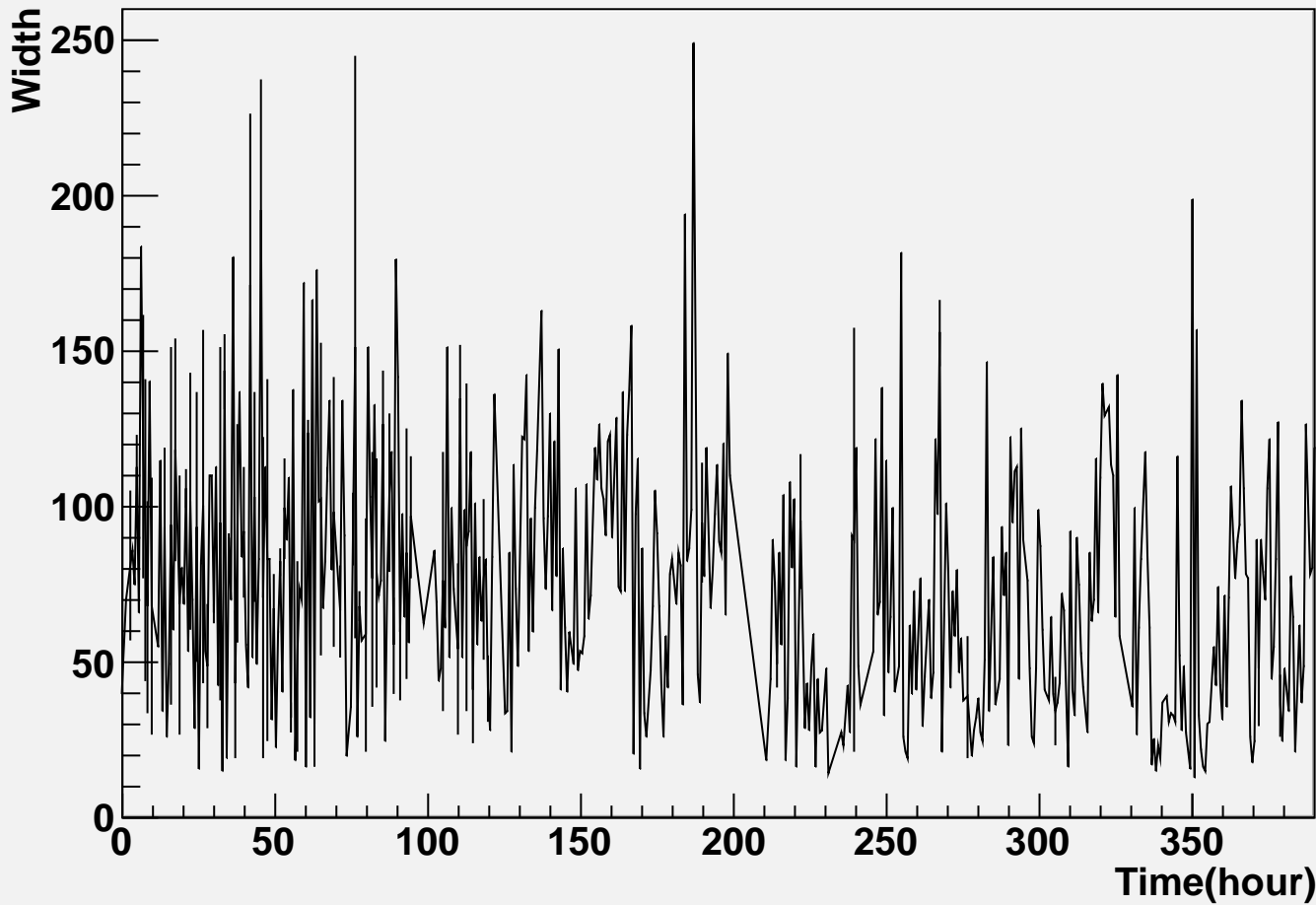
width-S1-SFEA2-Ch1



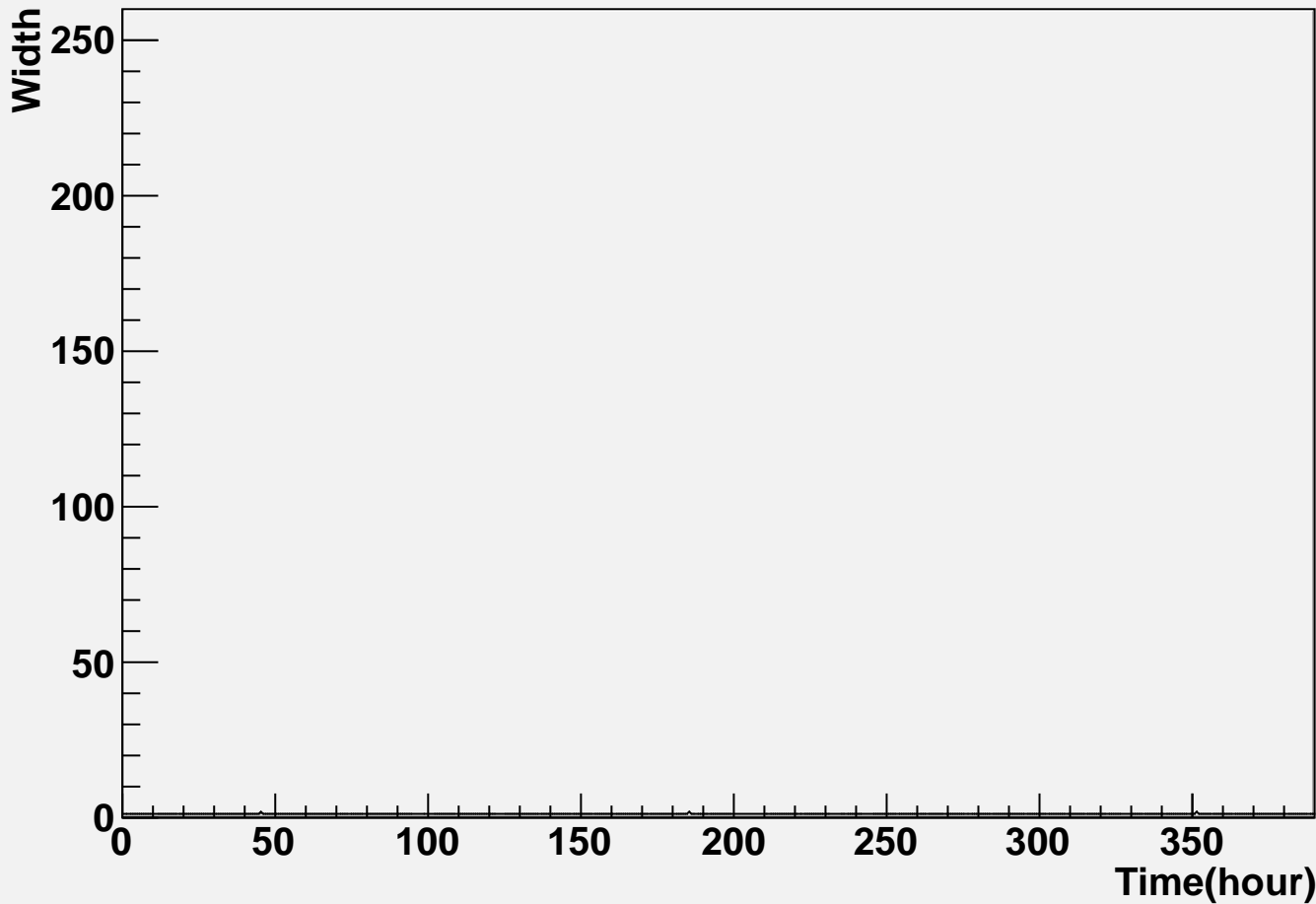
width-S1-SFEA2-Ch2



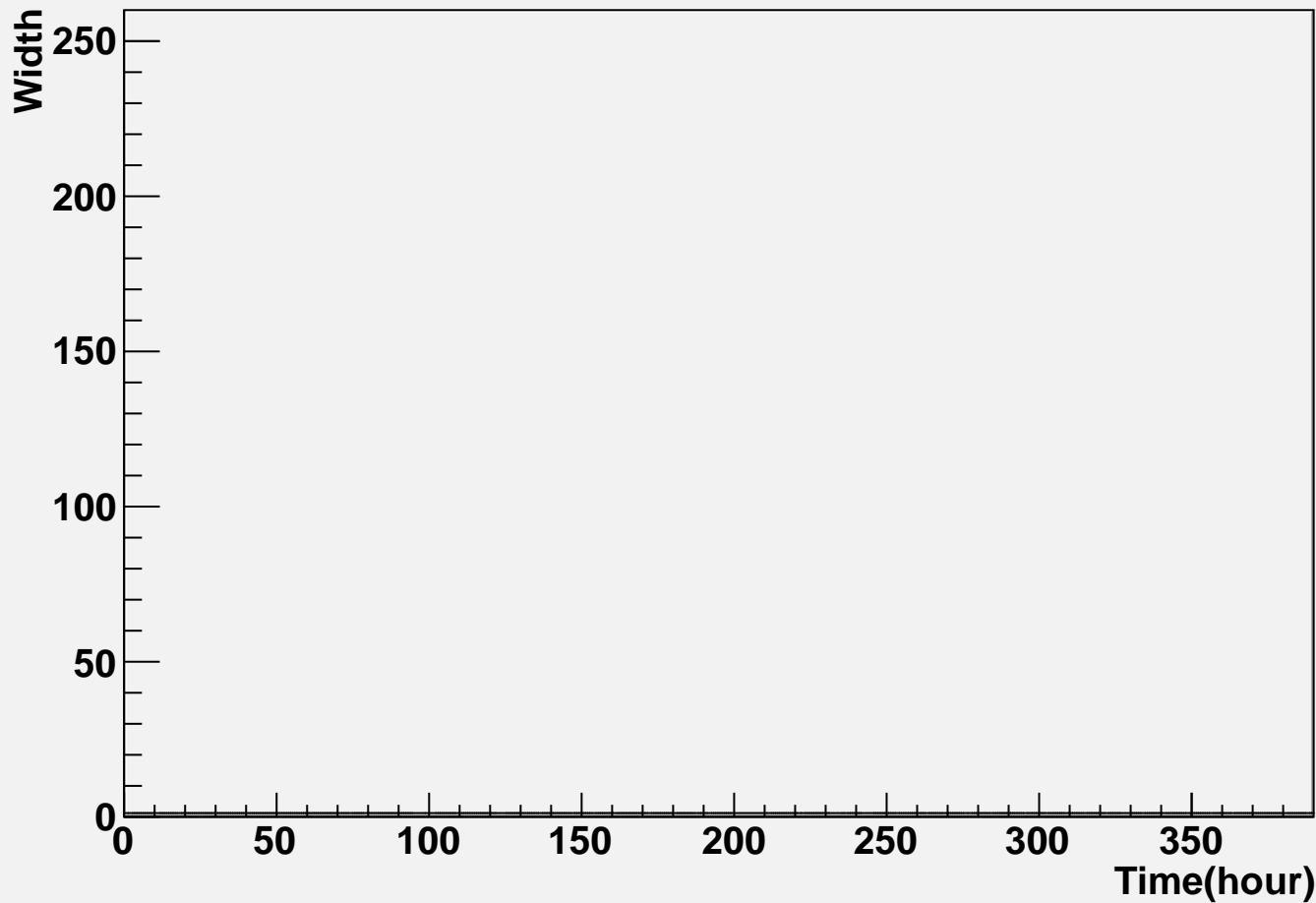
width-S1-SFEA2-Ch3



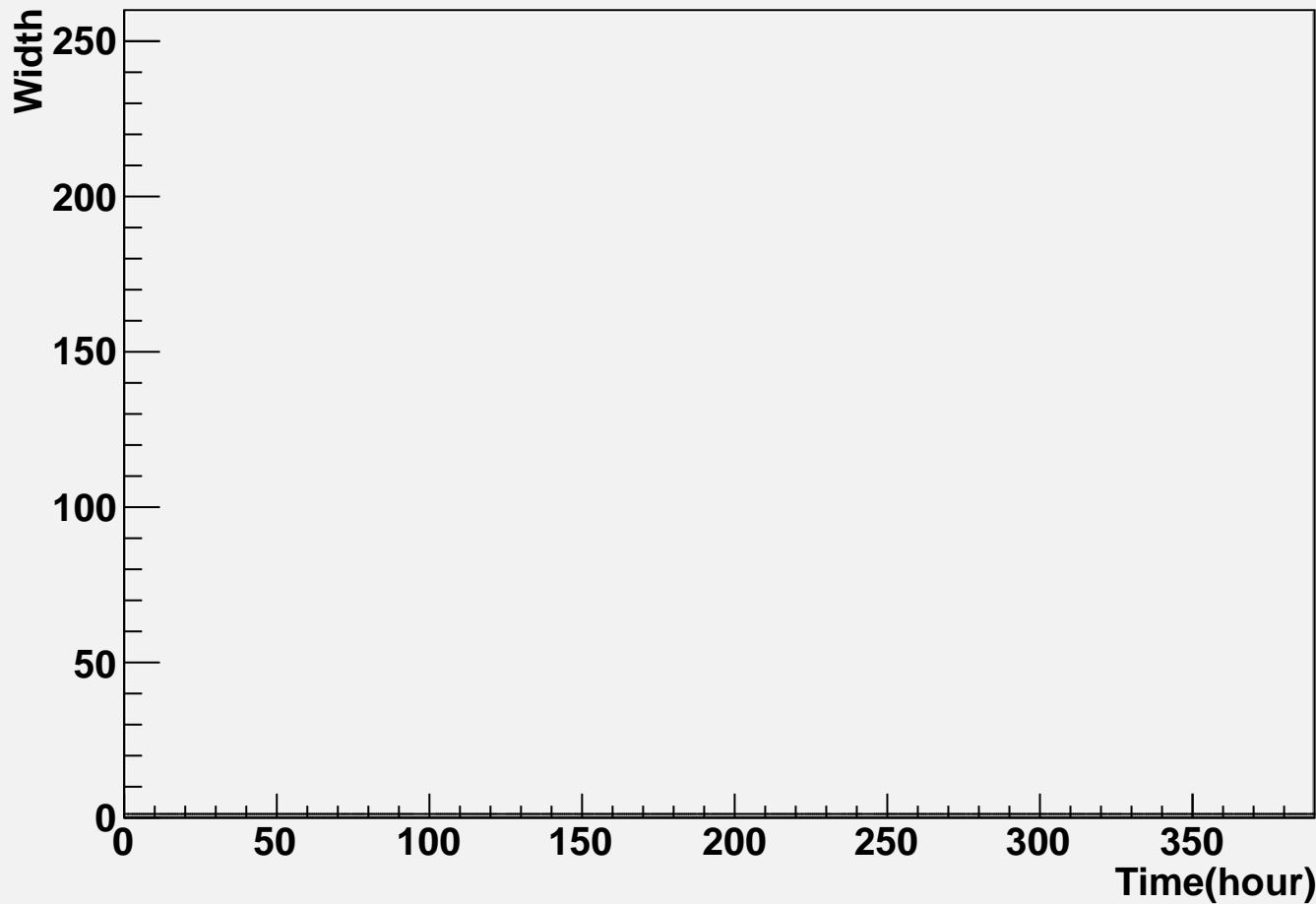
width-S1-SFEA2-Ch4



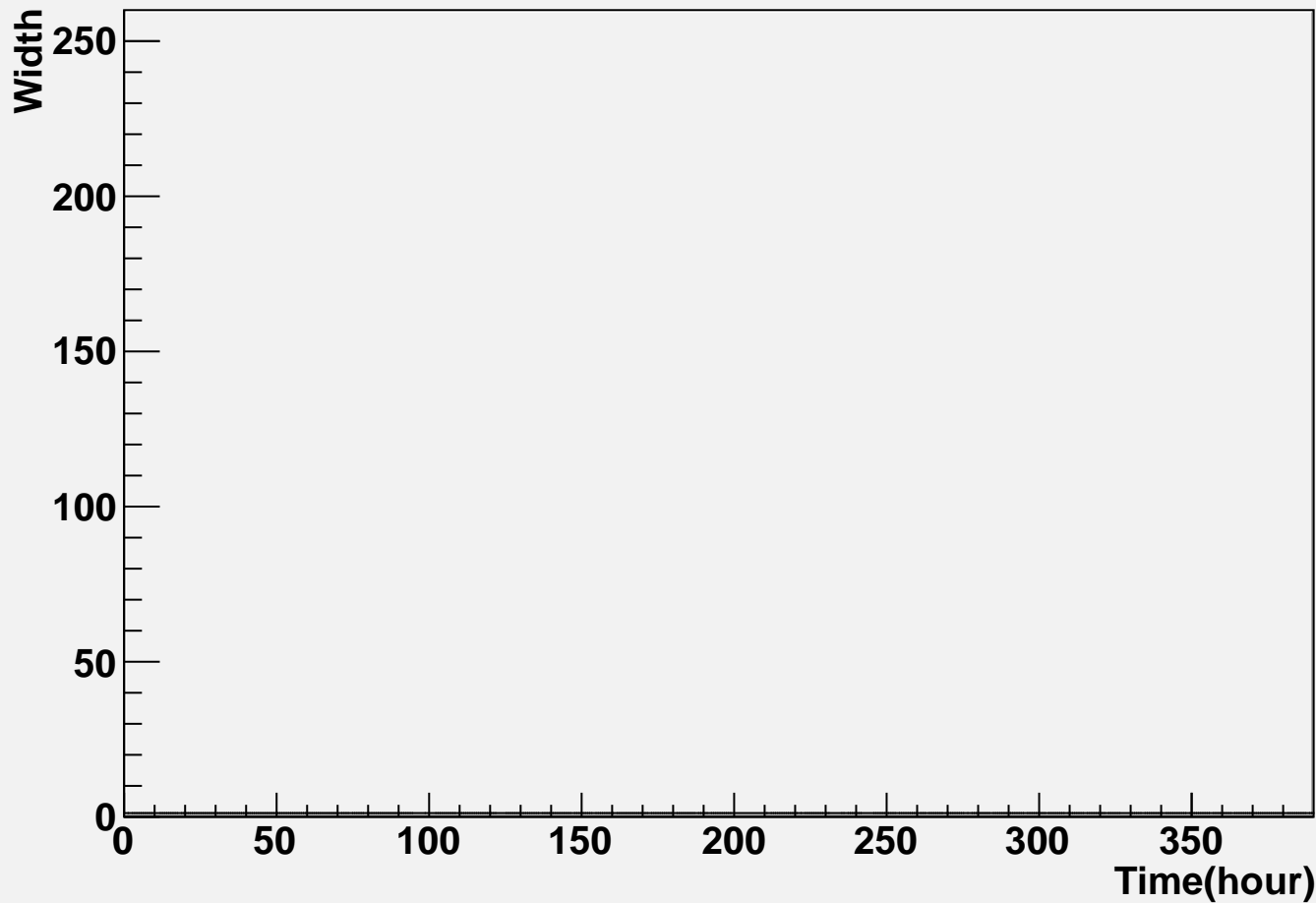
width-S1-SFEA2-Ch5



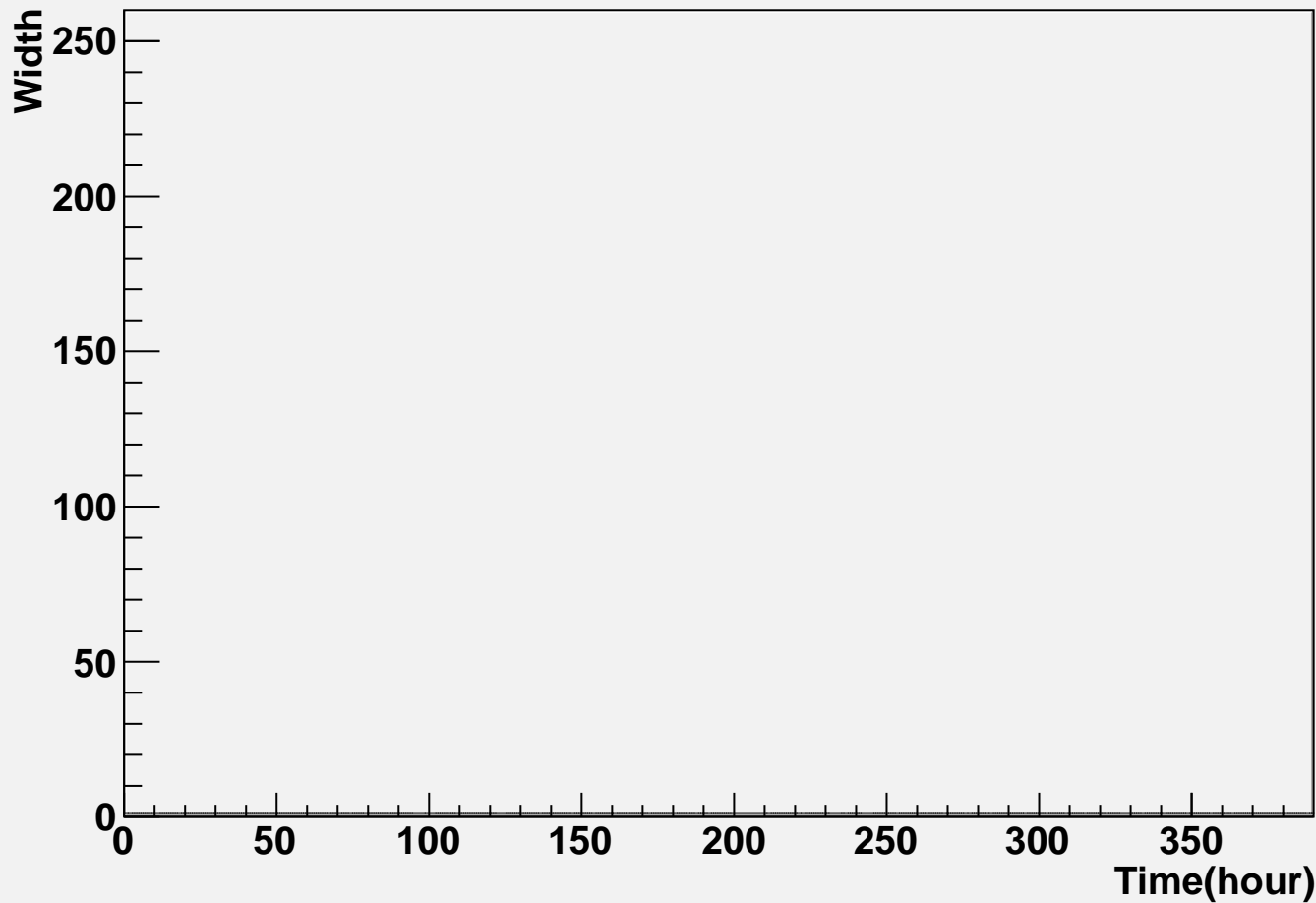
width-S1-SFEA2-Ch6



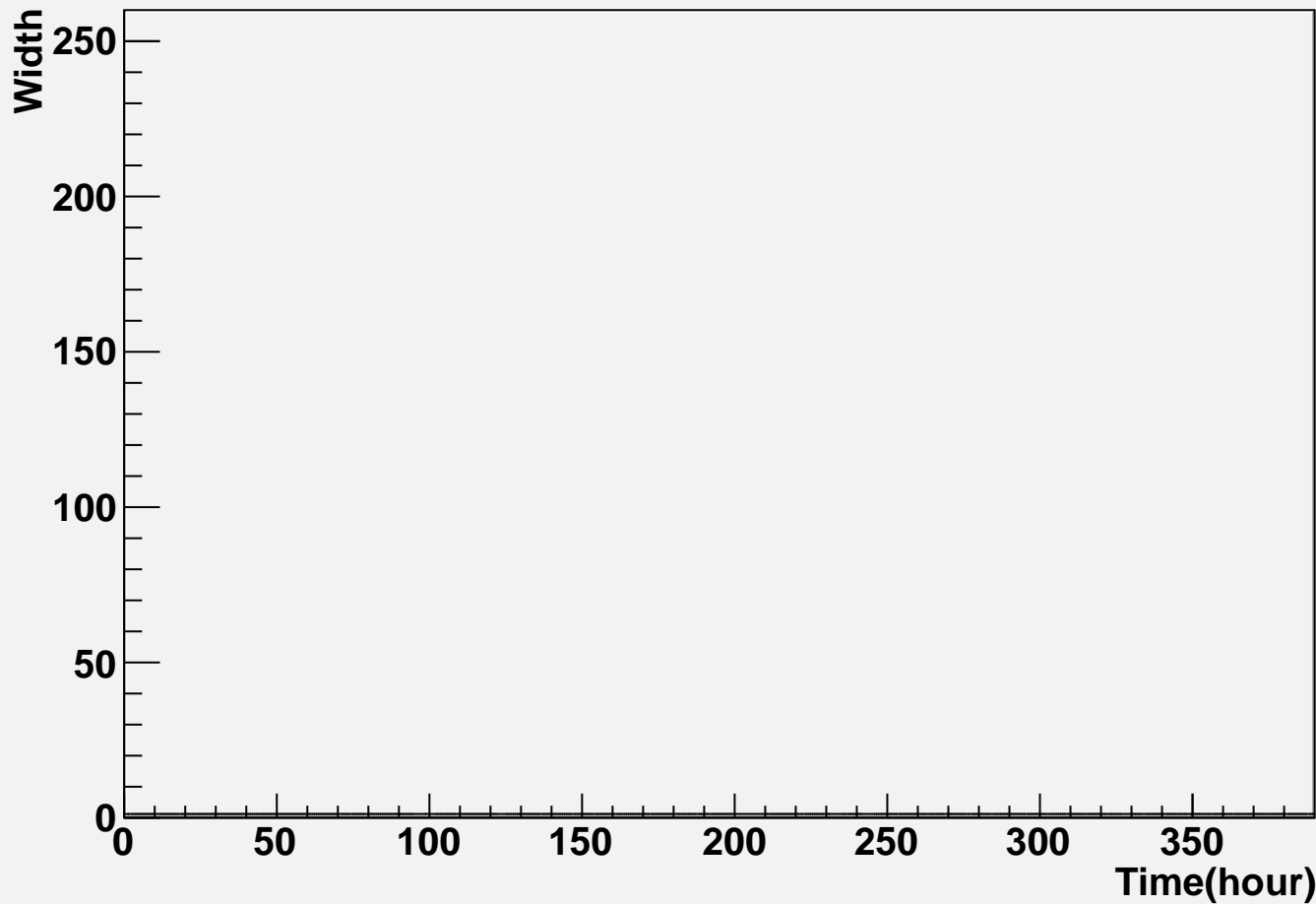
width-S1-SFEA2-Ch7



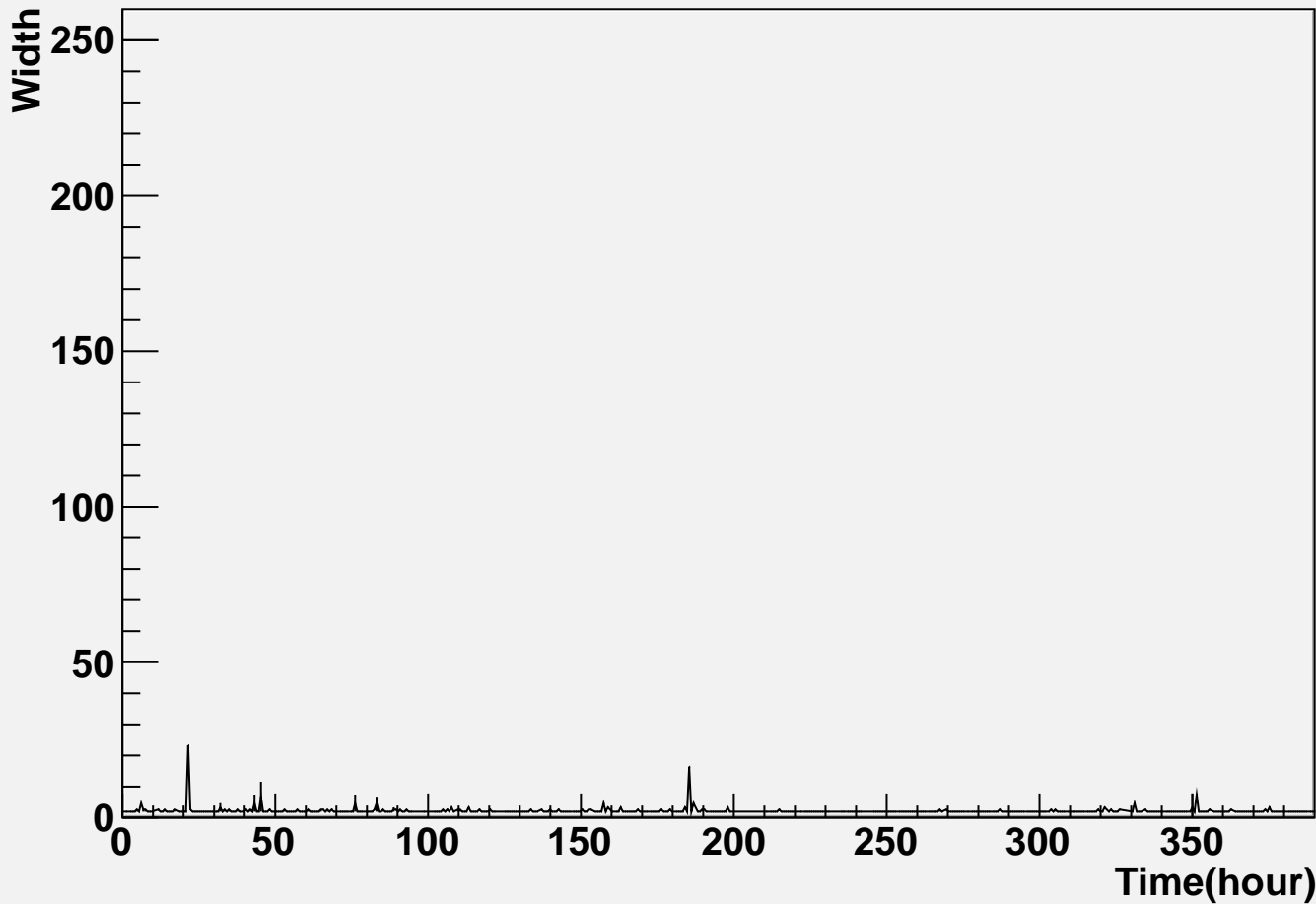
width-S1-SFEA2-Ch8



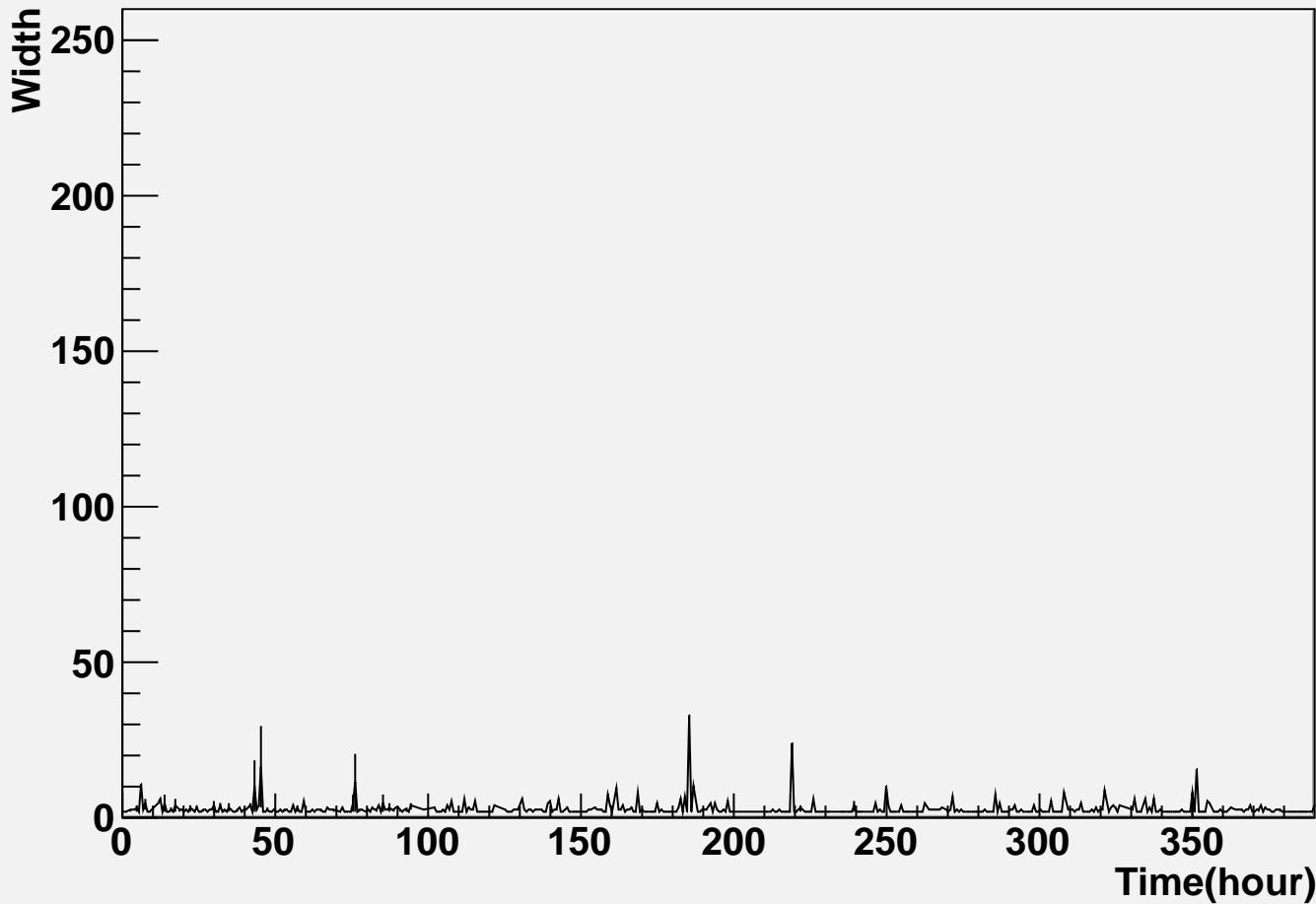
width-S1-SFEA2-Ch9



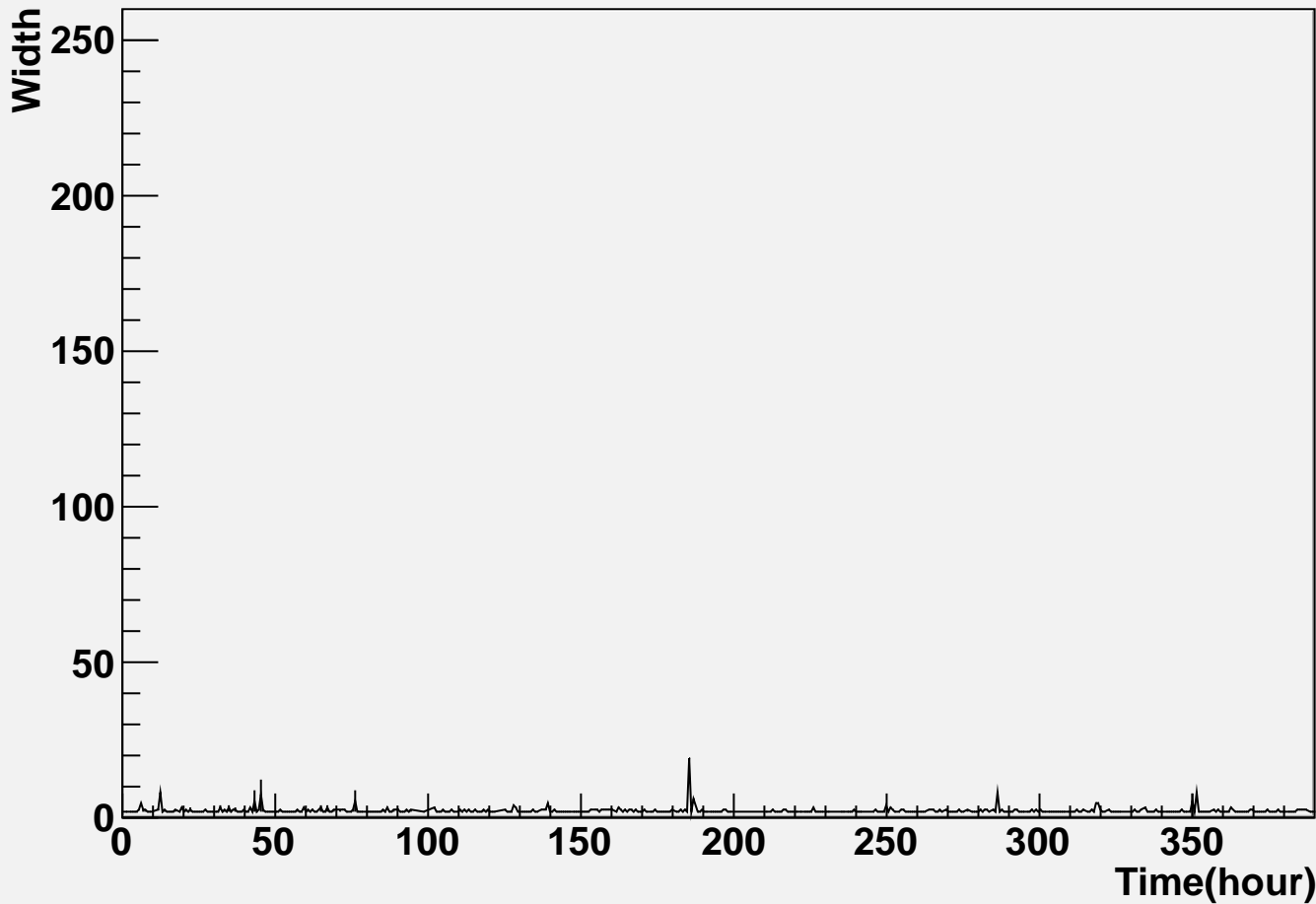
width-S1-SFEC00-Ch0



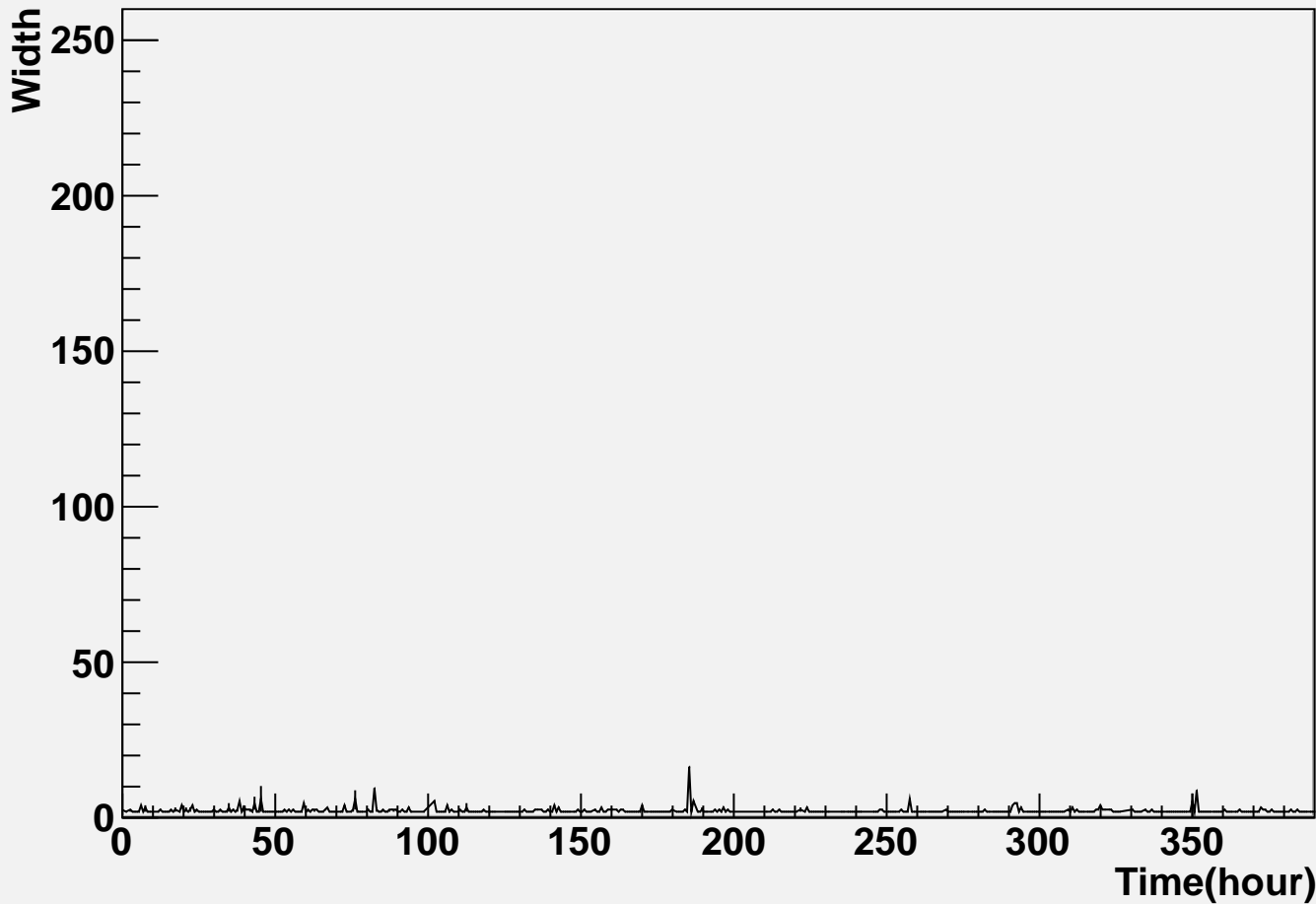
width-S1-SFEC00-Ch1



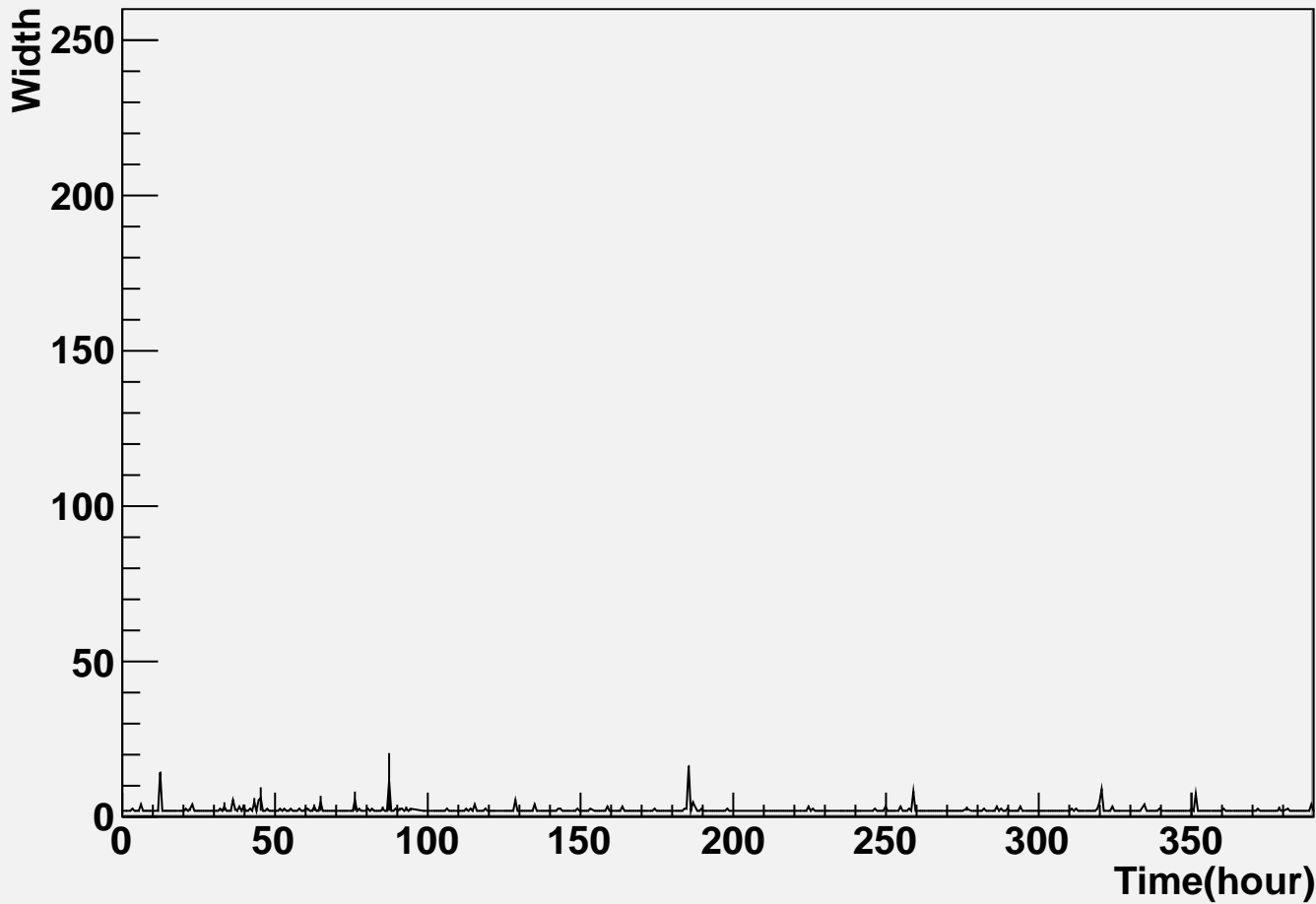
width-S1-SFEC00-Ch2



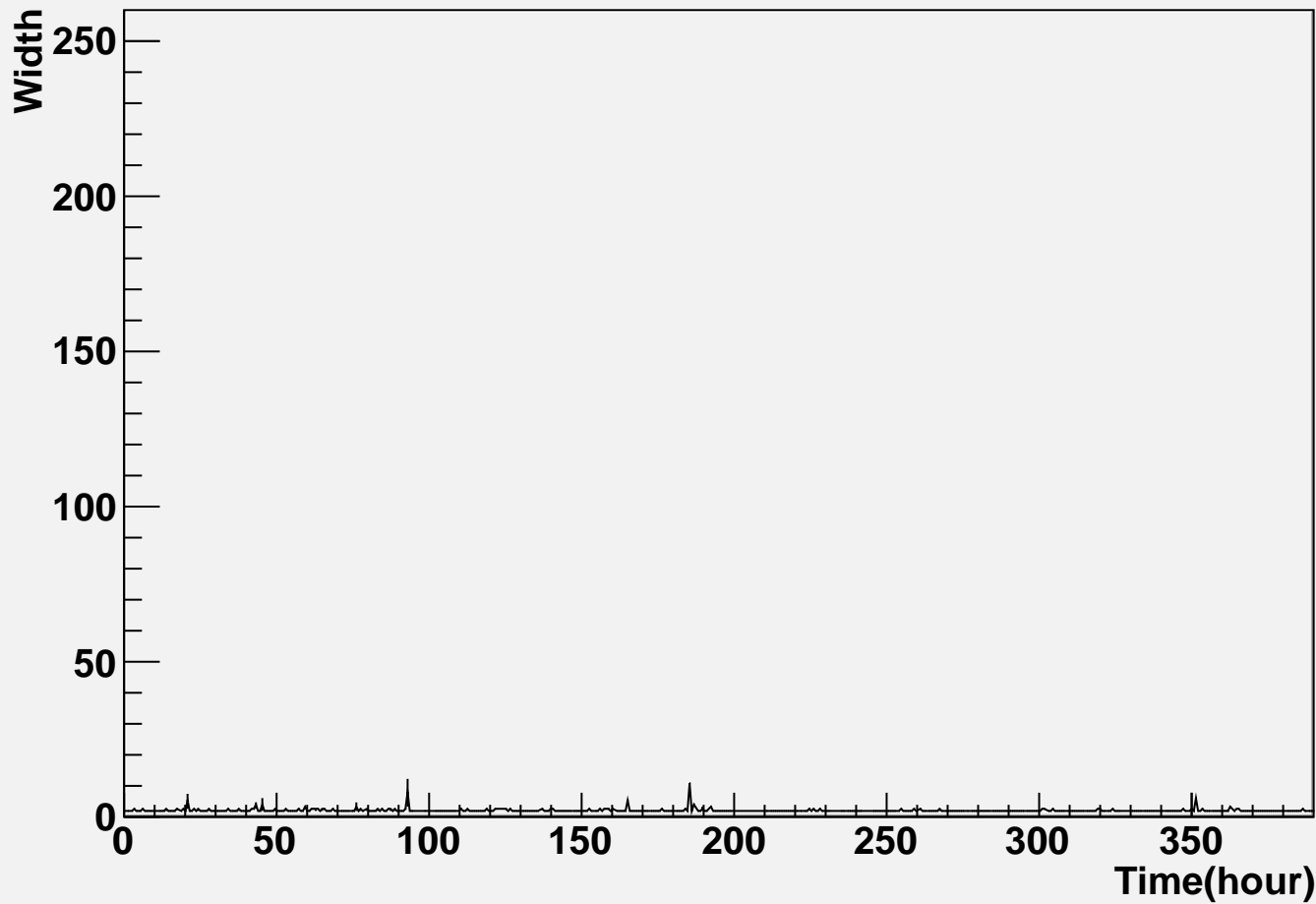
width-S1-SFEC00-Ch3



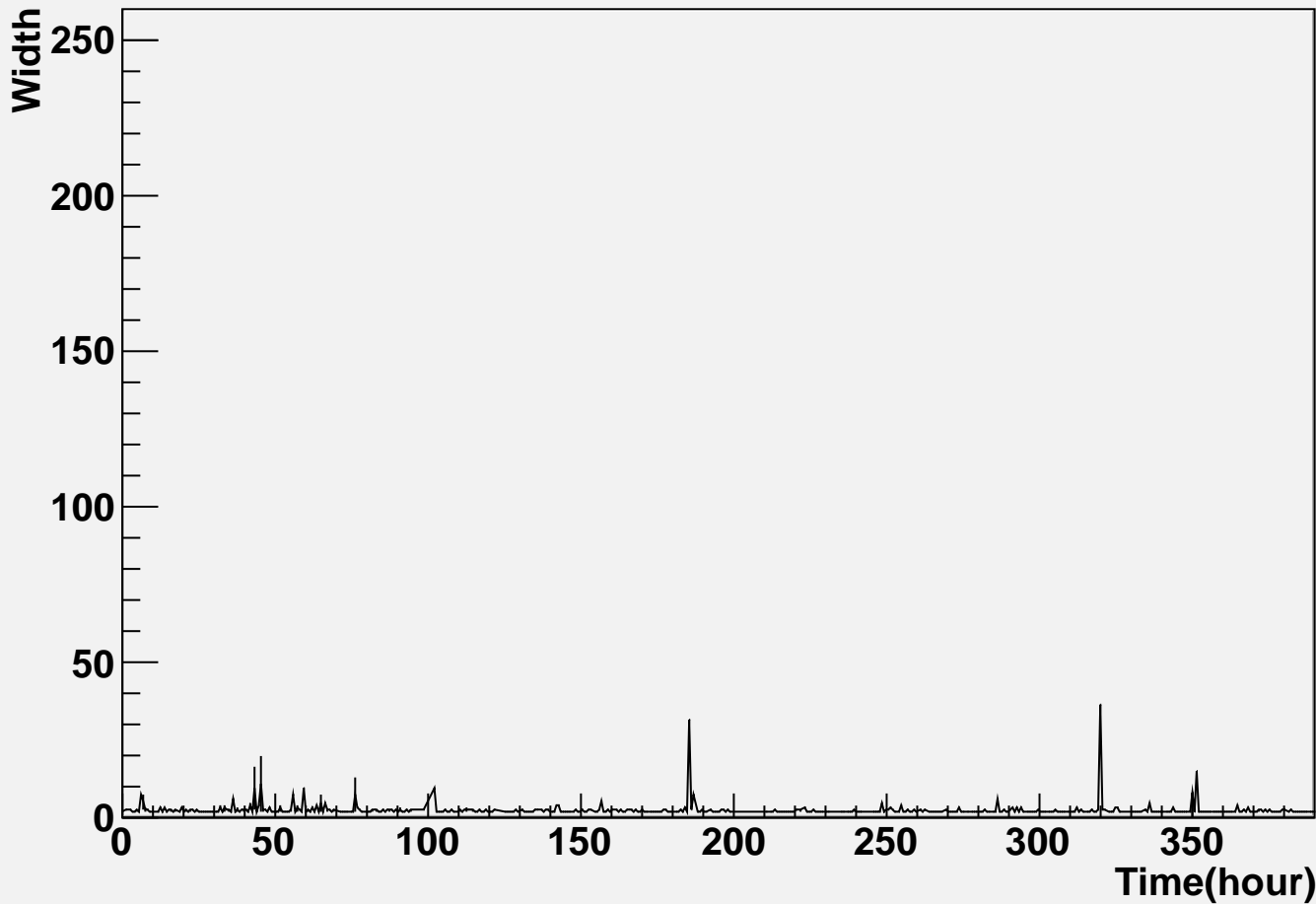
width-S1-SFEC00-Ch4



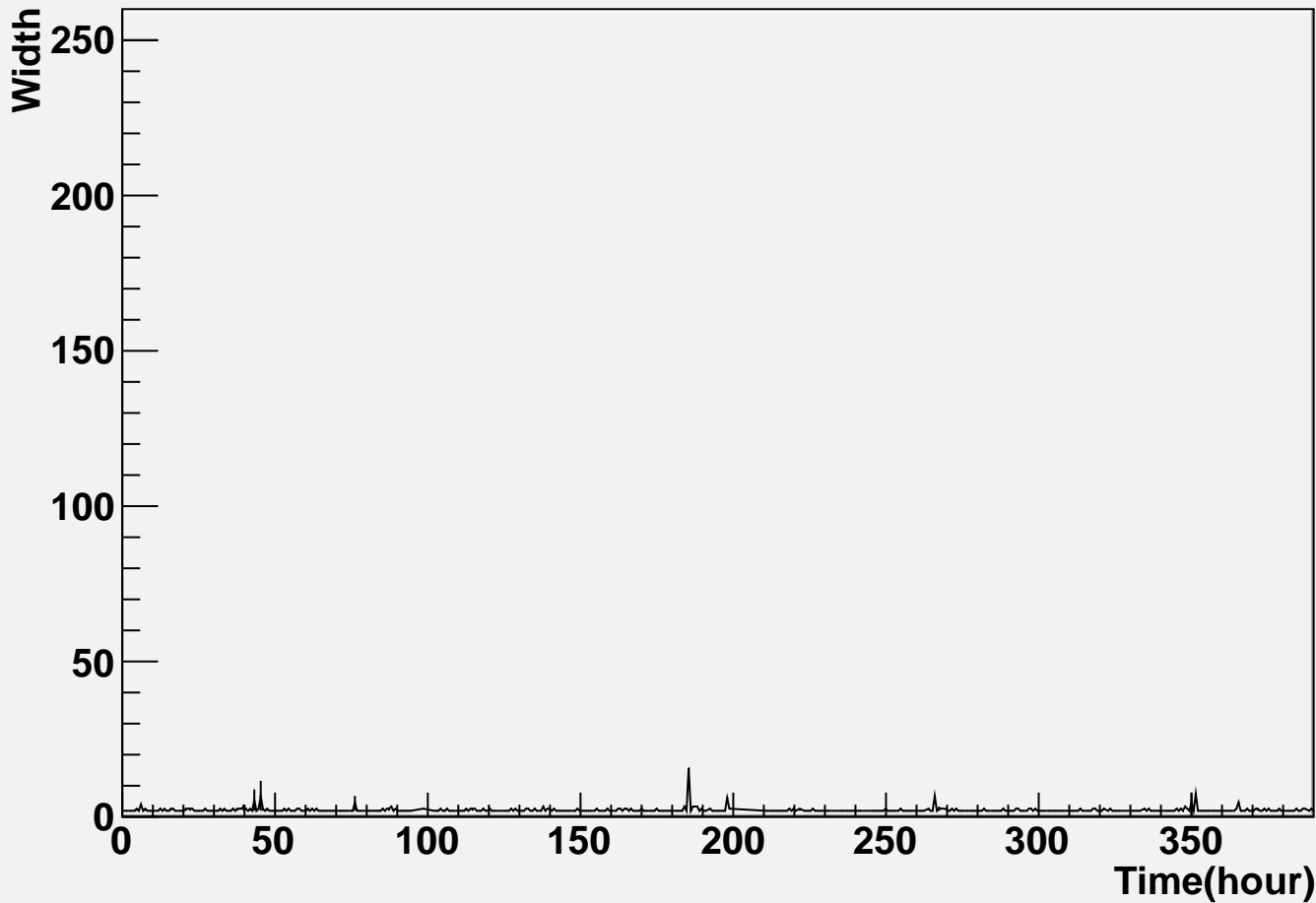
width-S1-SFEC00-Ch5



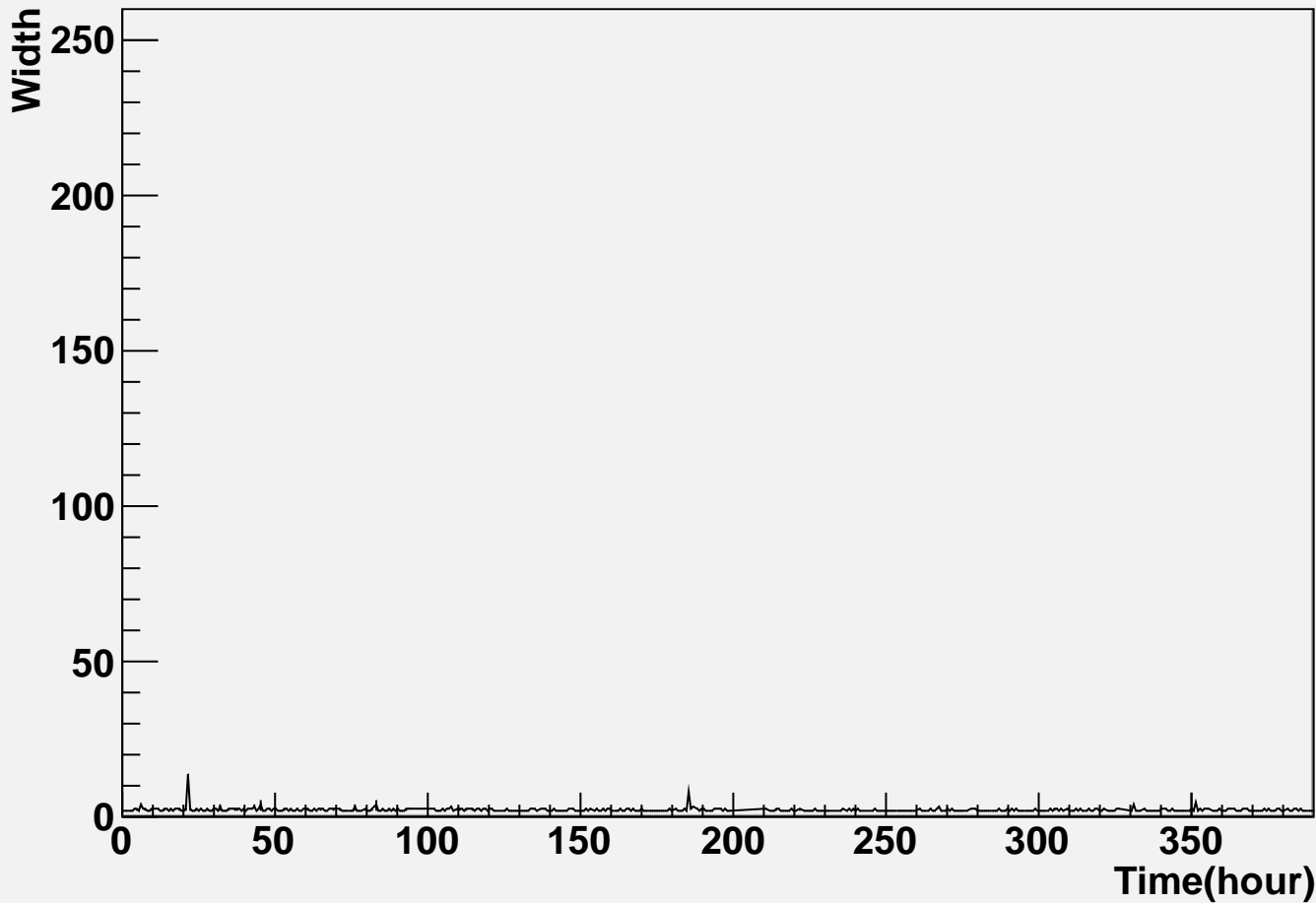
width-S1-SFEC00-Ch6



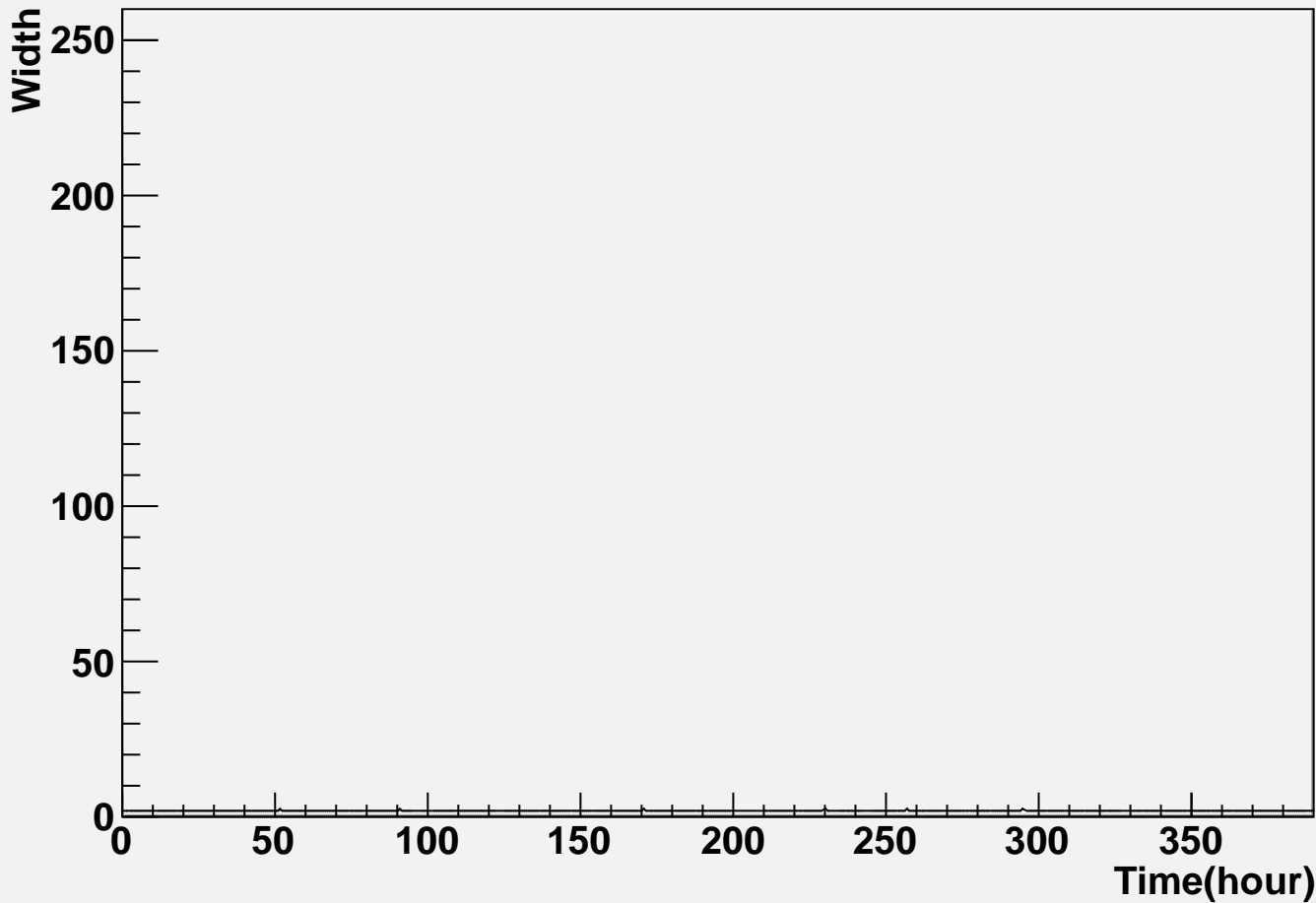
width-S1-SFEC00-Ch7



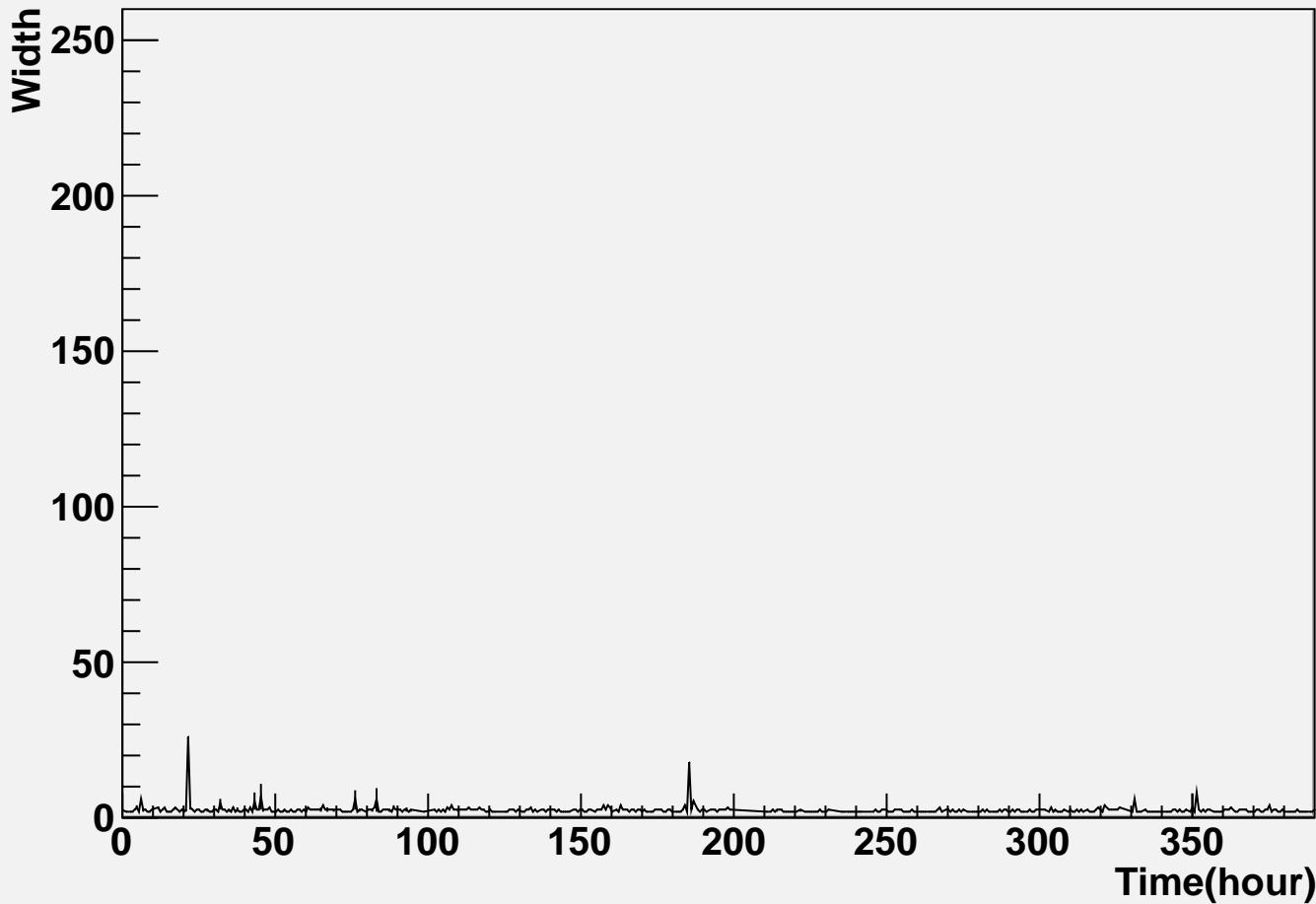
width-S1-SFEC00-Ch8



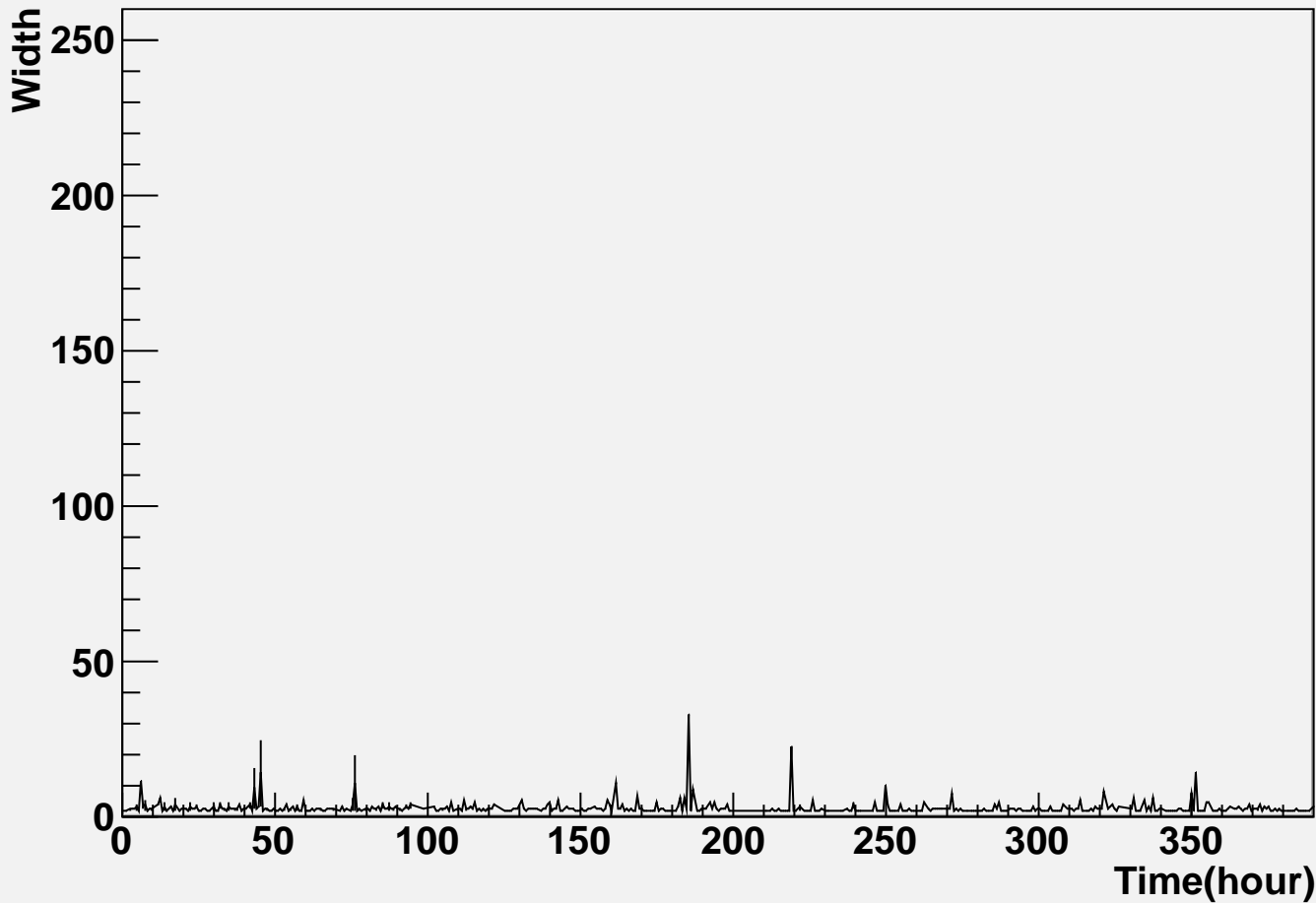
width-S1-SFEC00-Ch9



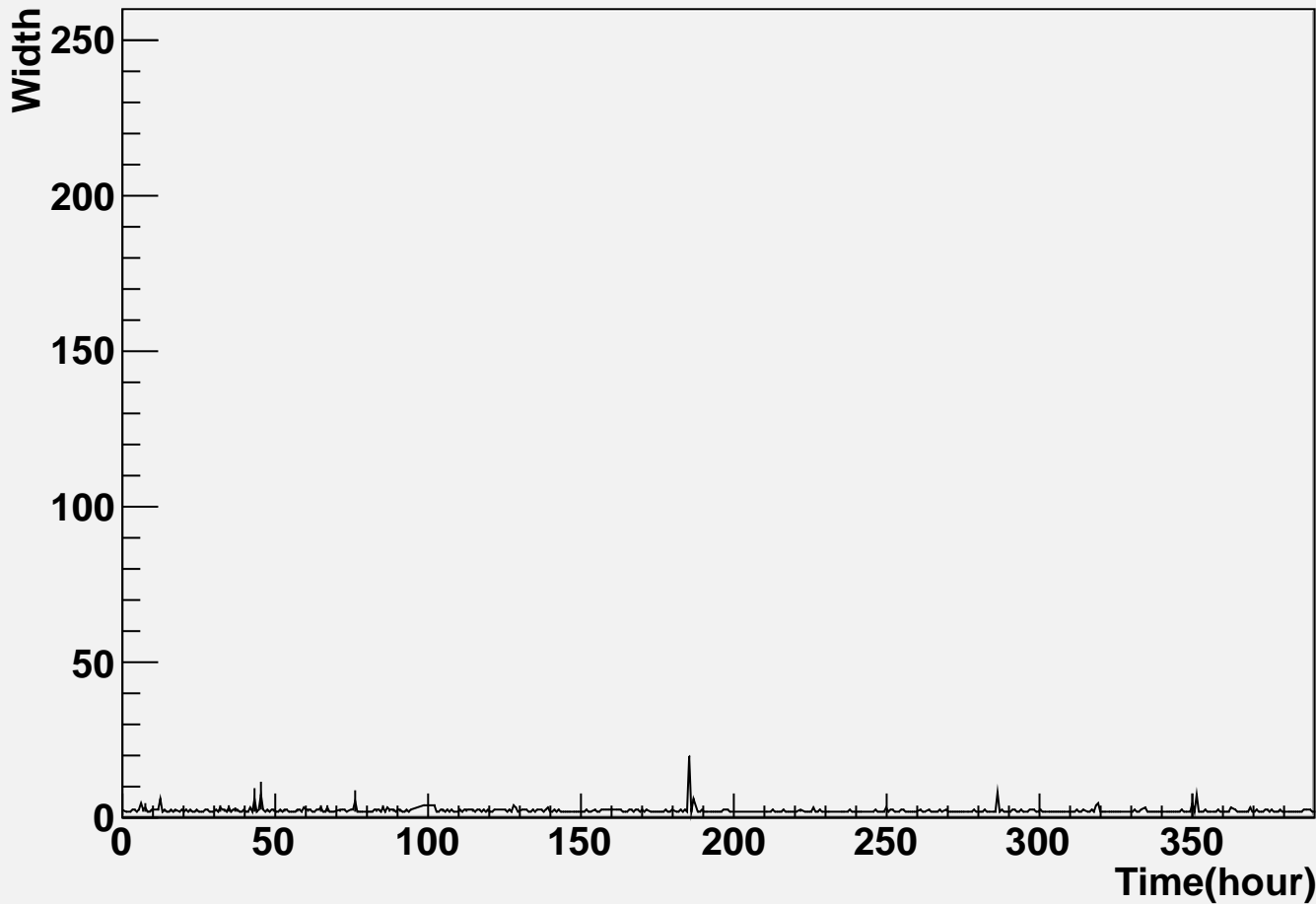
width-S1-SFEC01-Ch0



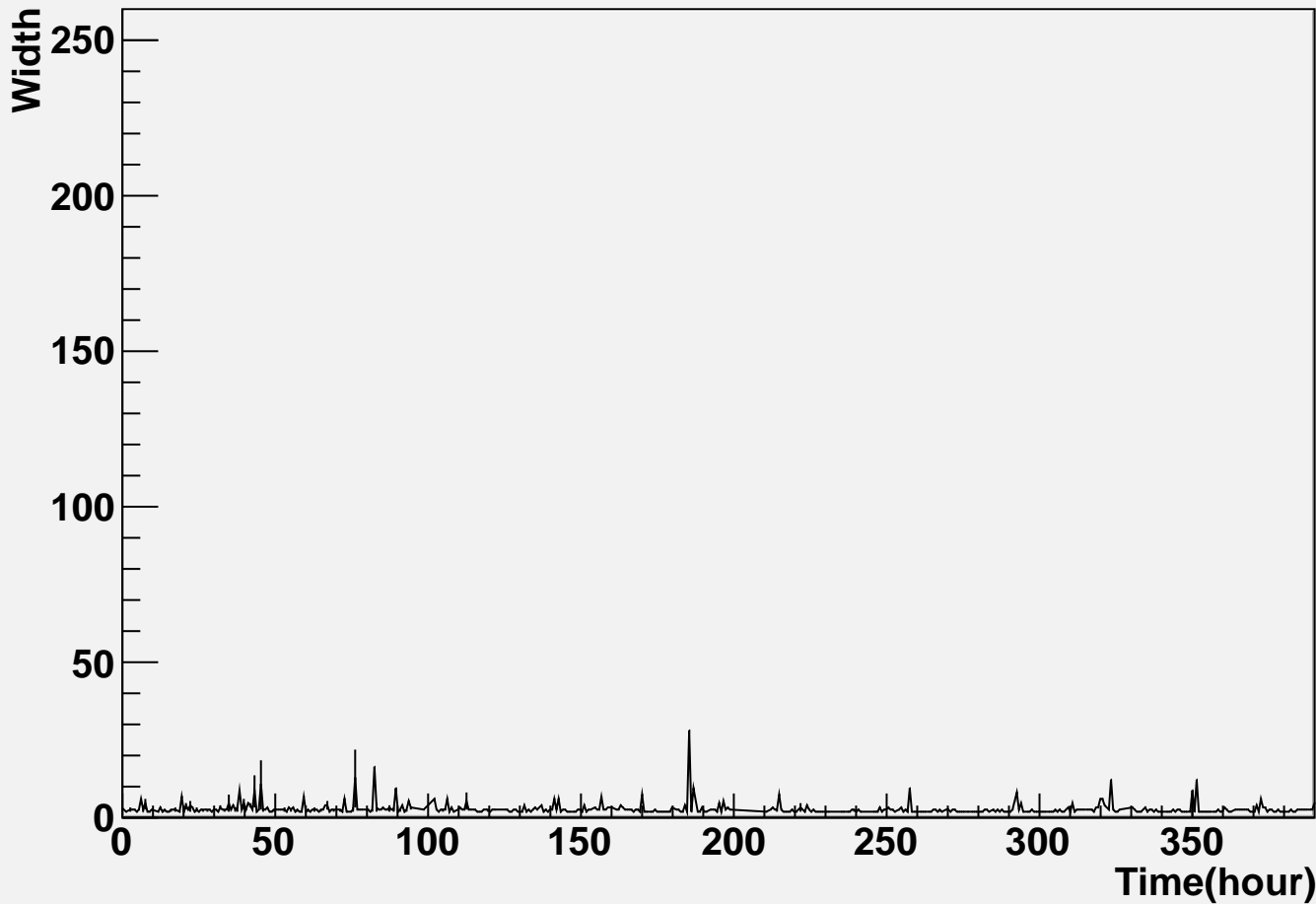
width-S1-SFEC01-Ch1



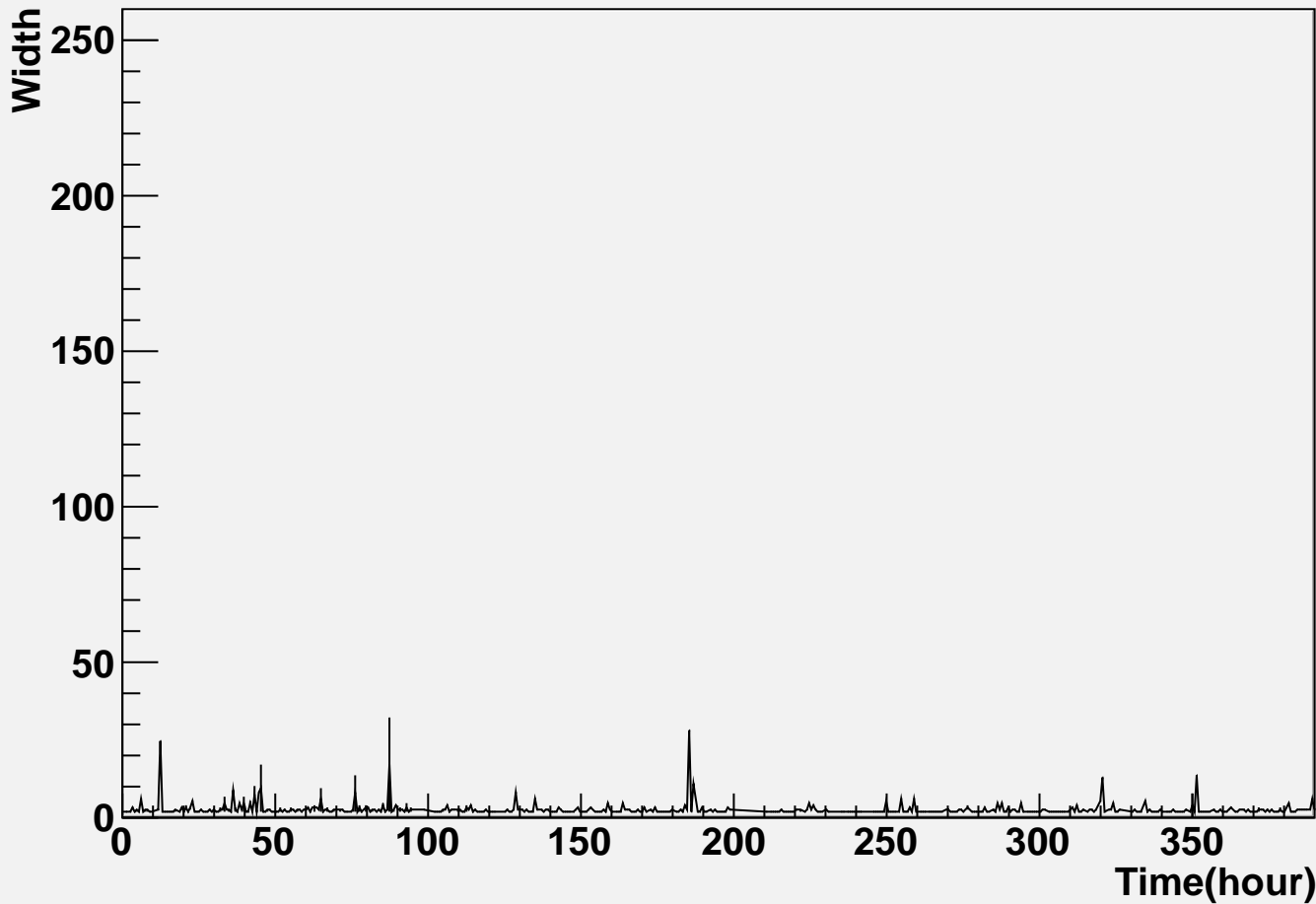
width-S1-SFEC01-Ch2



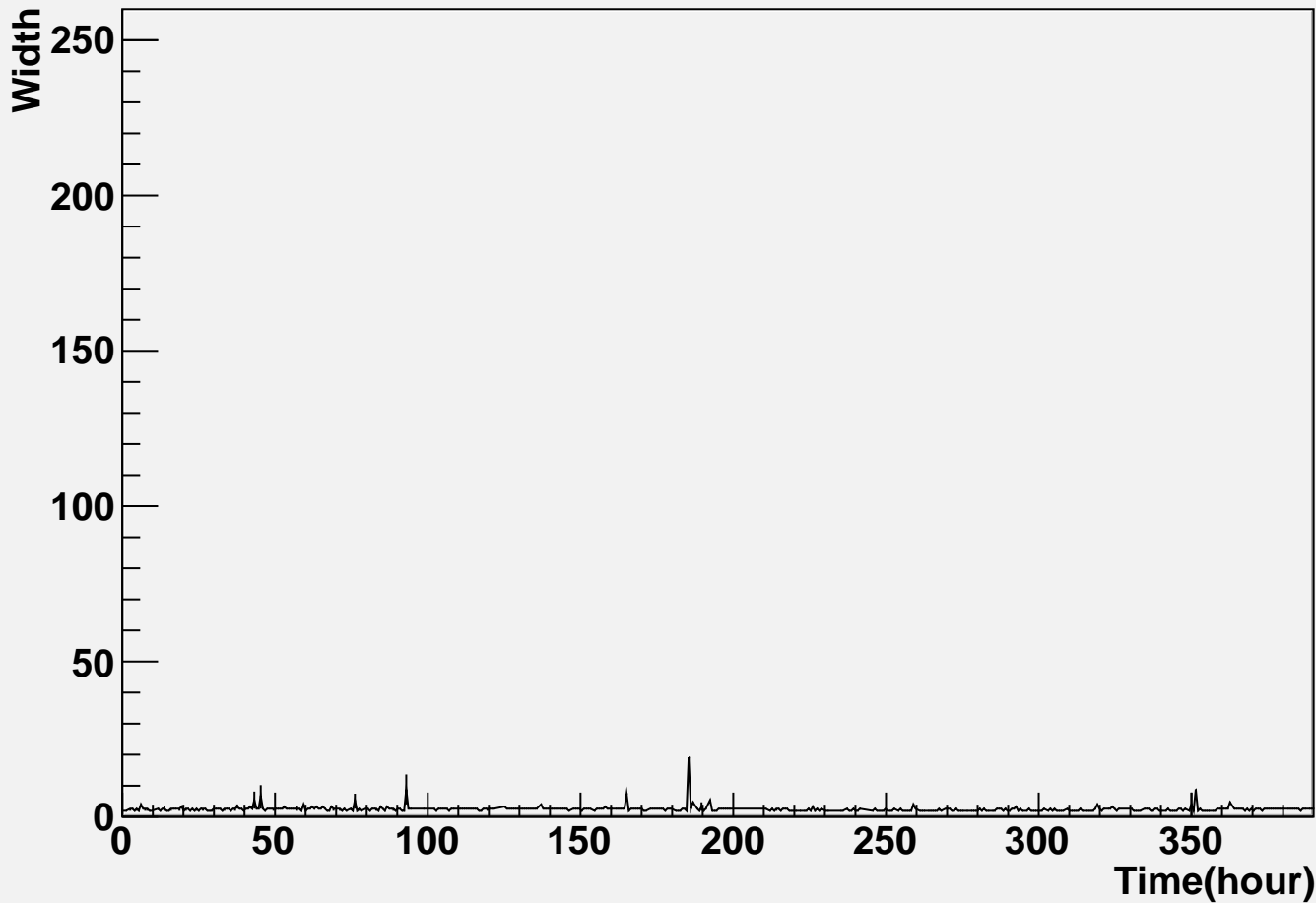
width-S1-SFEC01-Ch3



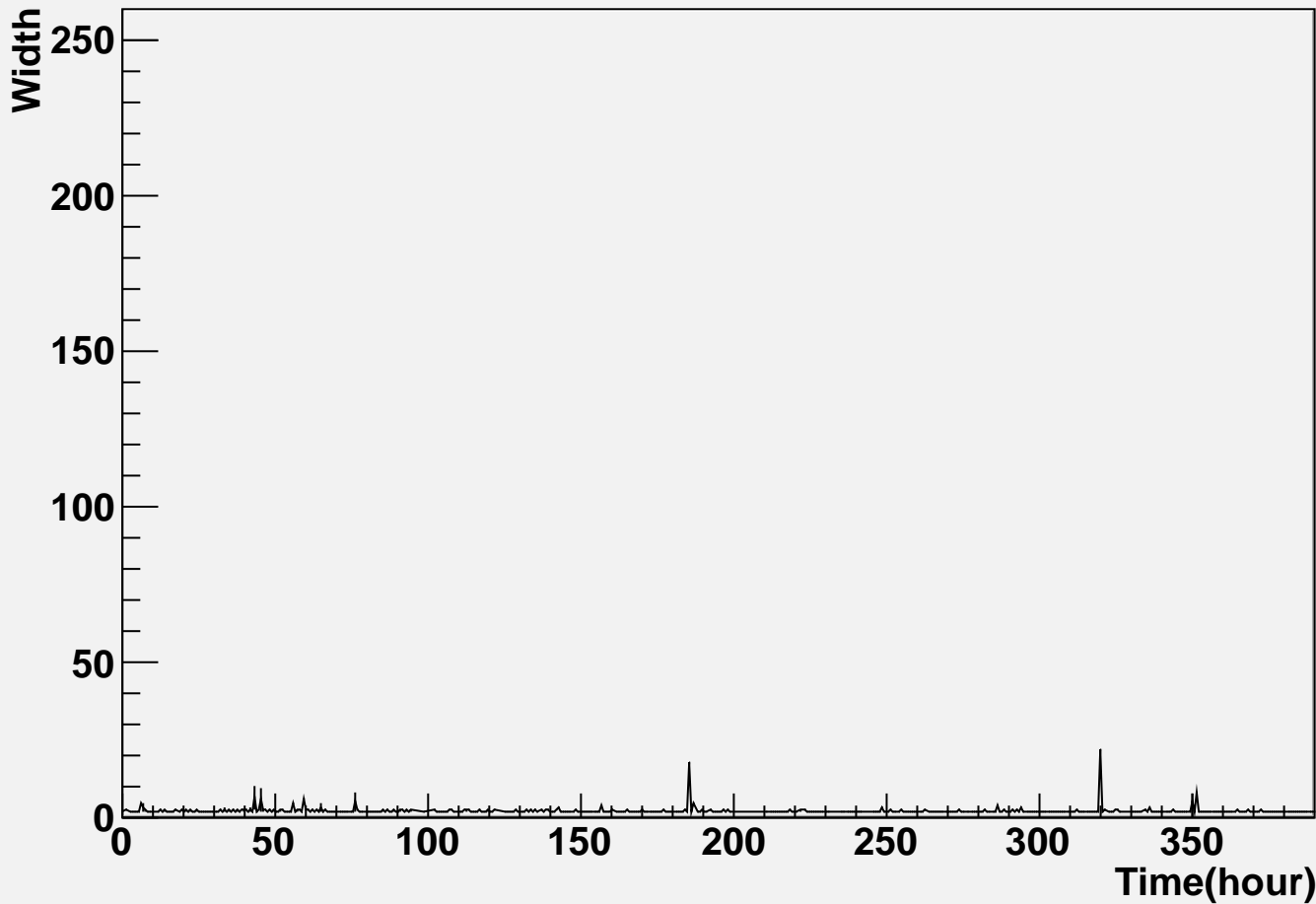
width-S1-SFEC01-Ch4



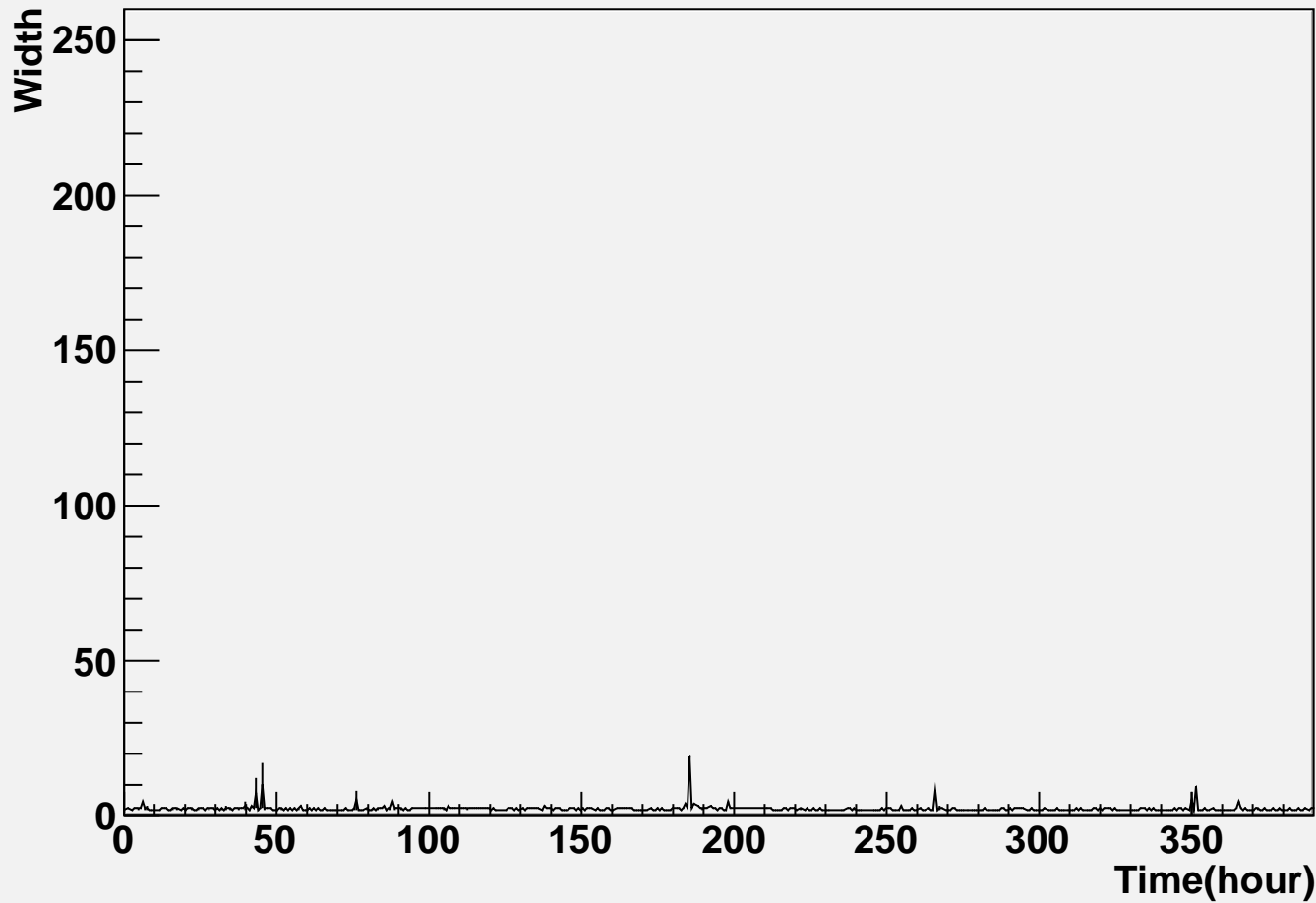
width-S1-SFEC01-Ch5



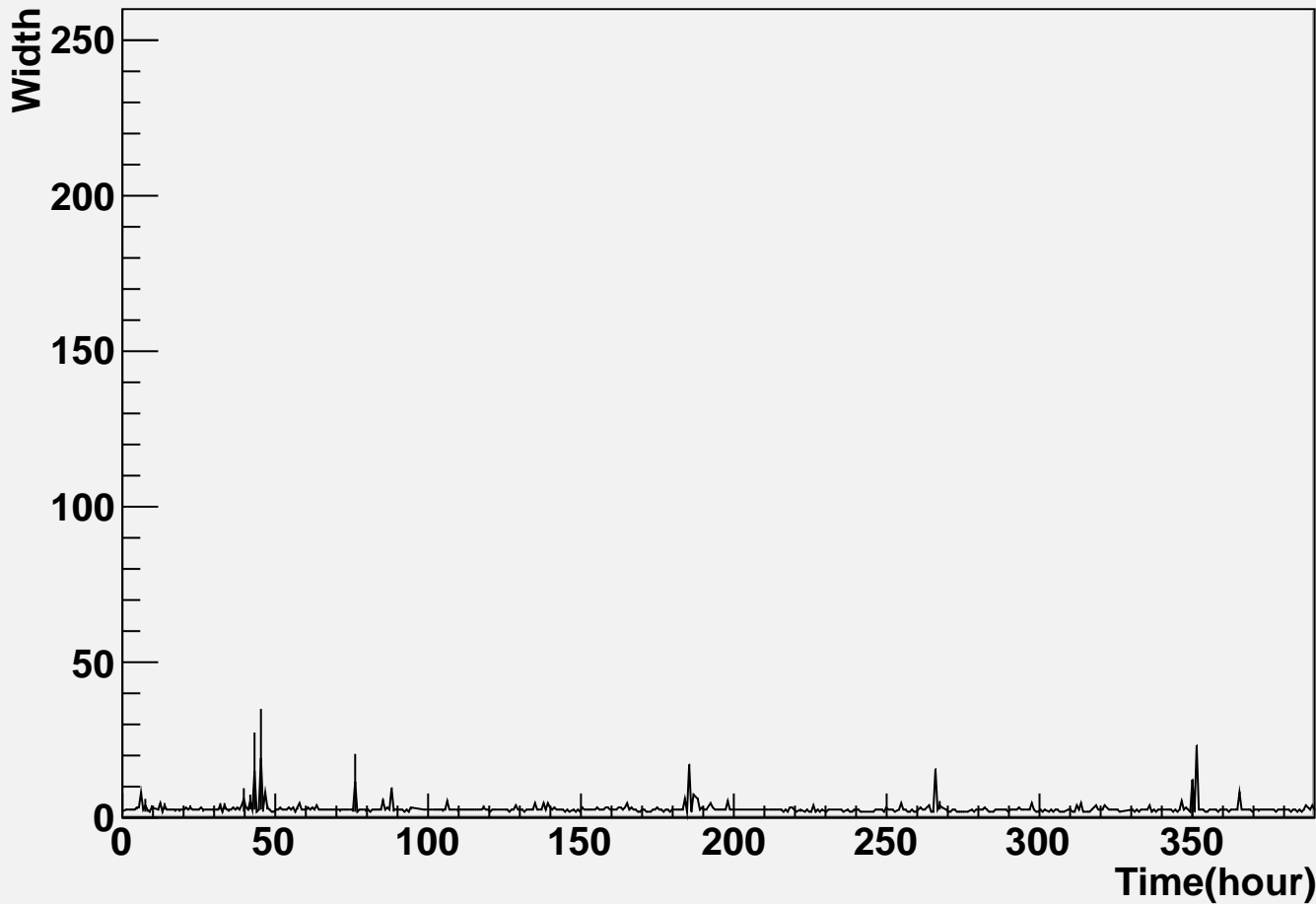
width-S1-SFEC01-Ch6



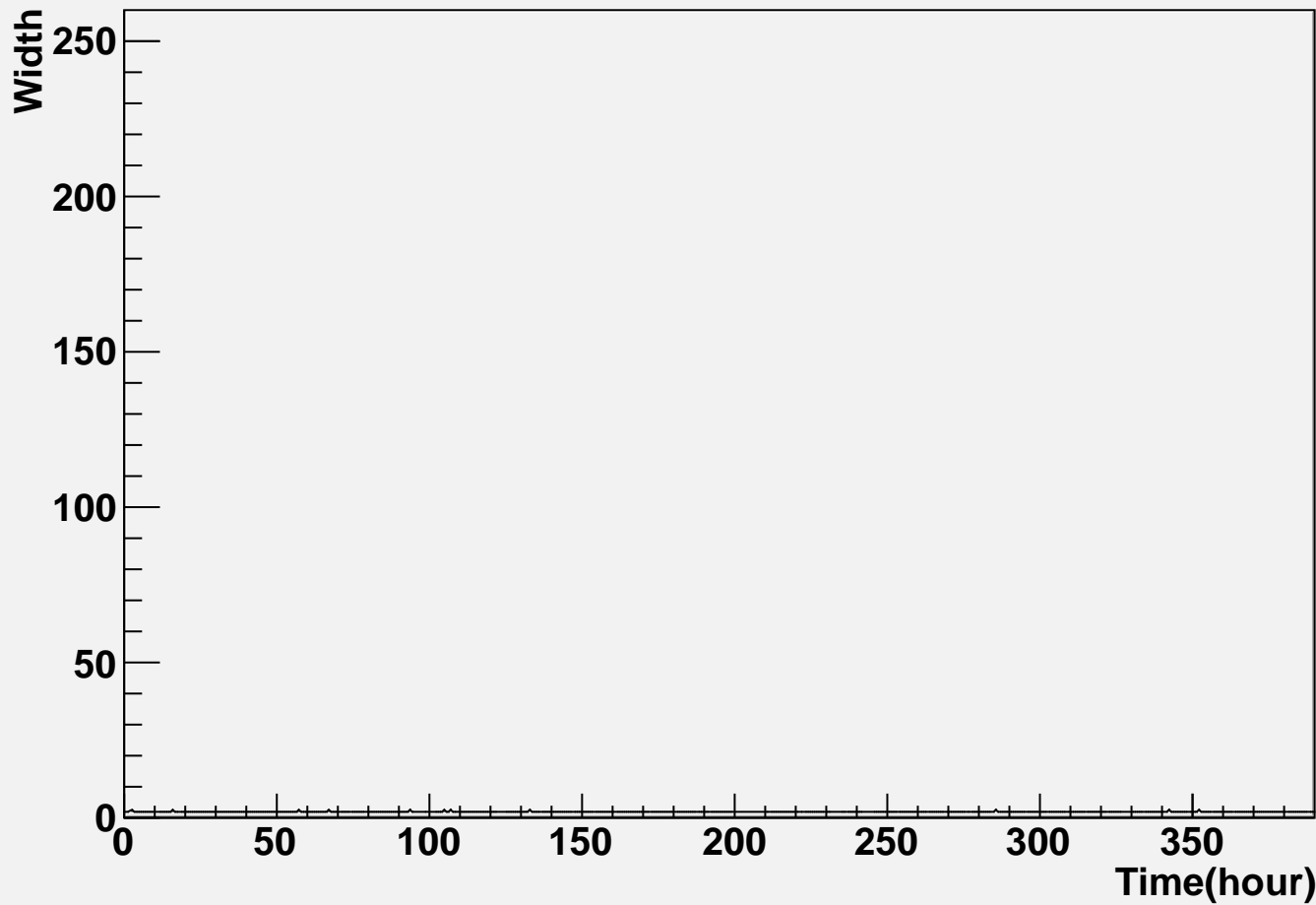
width-S1-SFEC01-Ch7



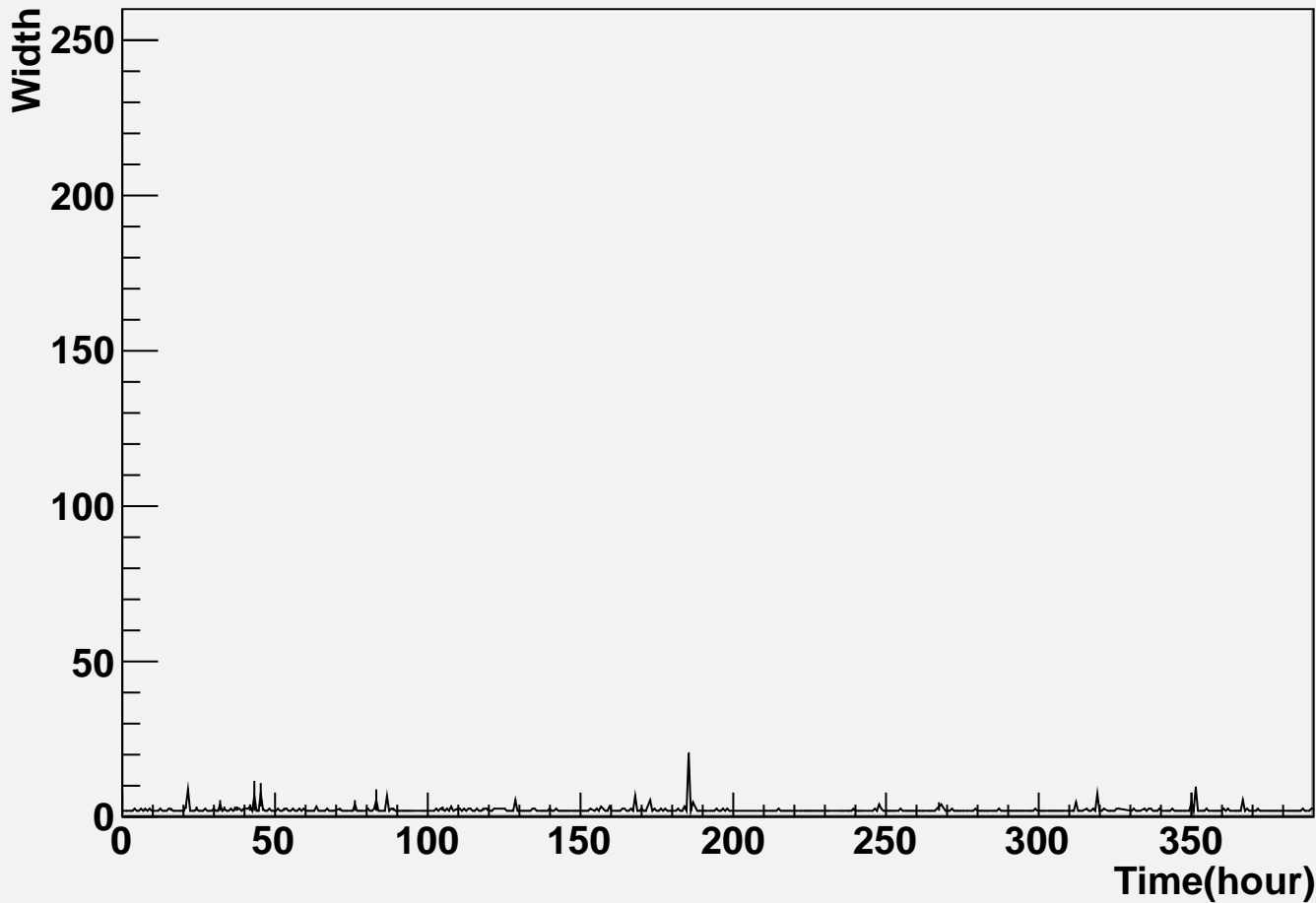
width-S1-SFEC01-Ch8



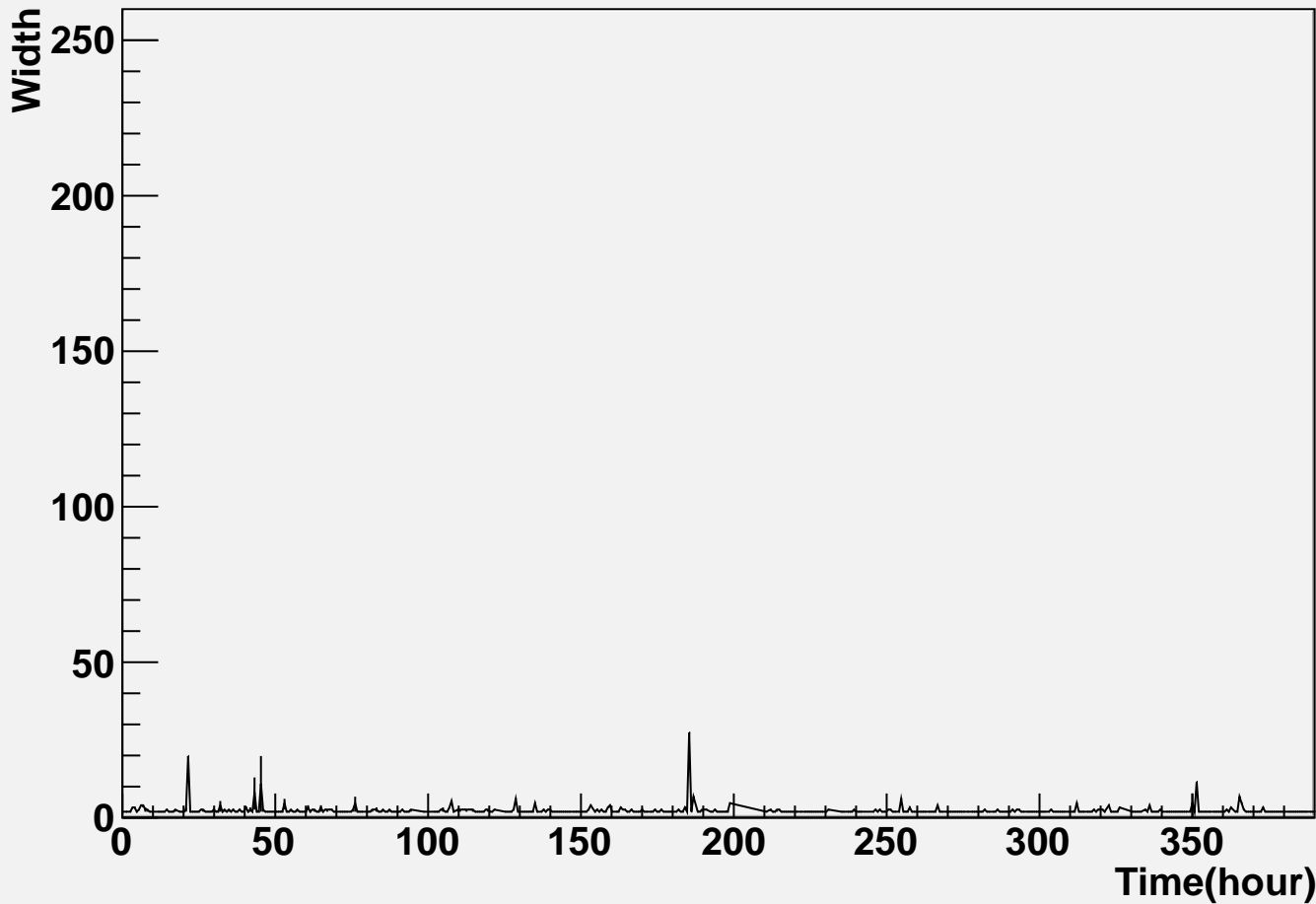
width-S1-SFEC01-Ch9



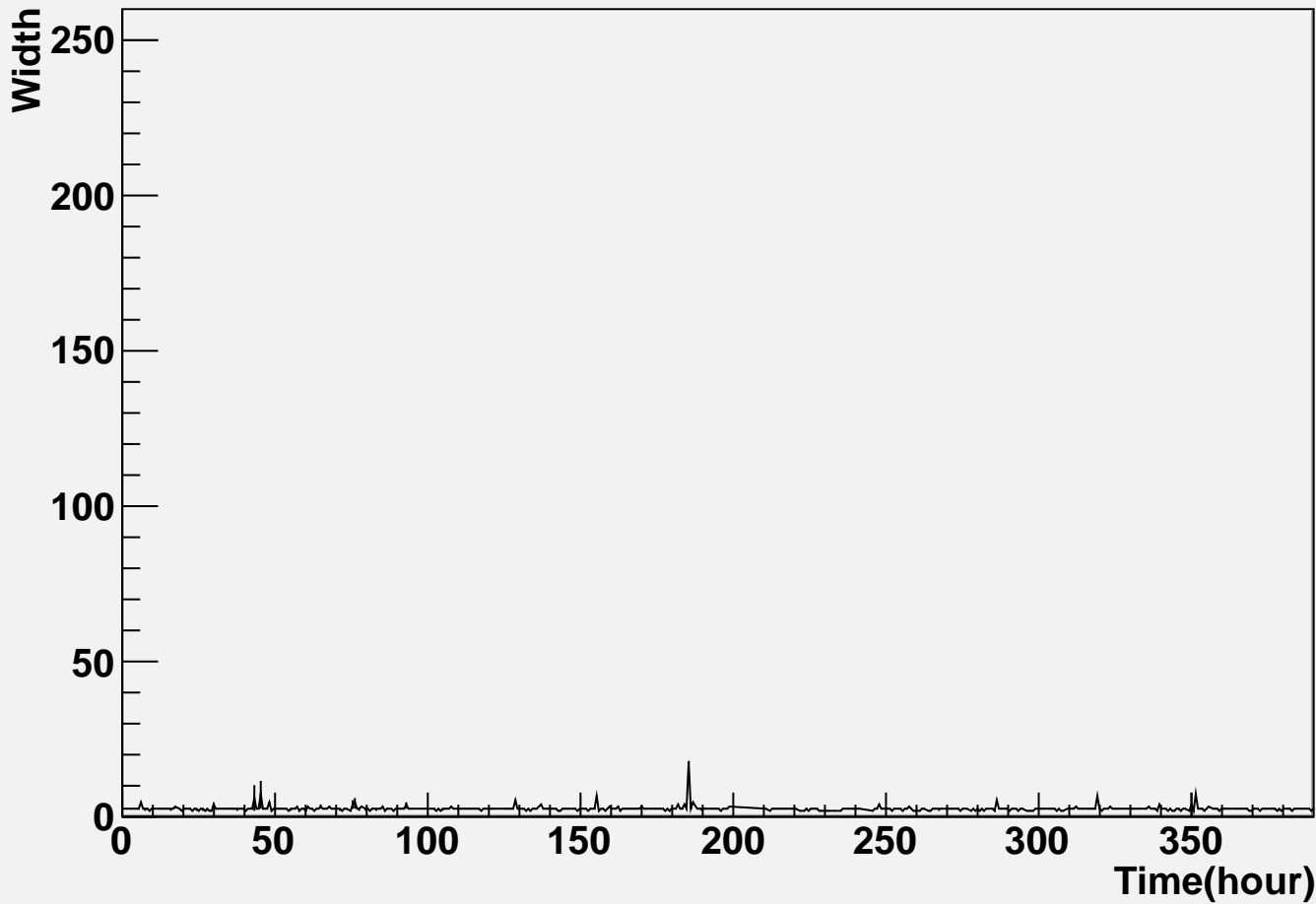
width-S1-SFEC10-Ch0



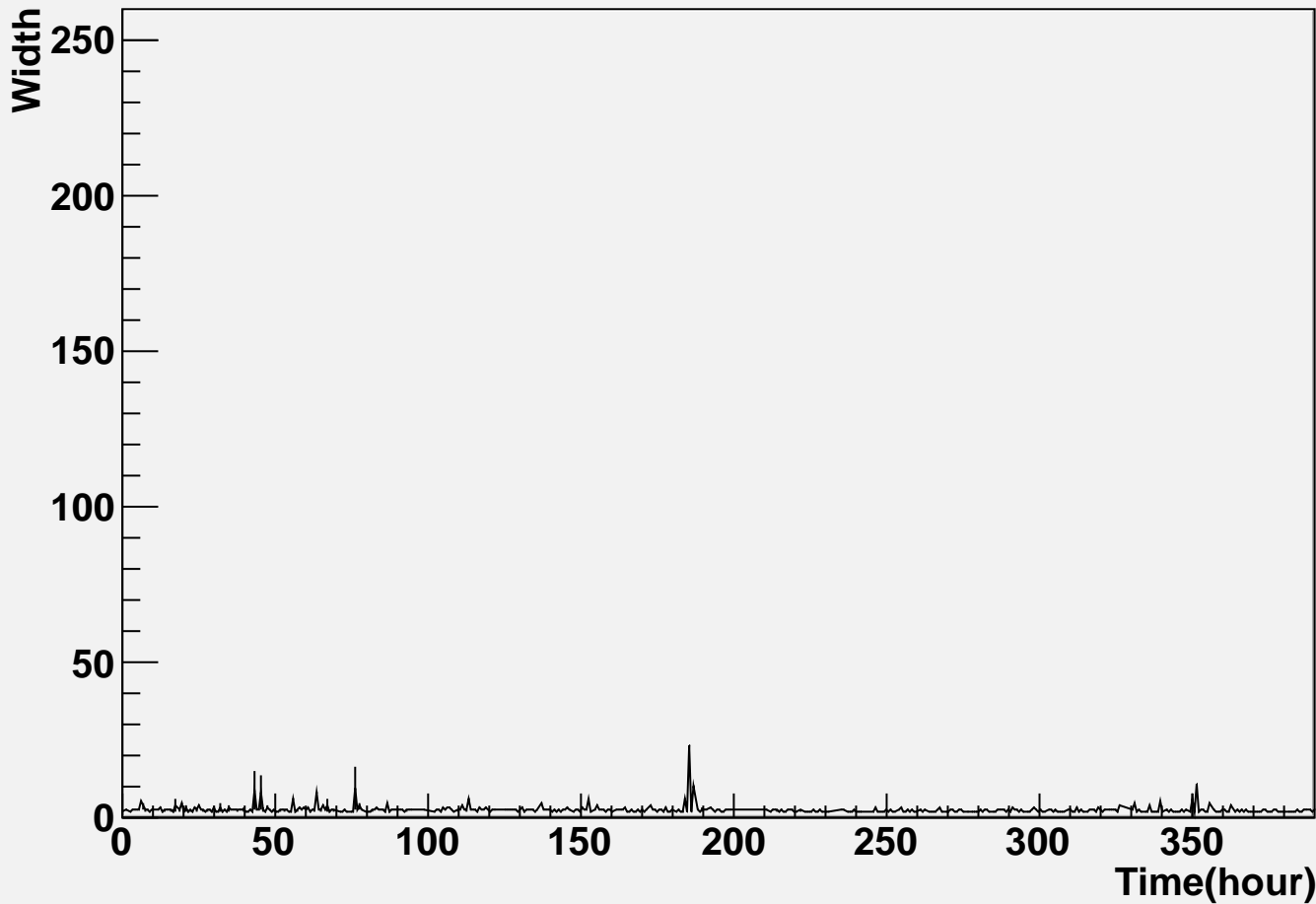
width-S1-SFEC10-Ch1



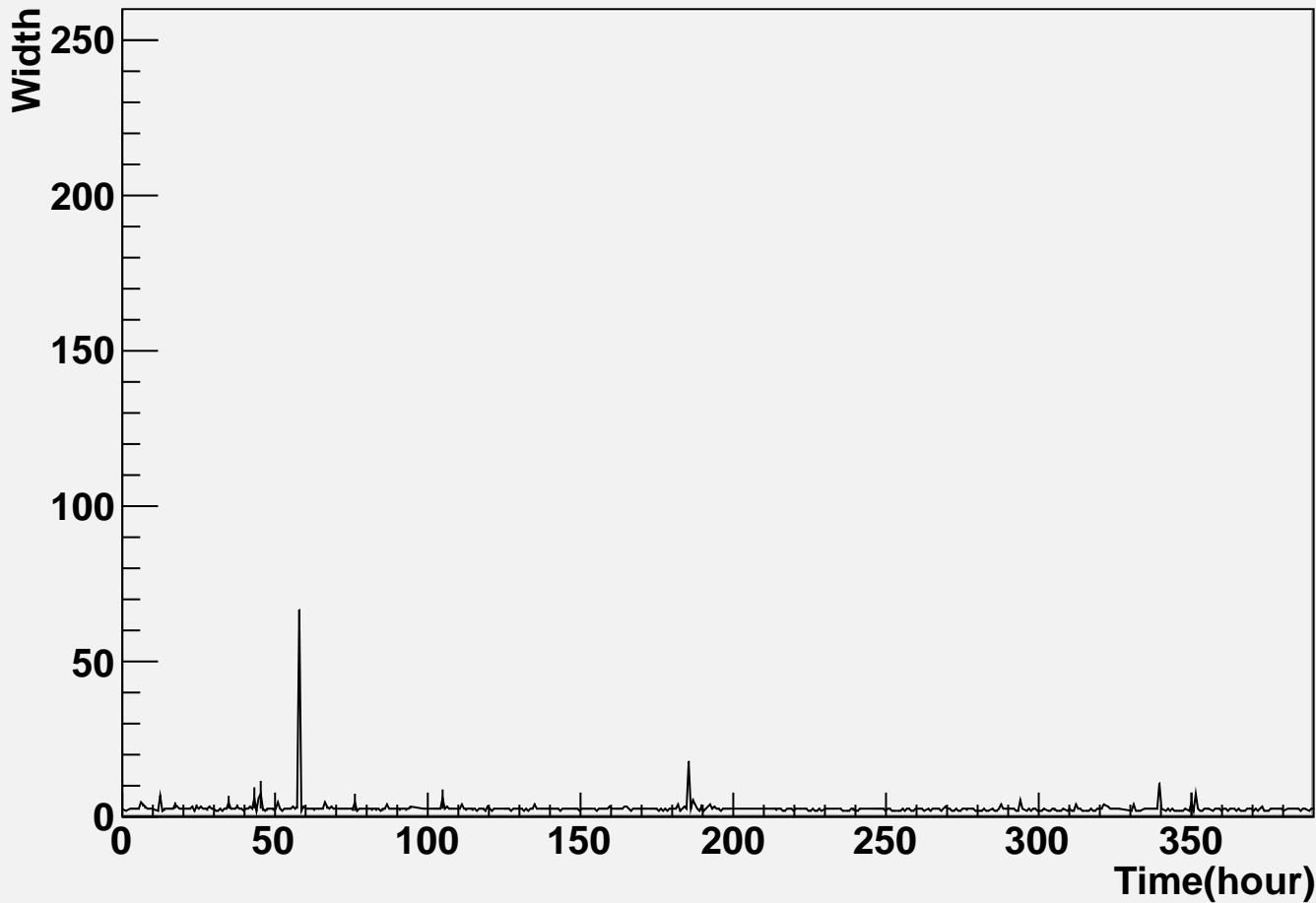
width-S1-SFEC10-Ch2



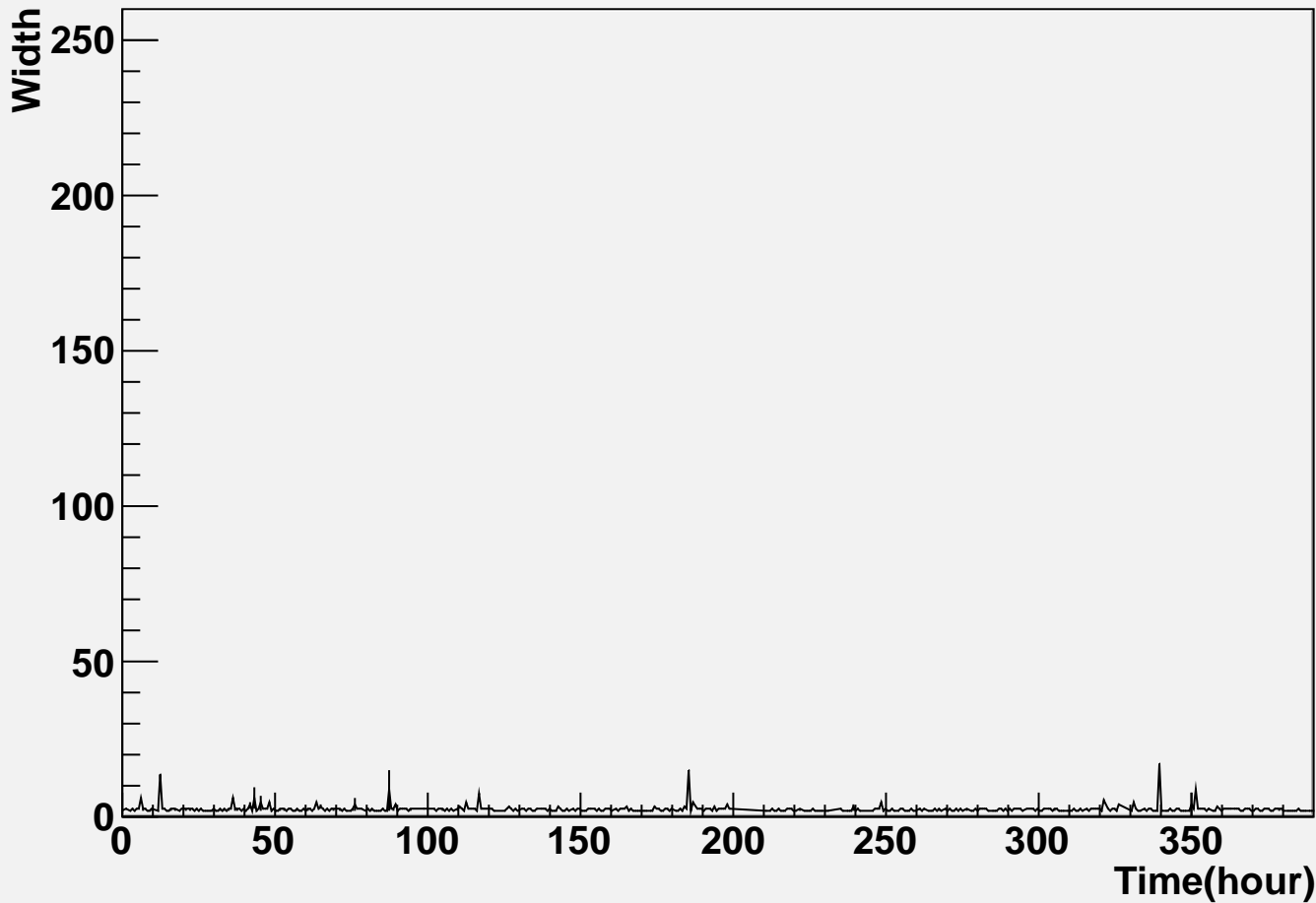
width-S1-SFEC10-Ch3



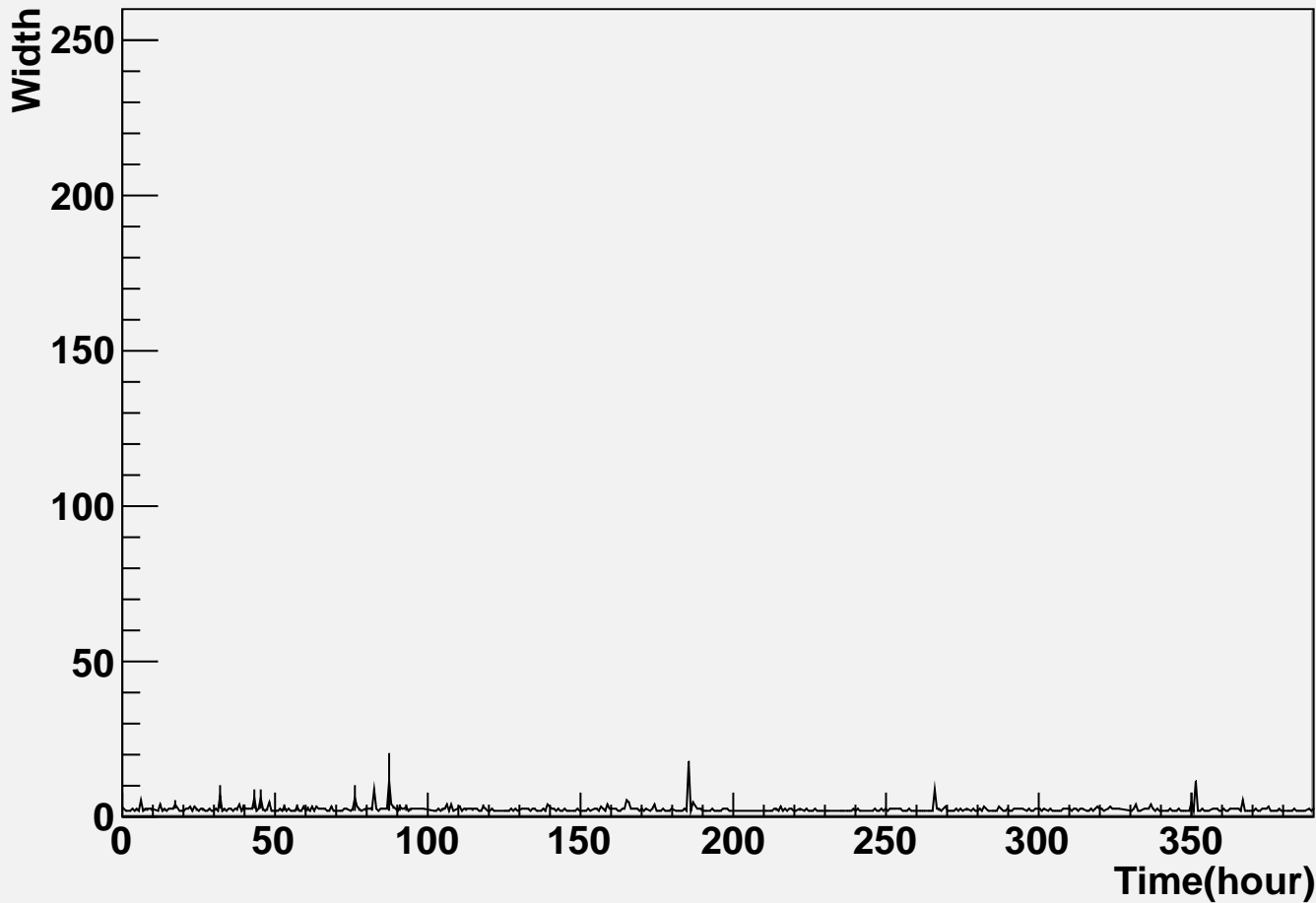
width-S1-SFEC10-Ch4



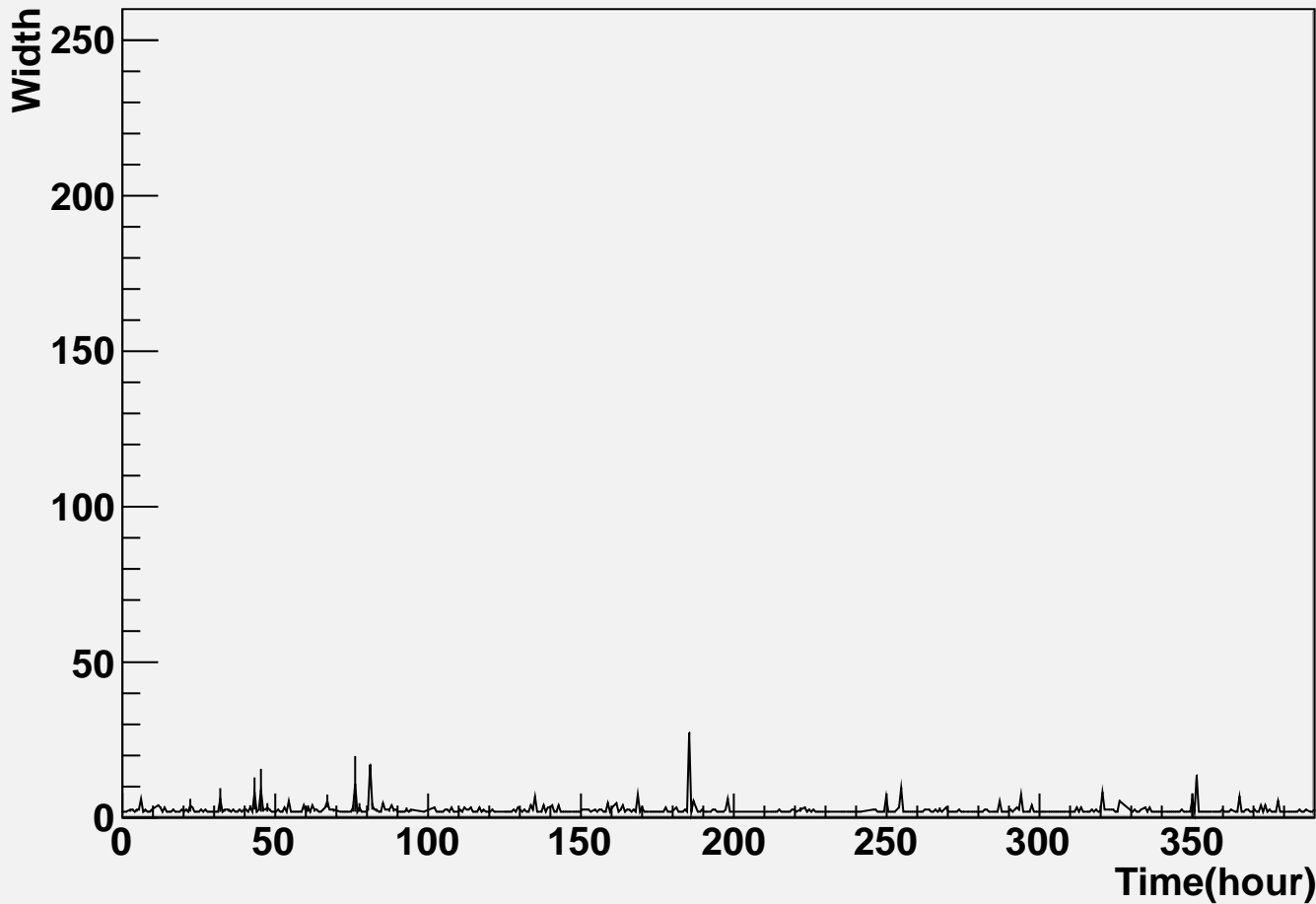
width-S1-SFEC10-Ch5



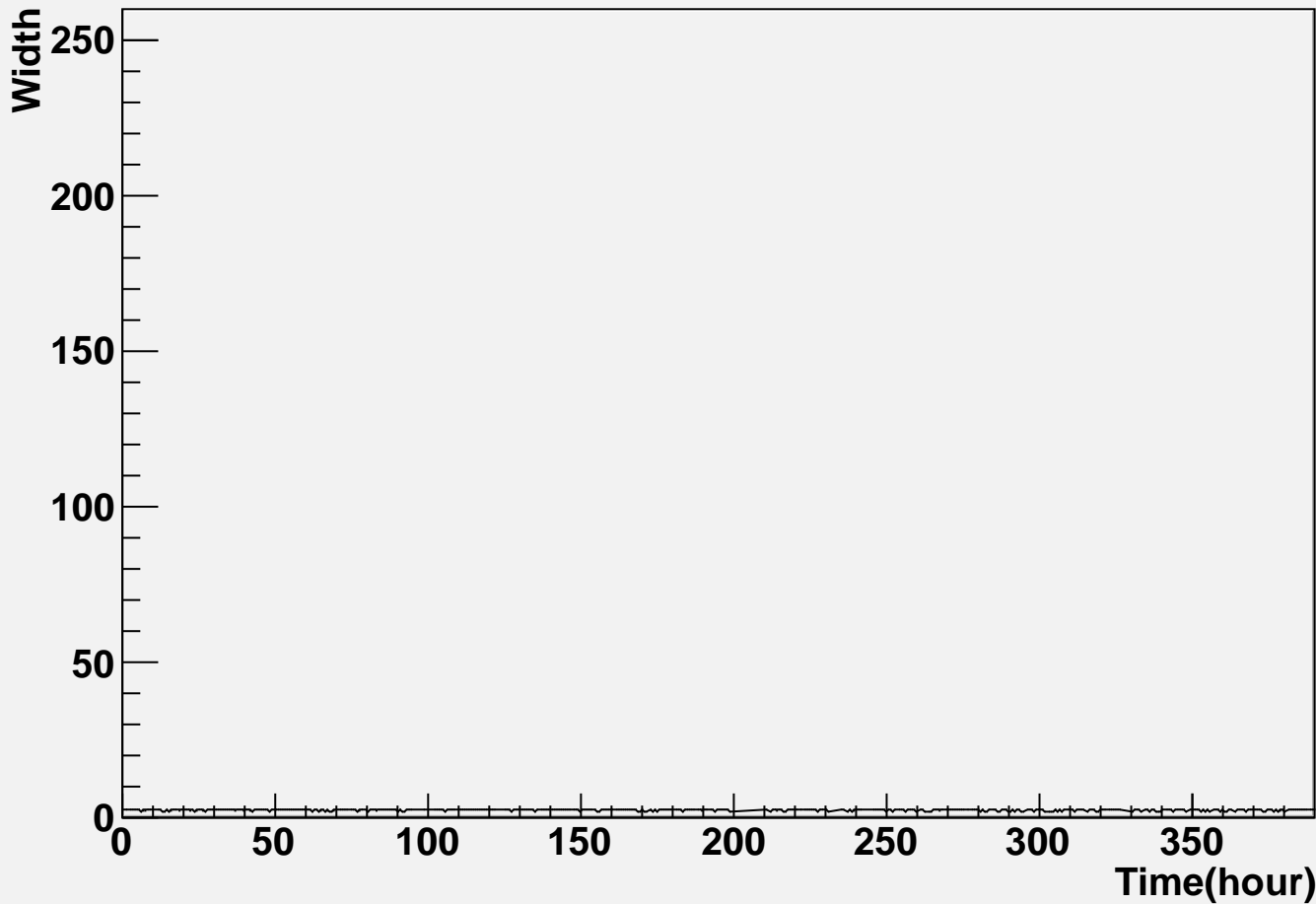
width-S1-SFEC10-Ch6



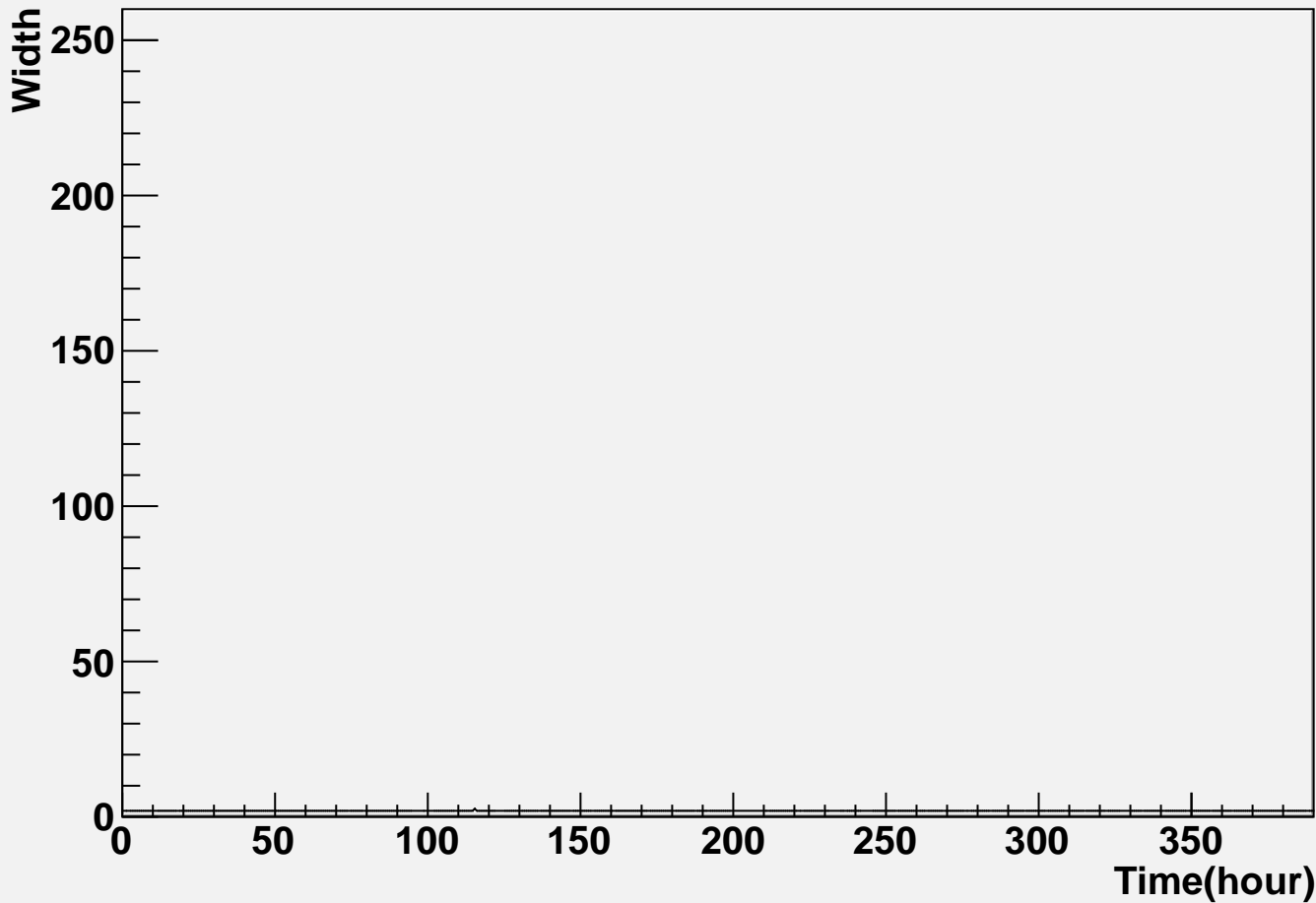
width-S1-SFEC10-Ch7



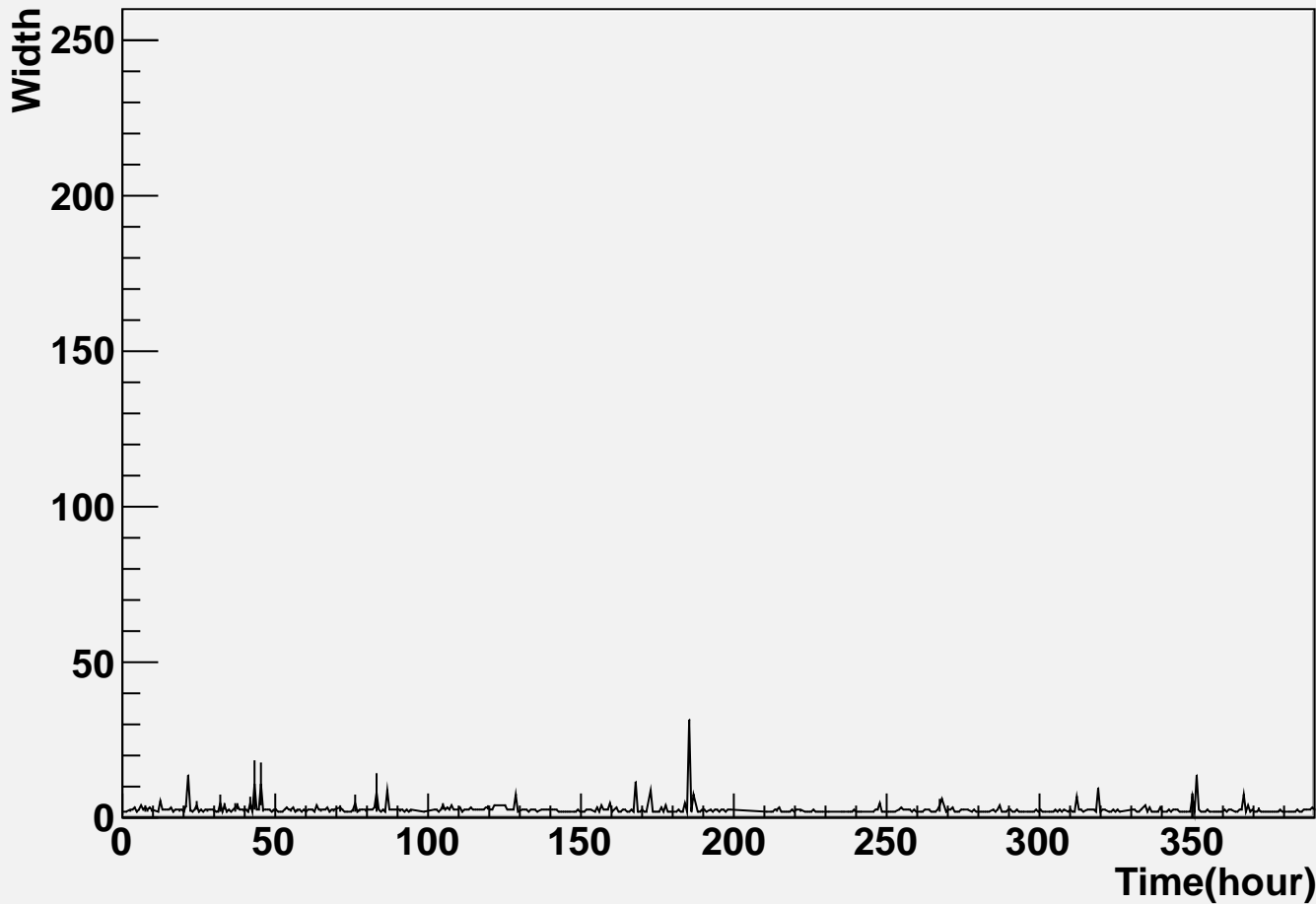
width-S1-SFEC10-Ch8



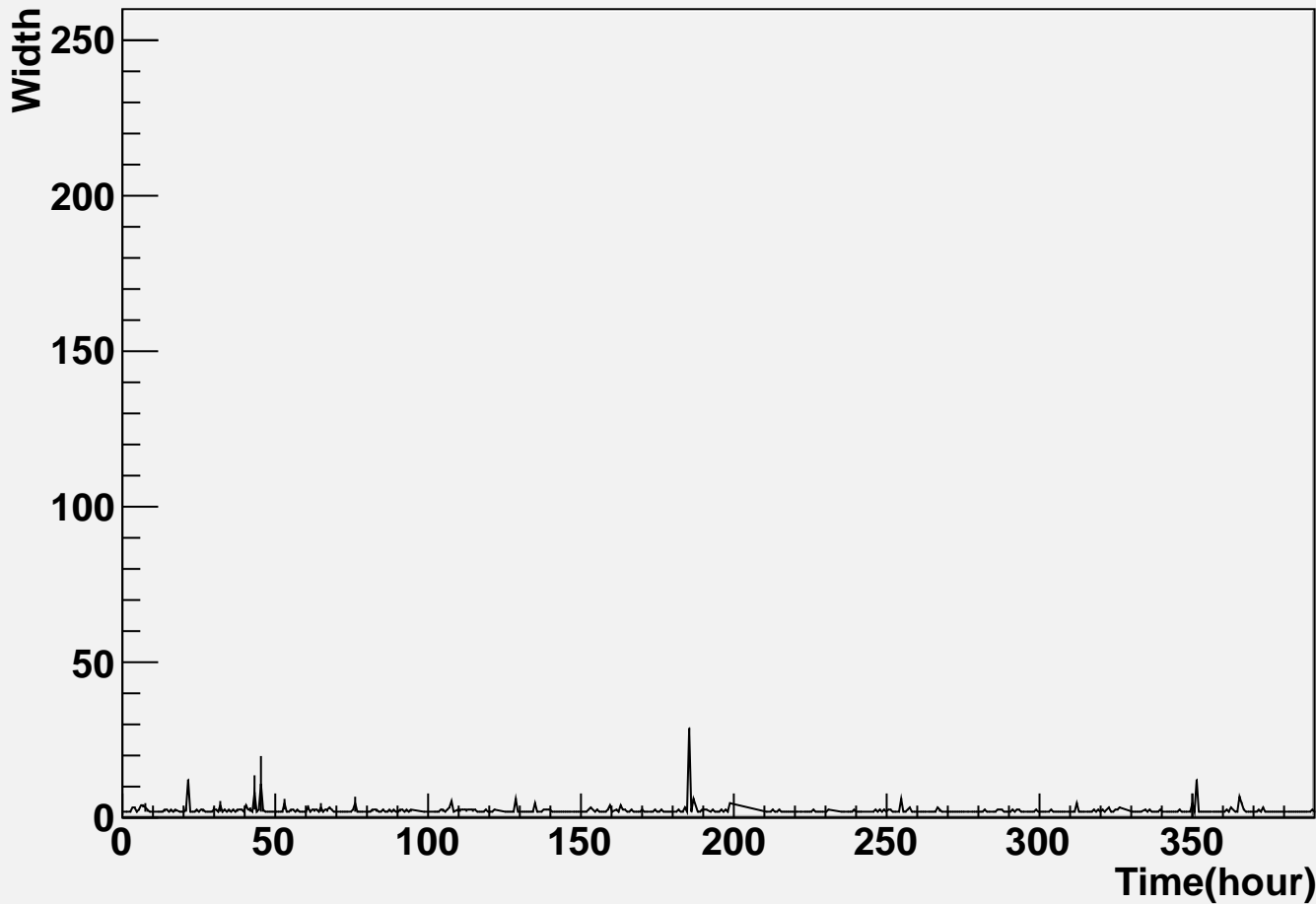
width-S1-SFEC10-Ch9



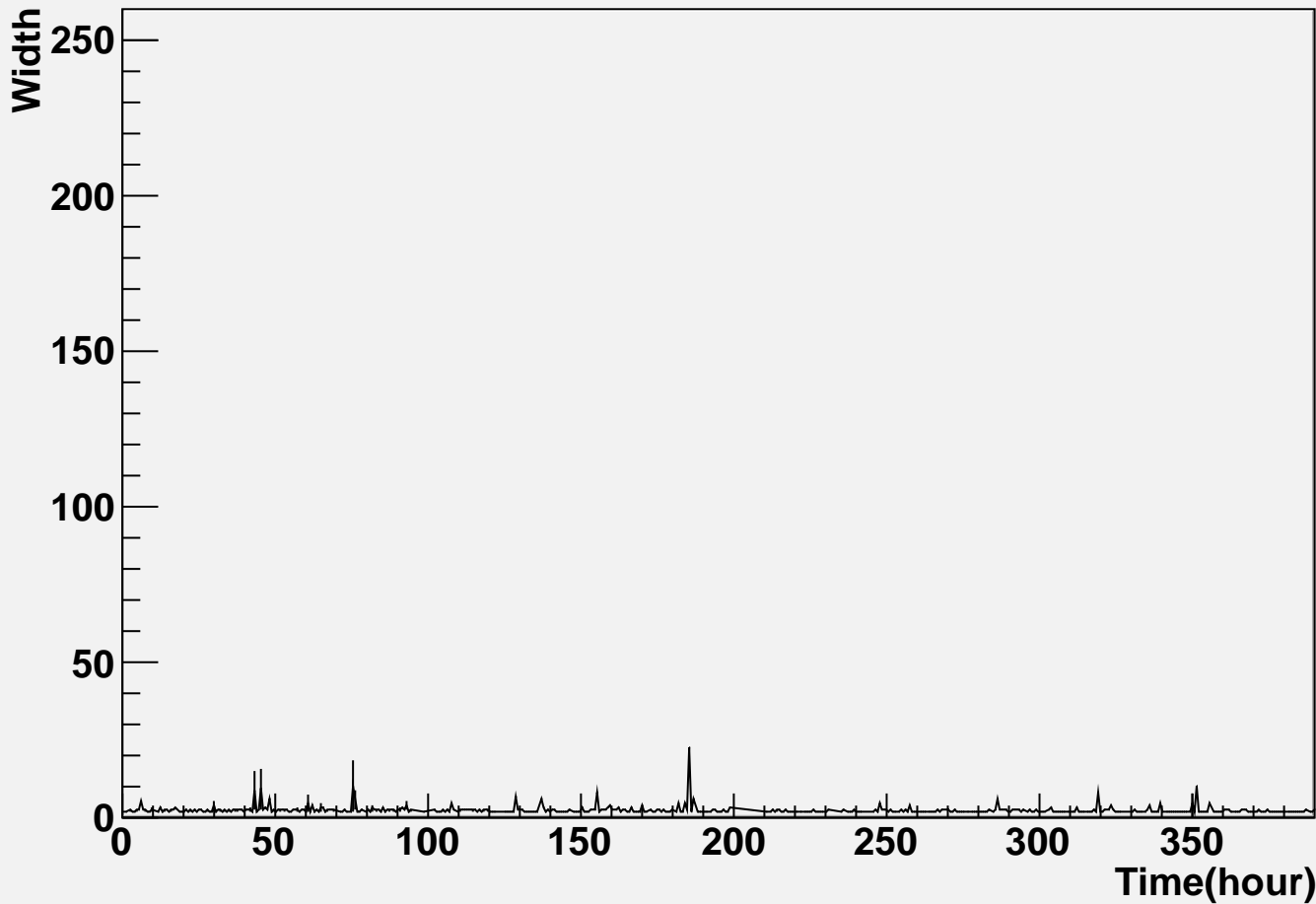
width-S1-SFEC11-Ch0



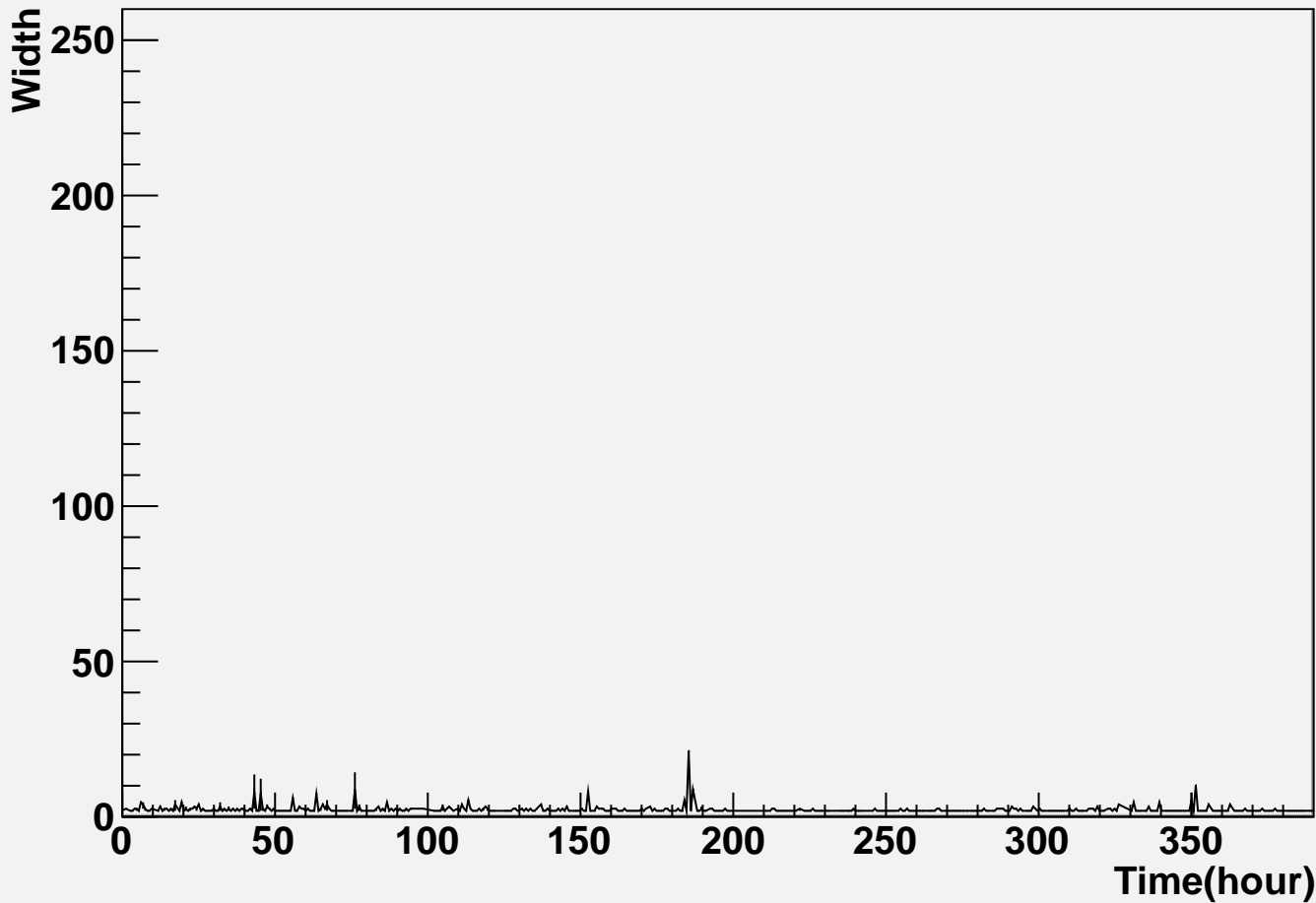
width-S1-SFEC11-Ch1



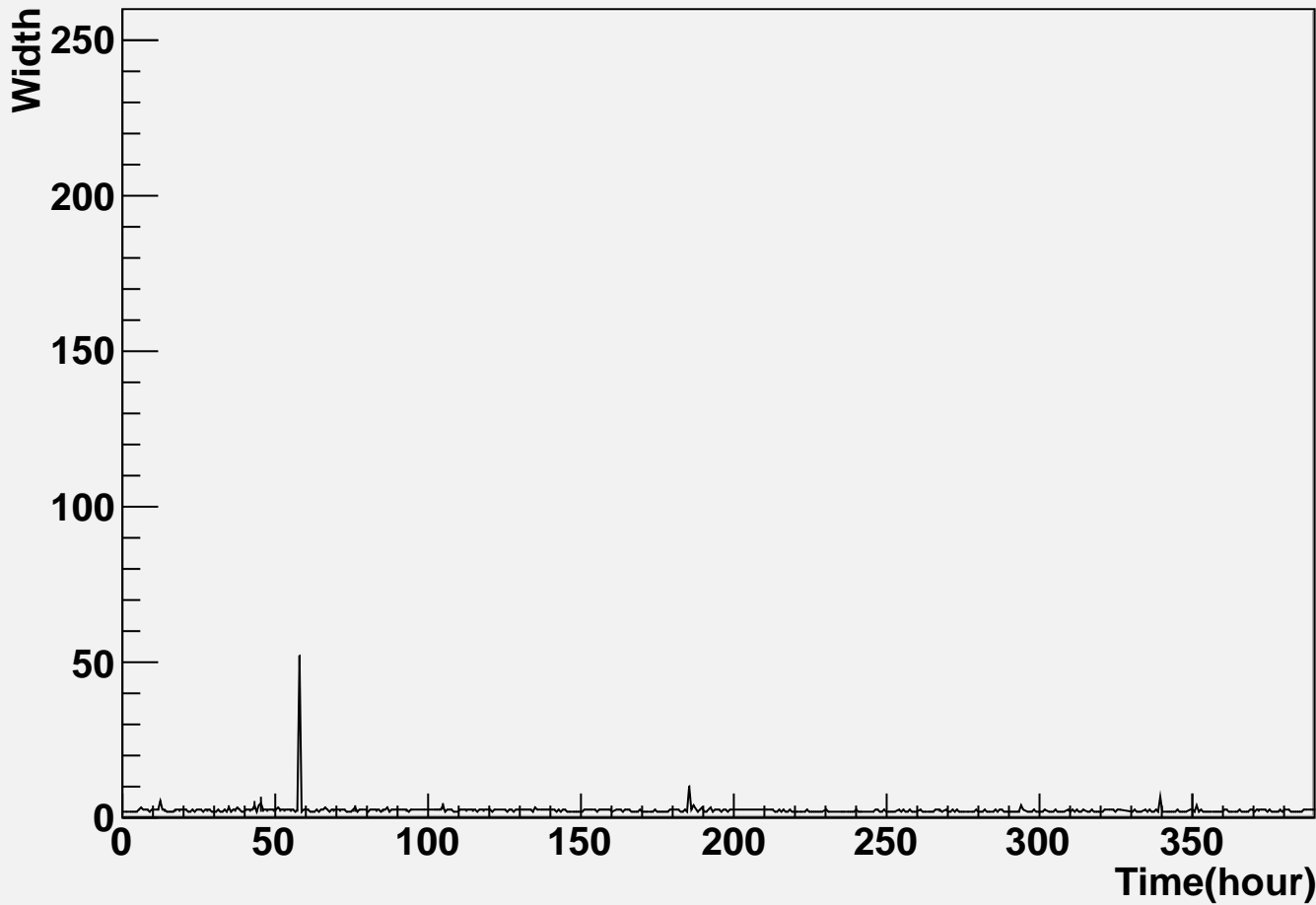
width-S1-SFEC11-Ch2



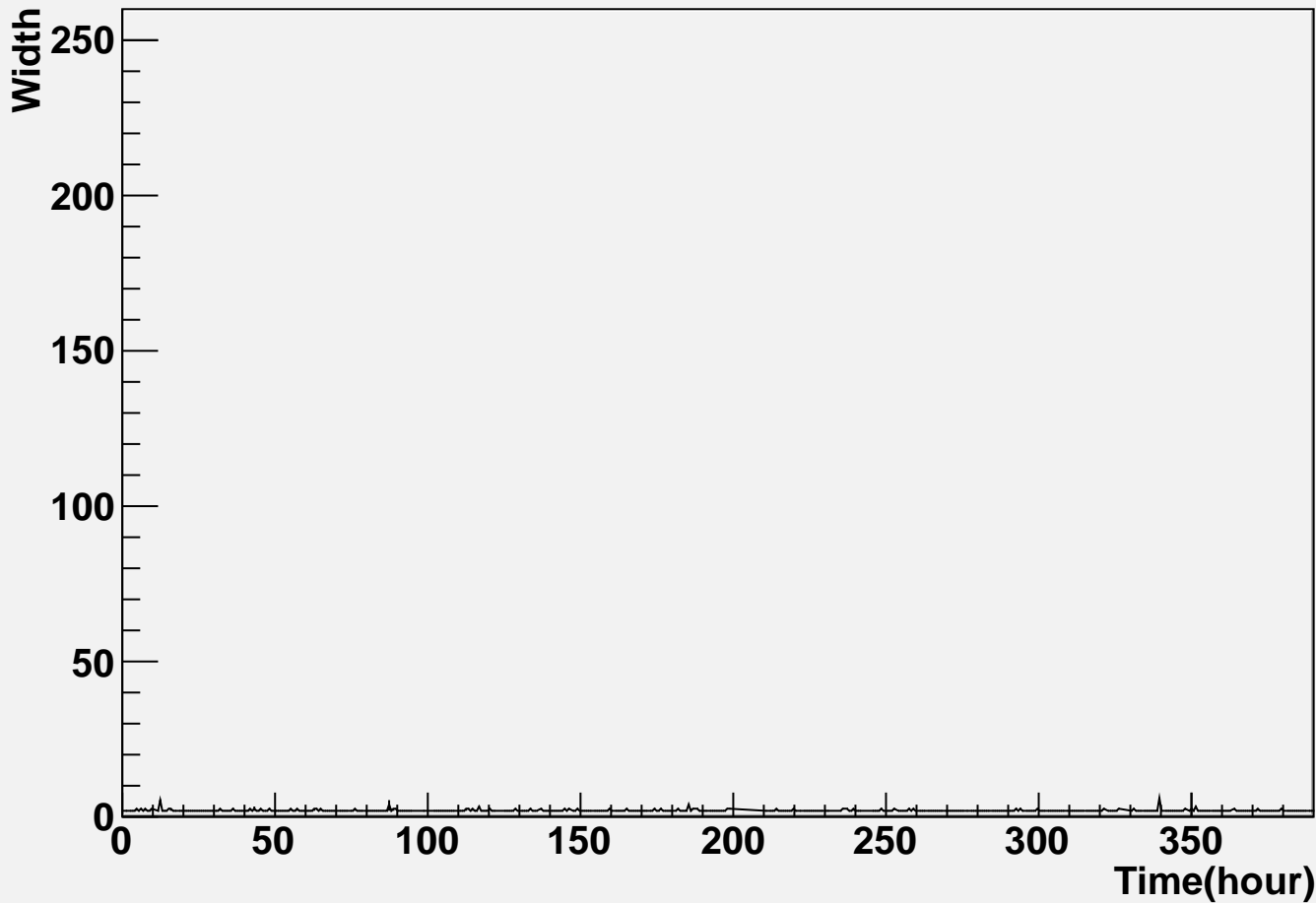
width-S1-SFEC11-Ch3



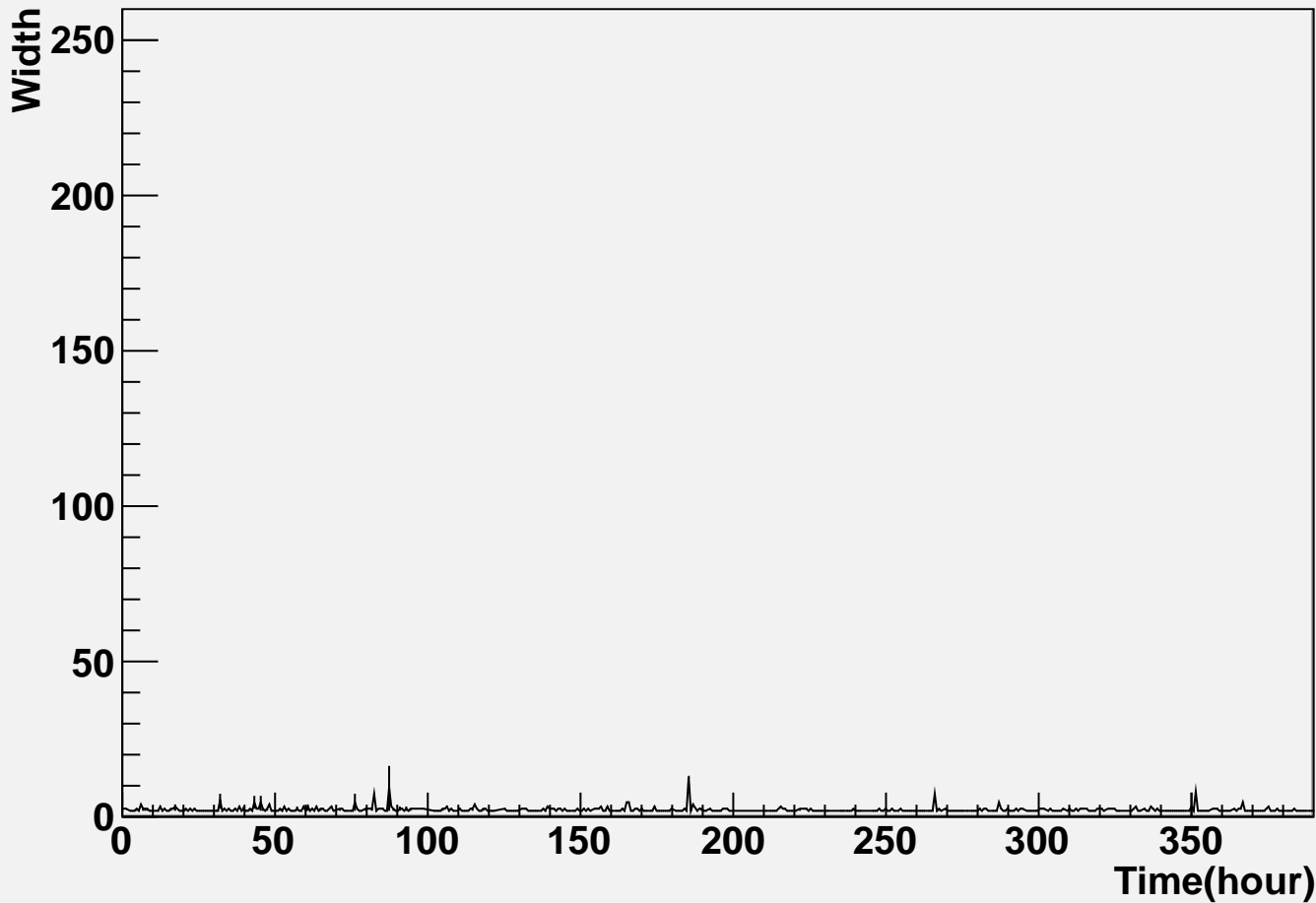
width-S1-SFEC11-Ch4



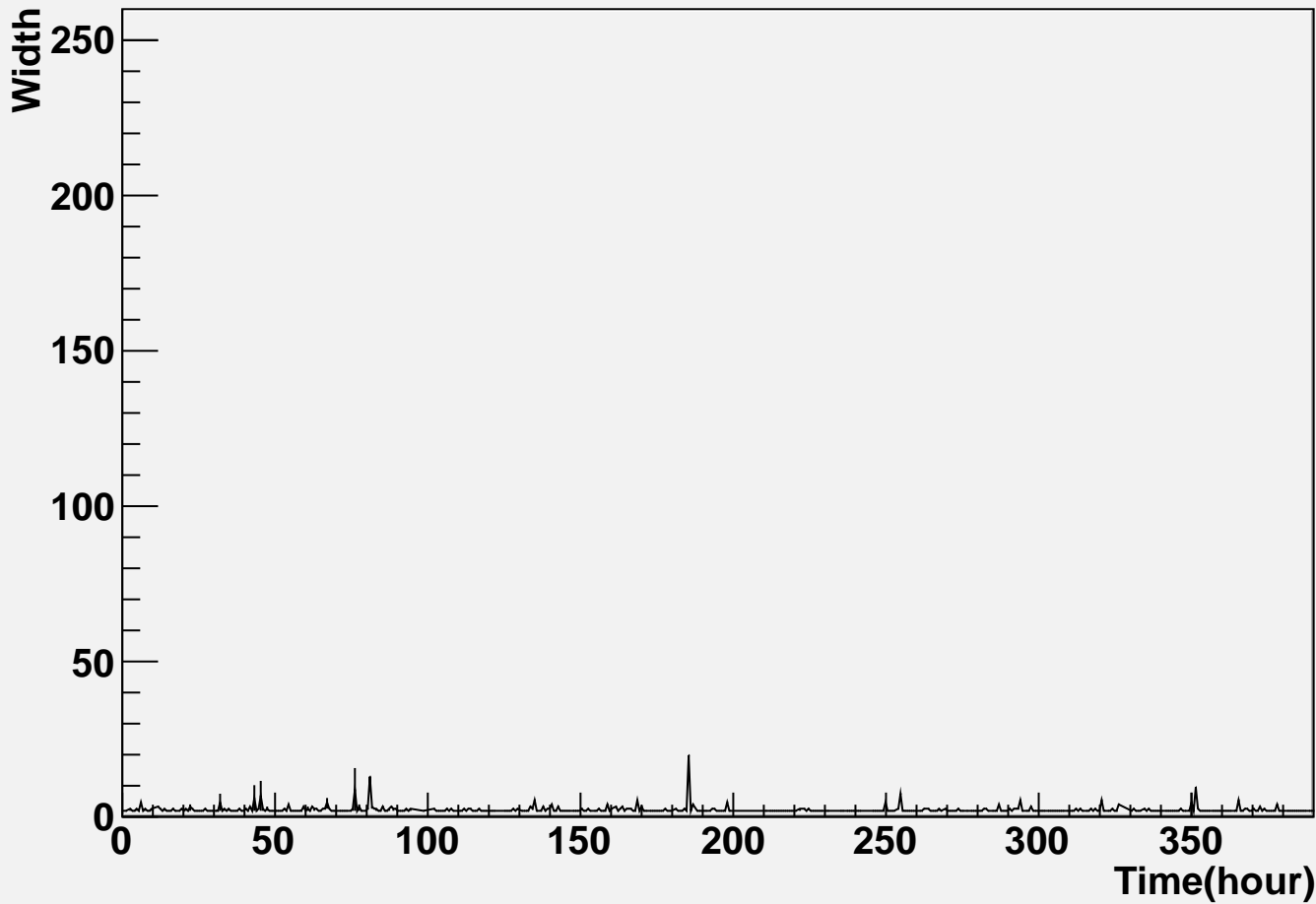
width-S1-SFEC11-Ch5



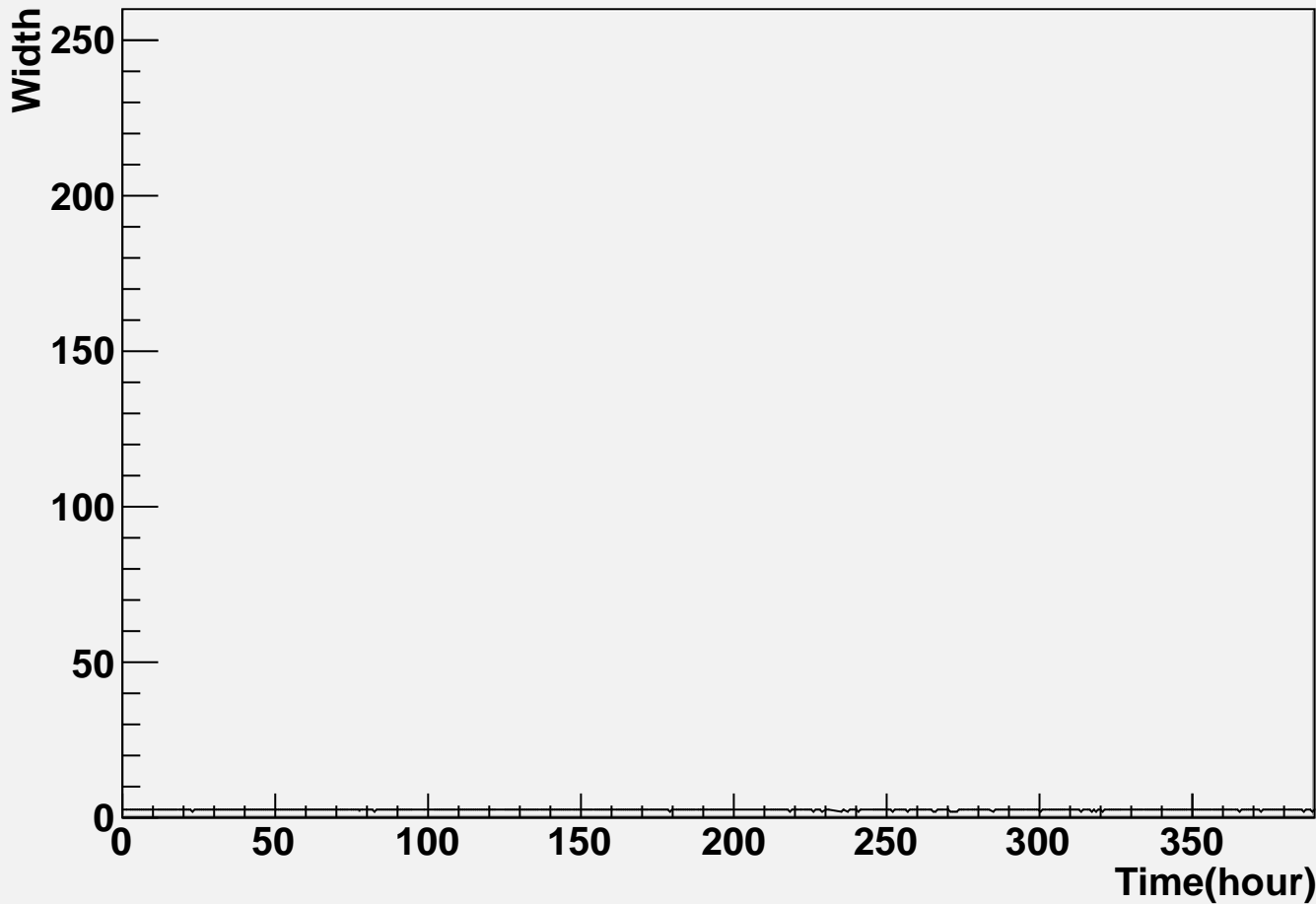
width-S1-SFEC11-Ch6



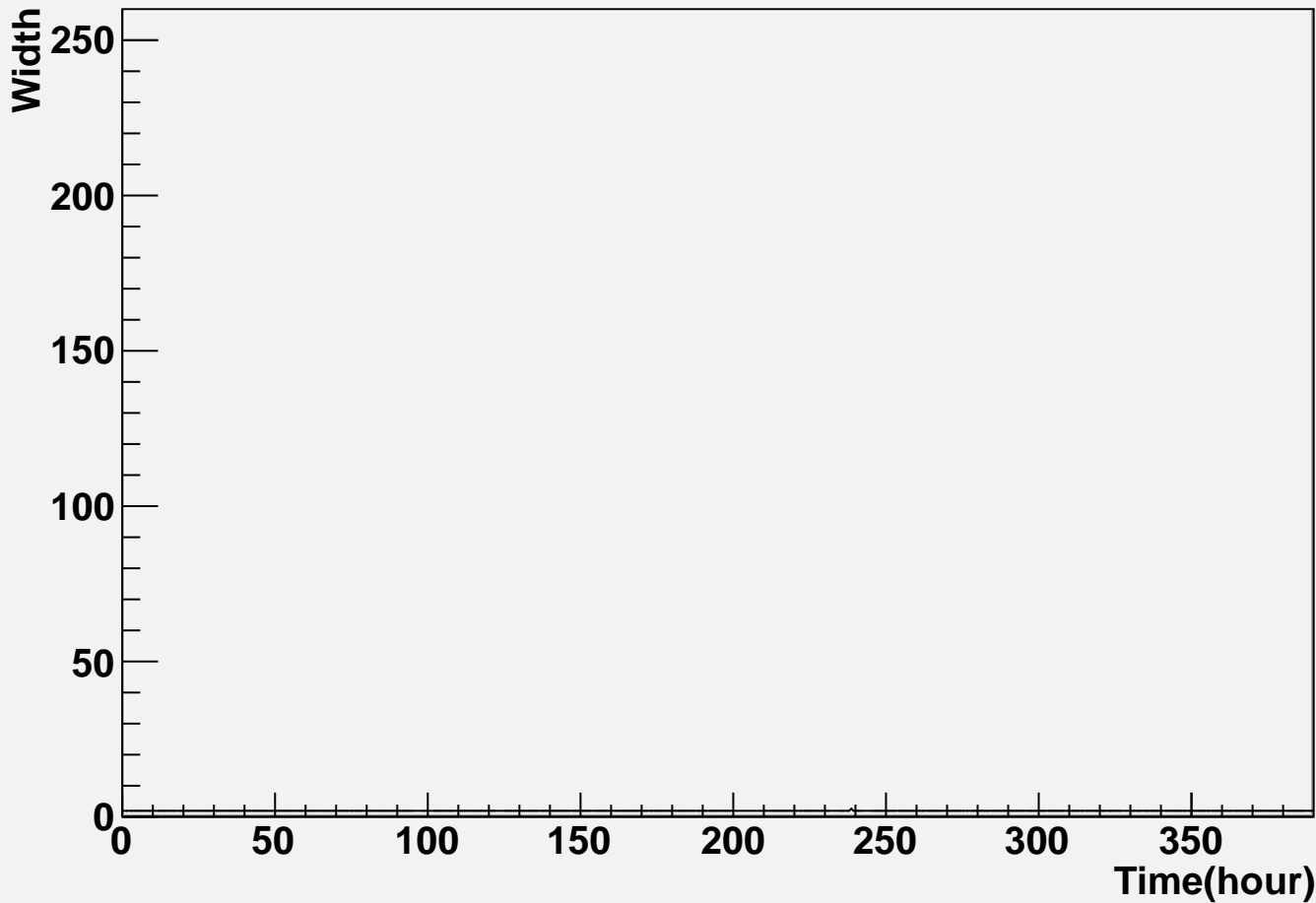
width-S1-SFEC11-Ch7



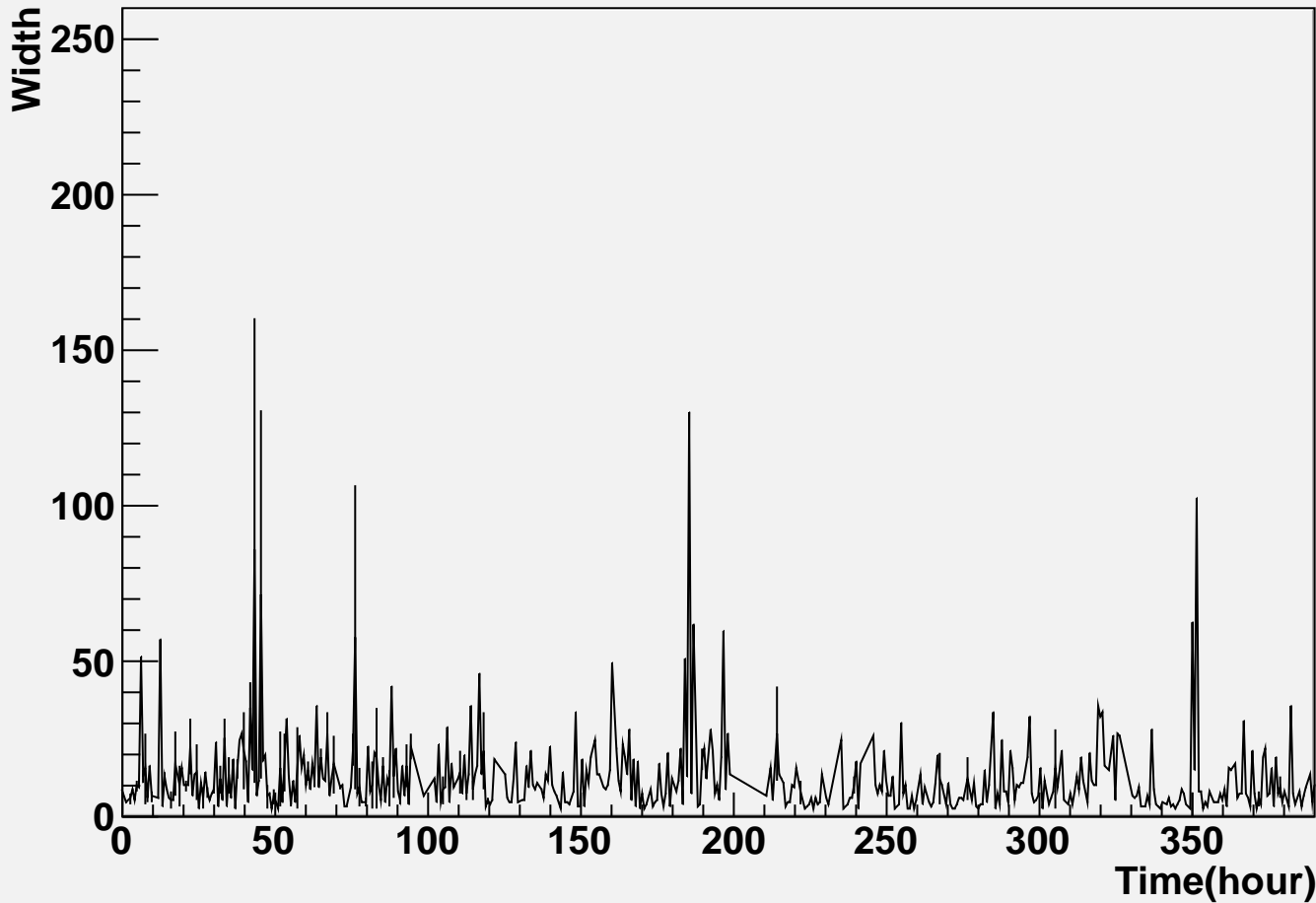
width-S1-SFEC11-Ch8



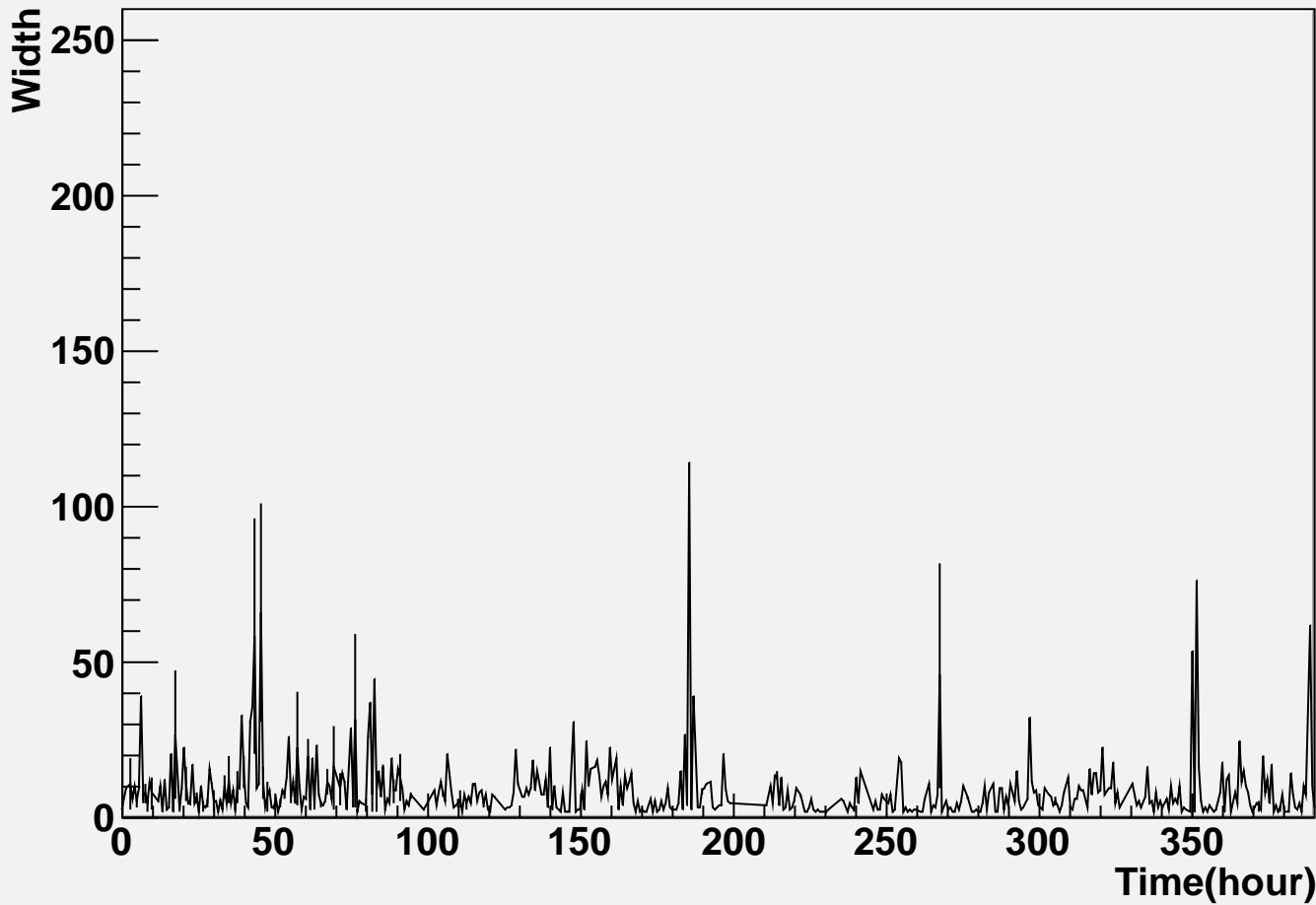
width-S1-SFEC11-Ch9



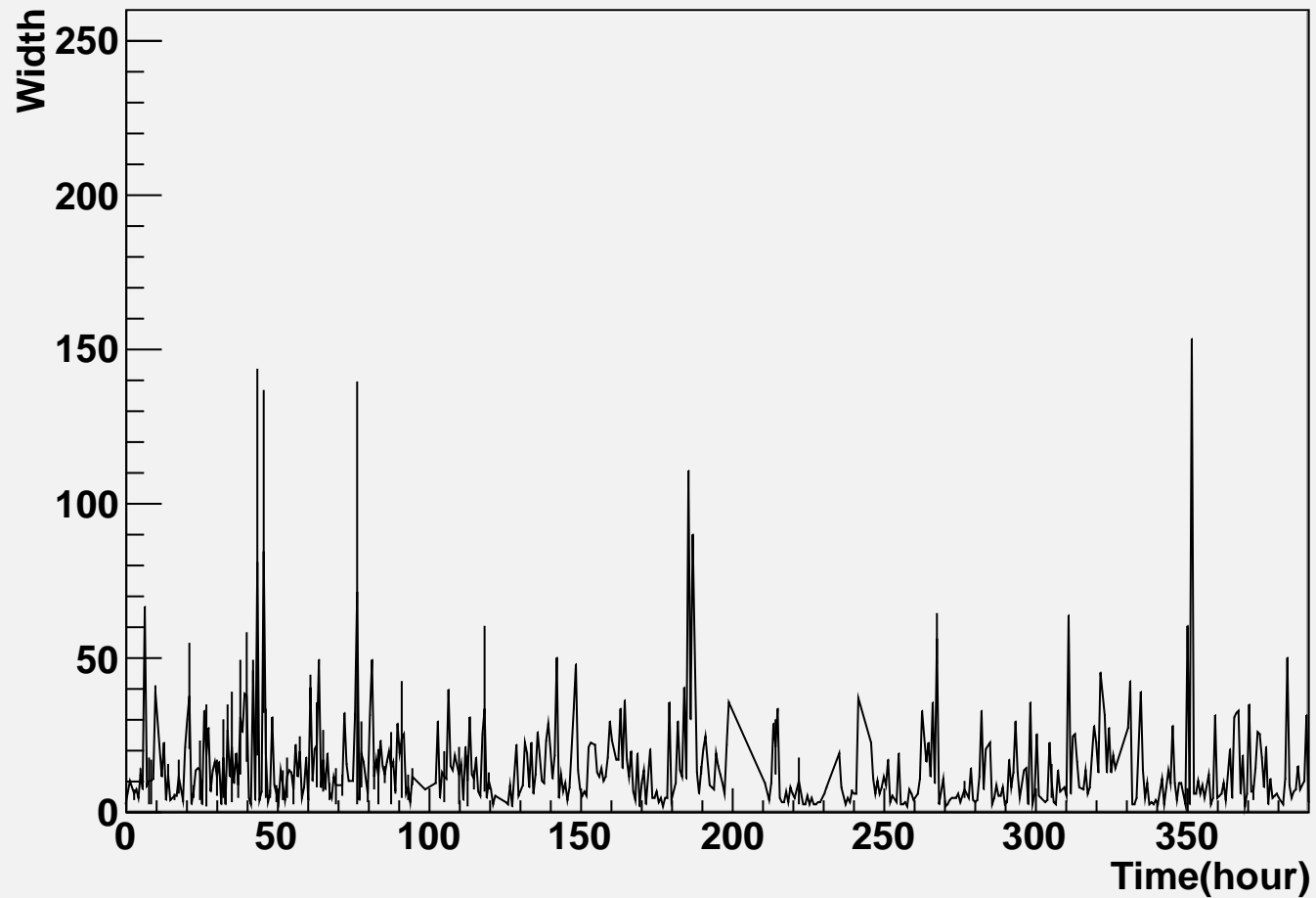
width-S2-SFET2A-Ch0



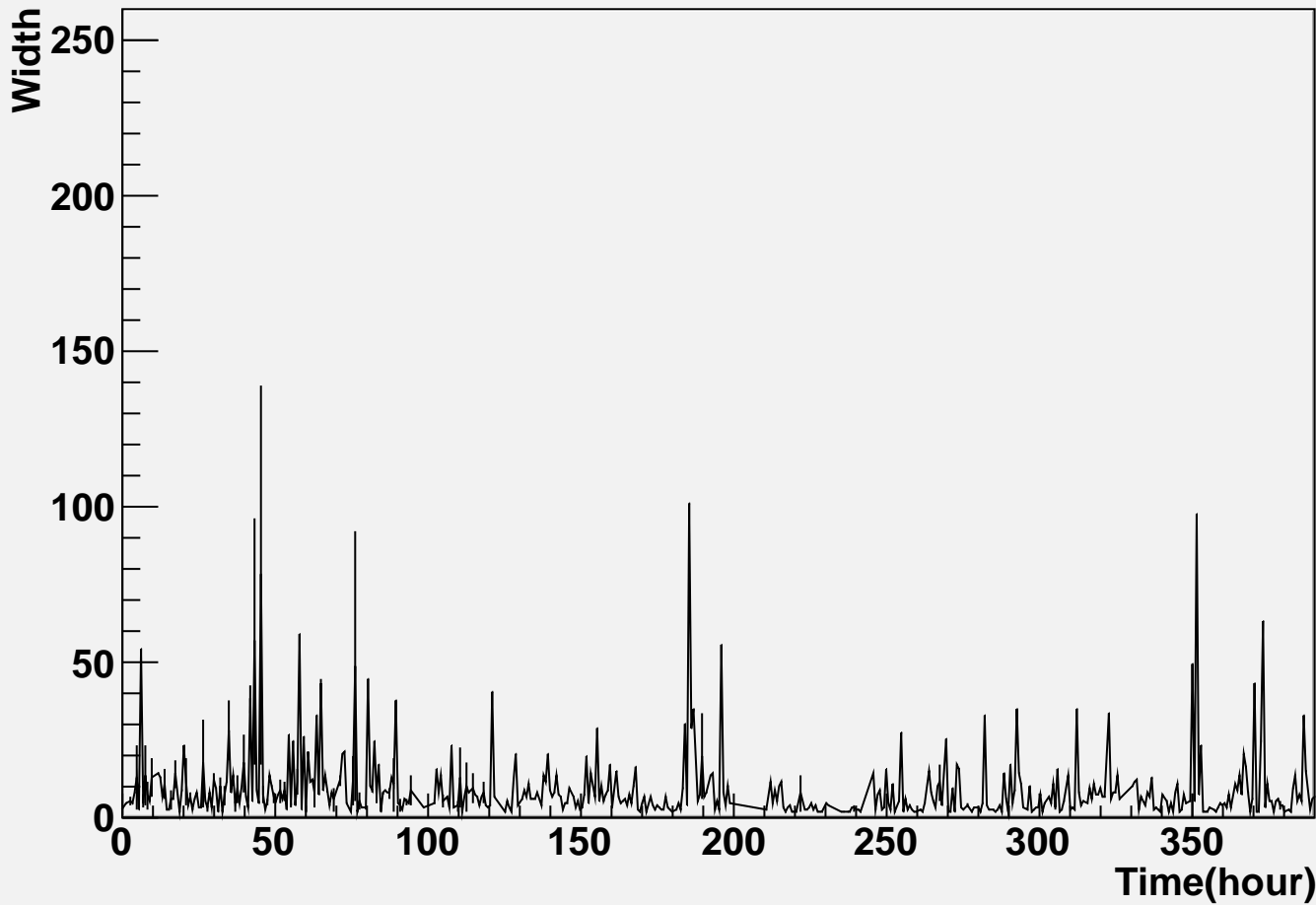
width-S2-SFET2A-Ch1



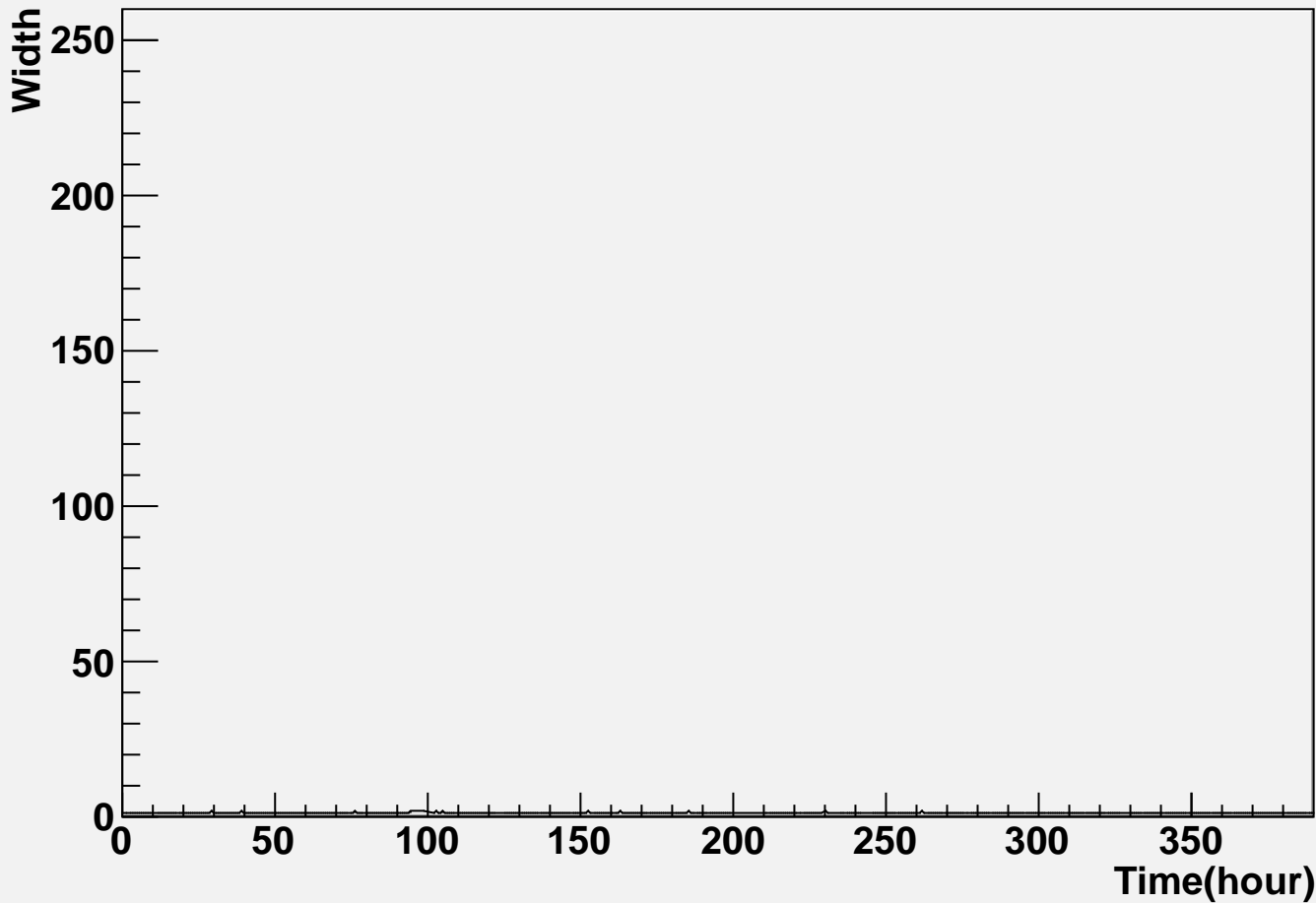
width-S2-SFET2A-Ch2



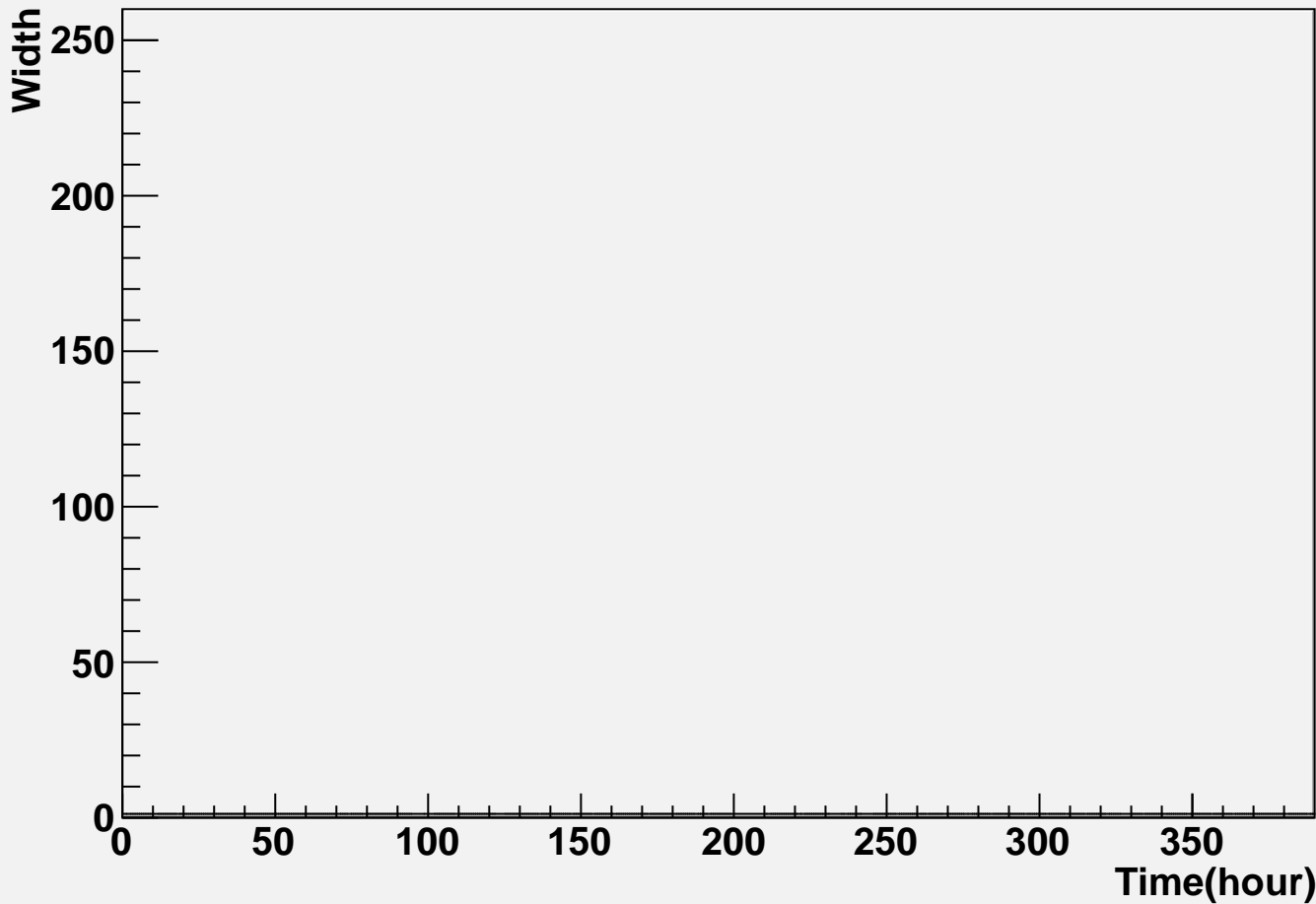
width-S2-SFET2A-Ch3



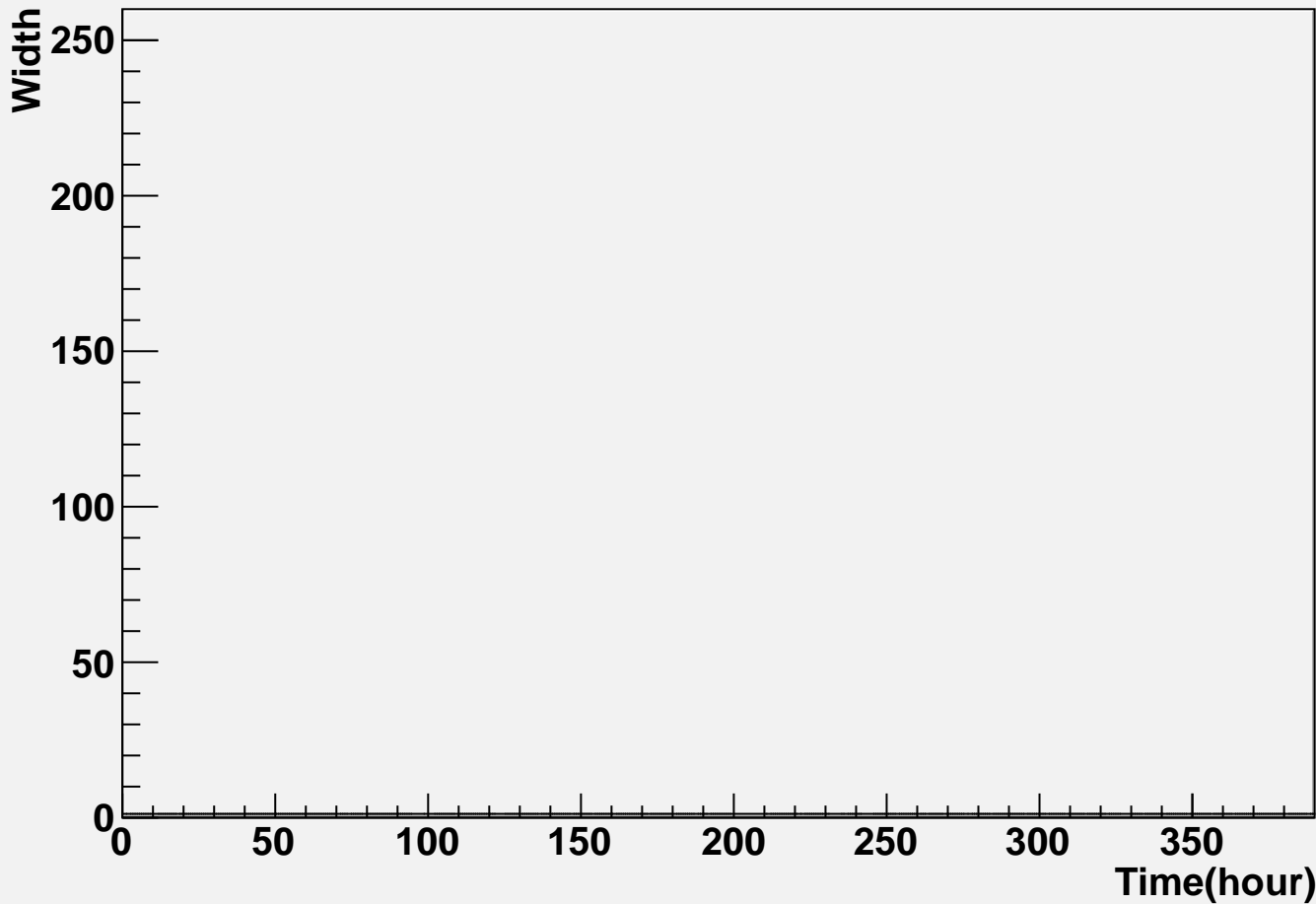
width-S2-SFET2A-Ch4



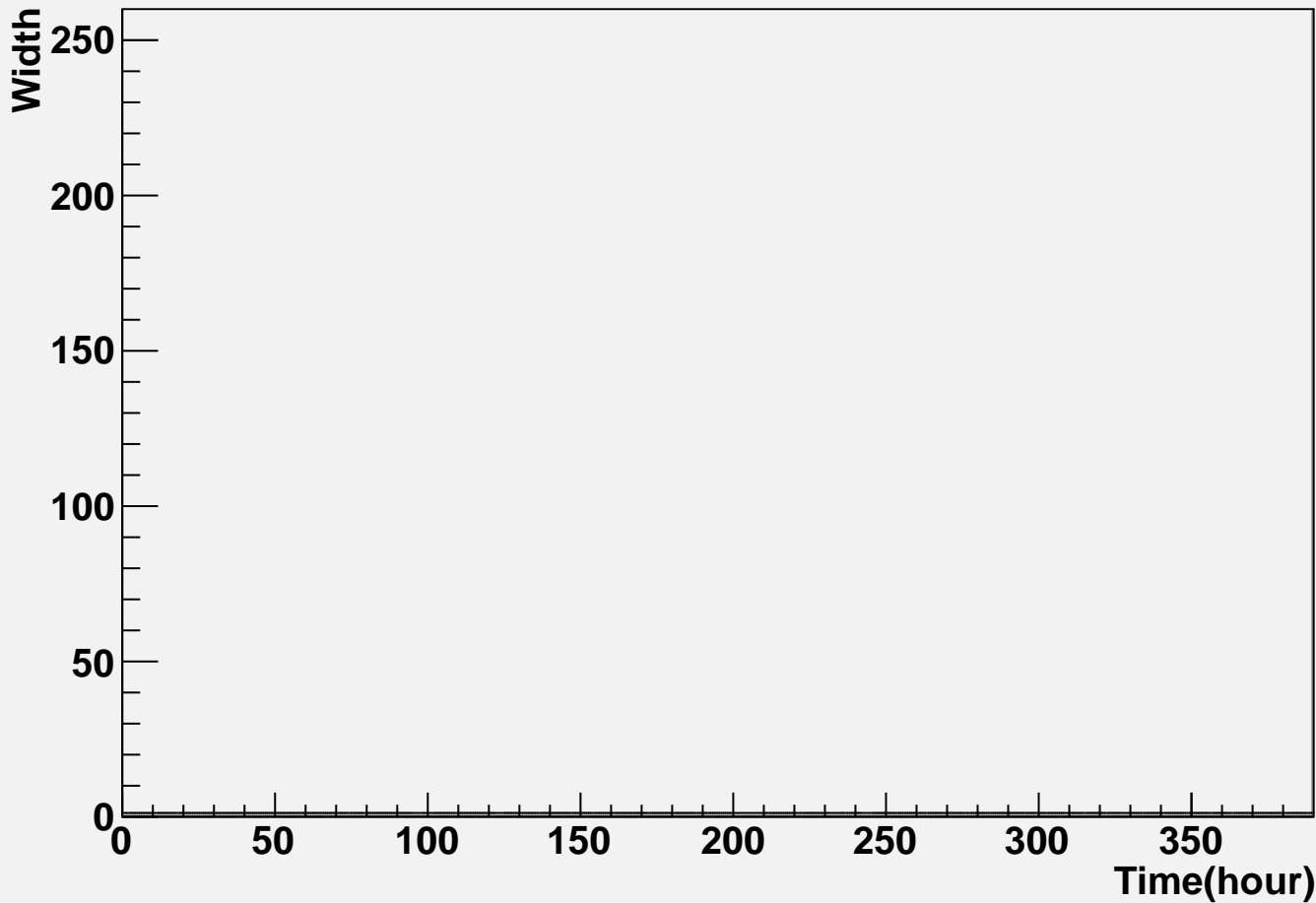
width-S2-SFET2A-Ch5



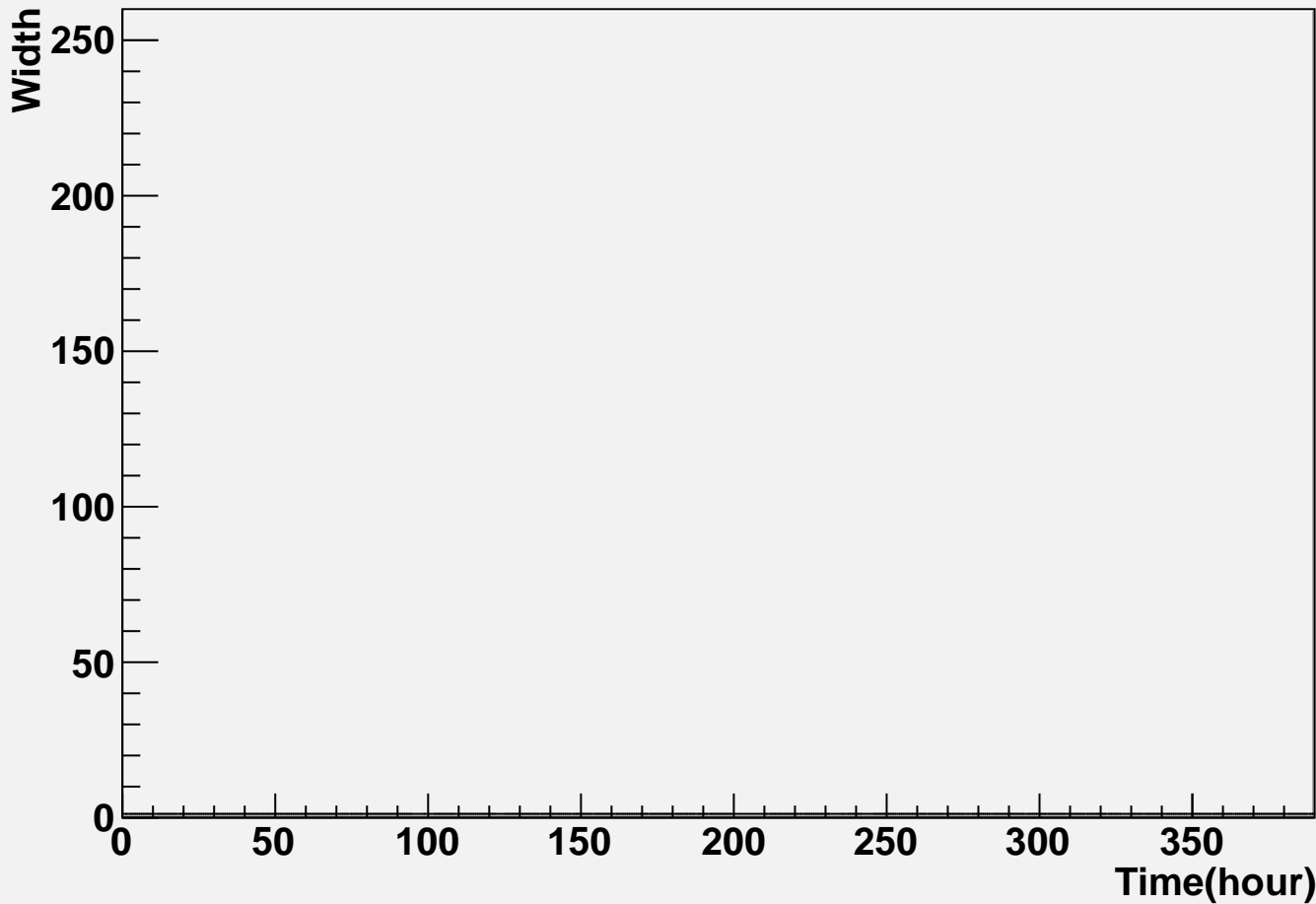
width-S2-SFET2A-Ch6



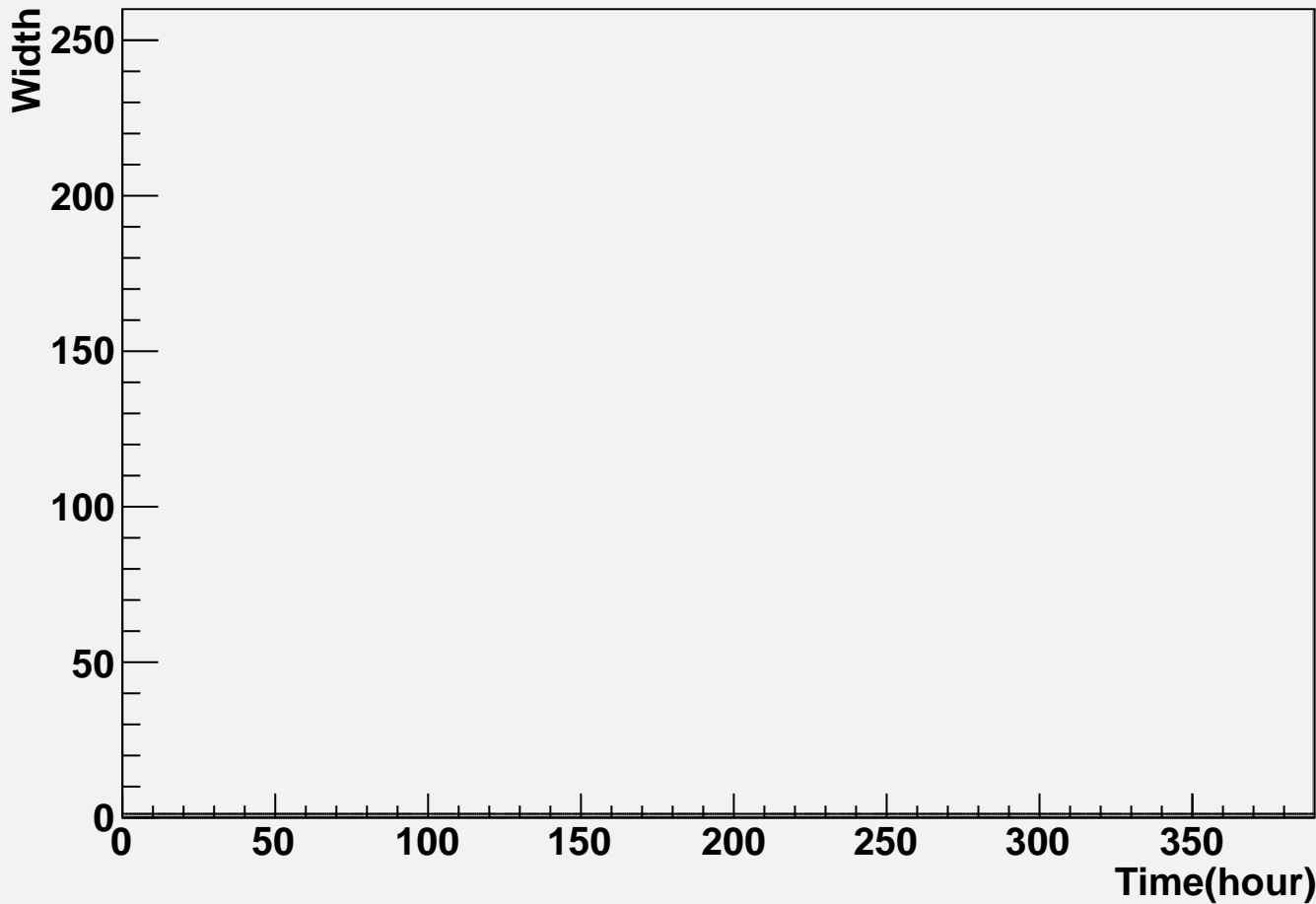
width-S2-SFET2A-Ch7



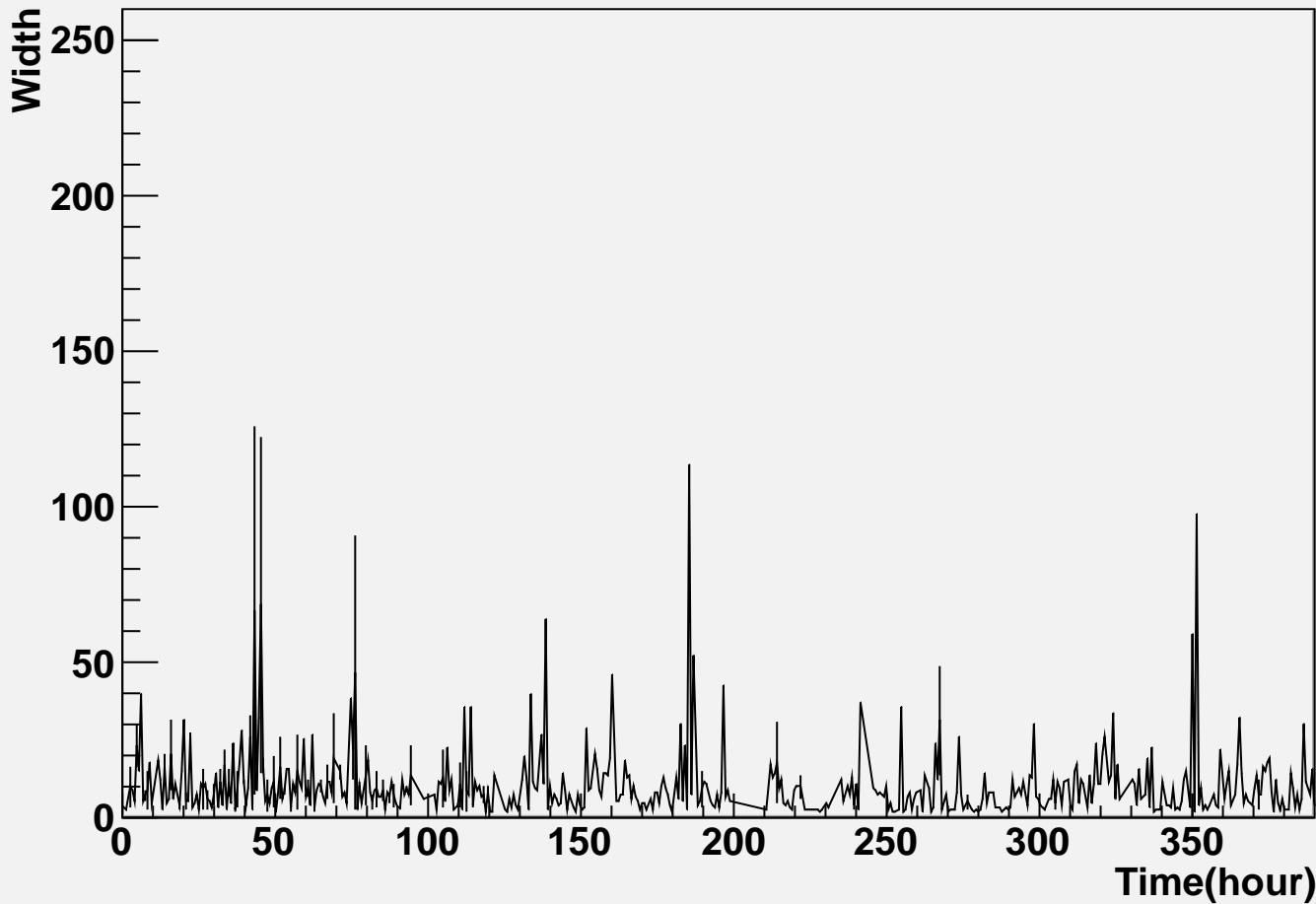
width-S2-SFET2A-Ch8



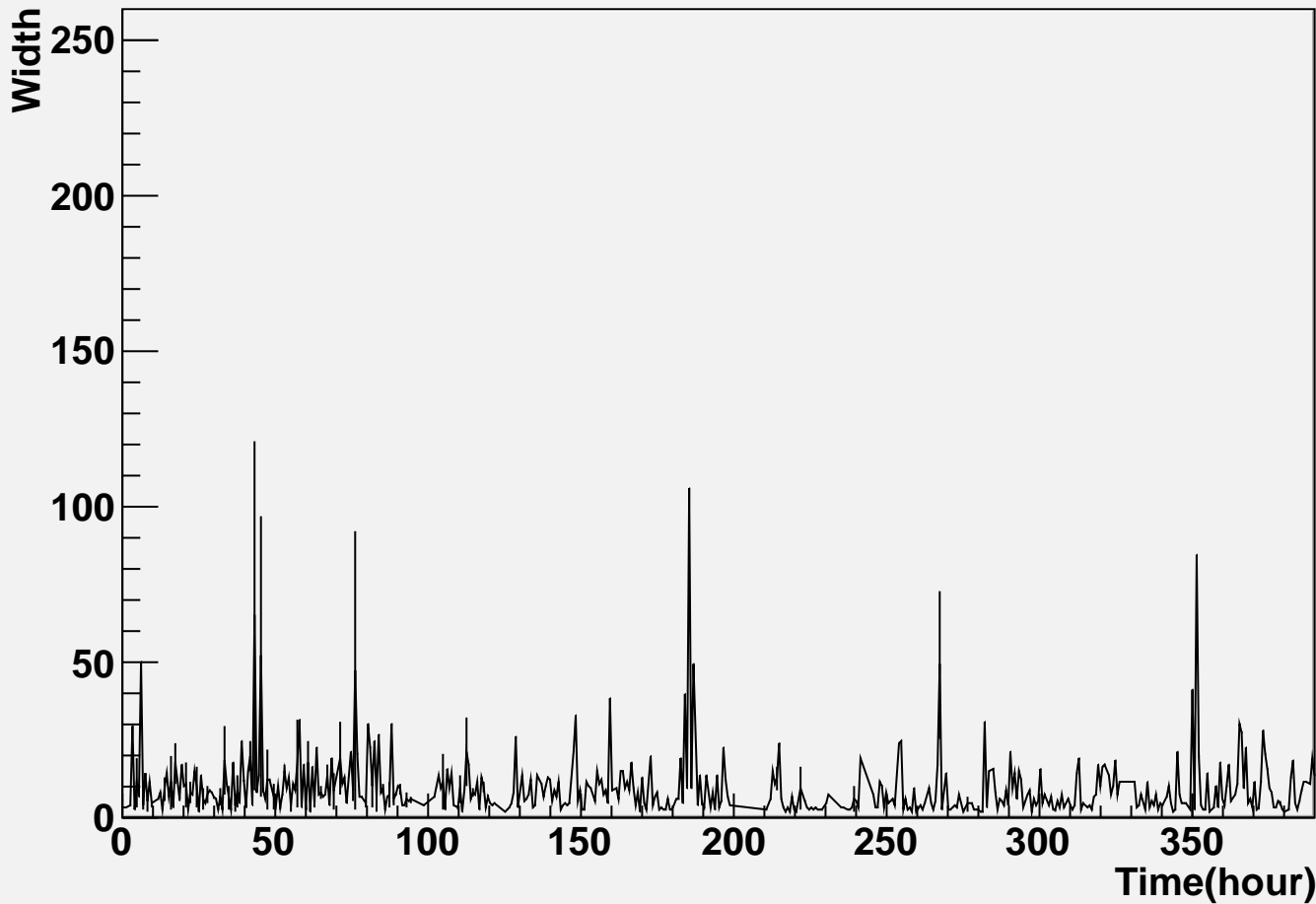
width-S2-SFET2A-Ch9



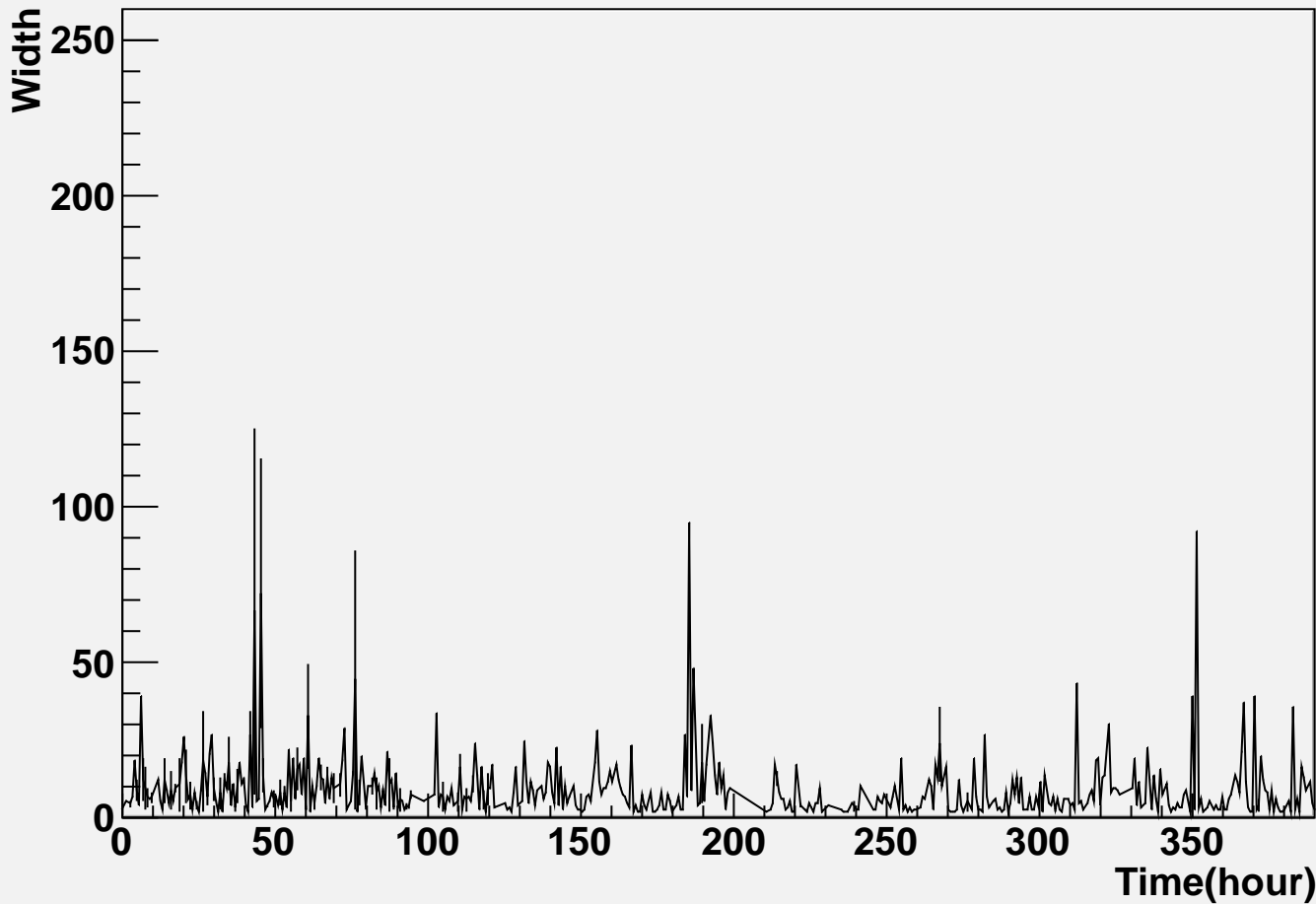
width-S2-SFET2B-Ch0



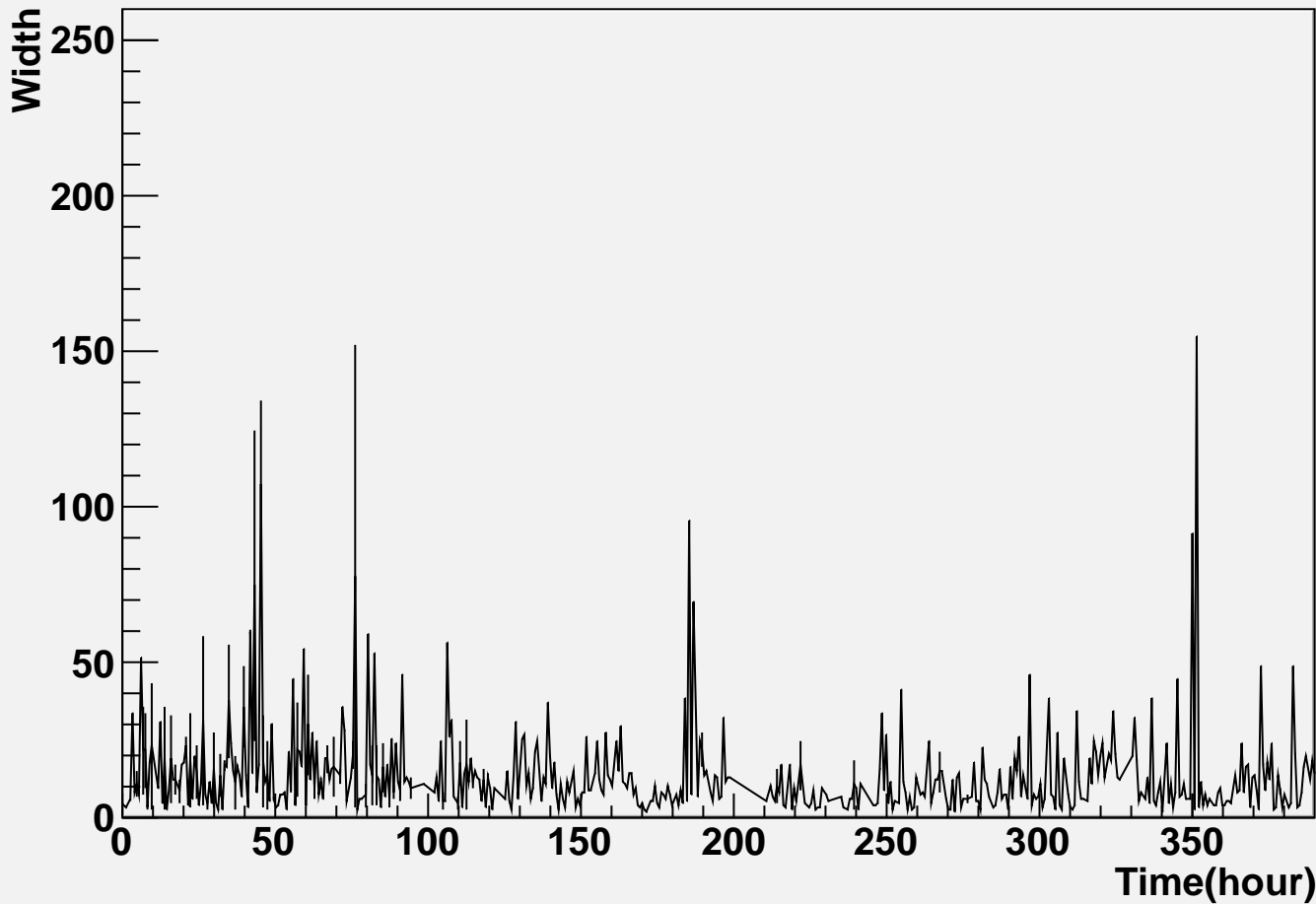
width-S2-SFET2B-Ch1



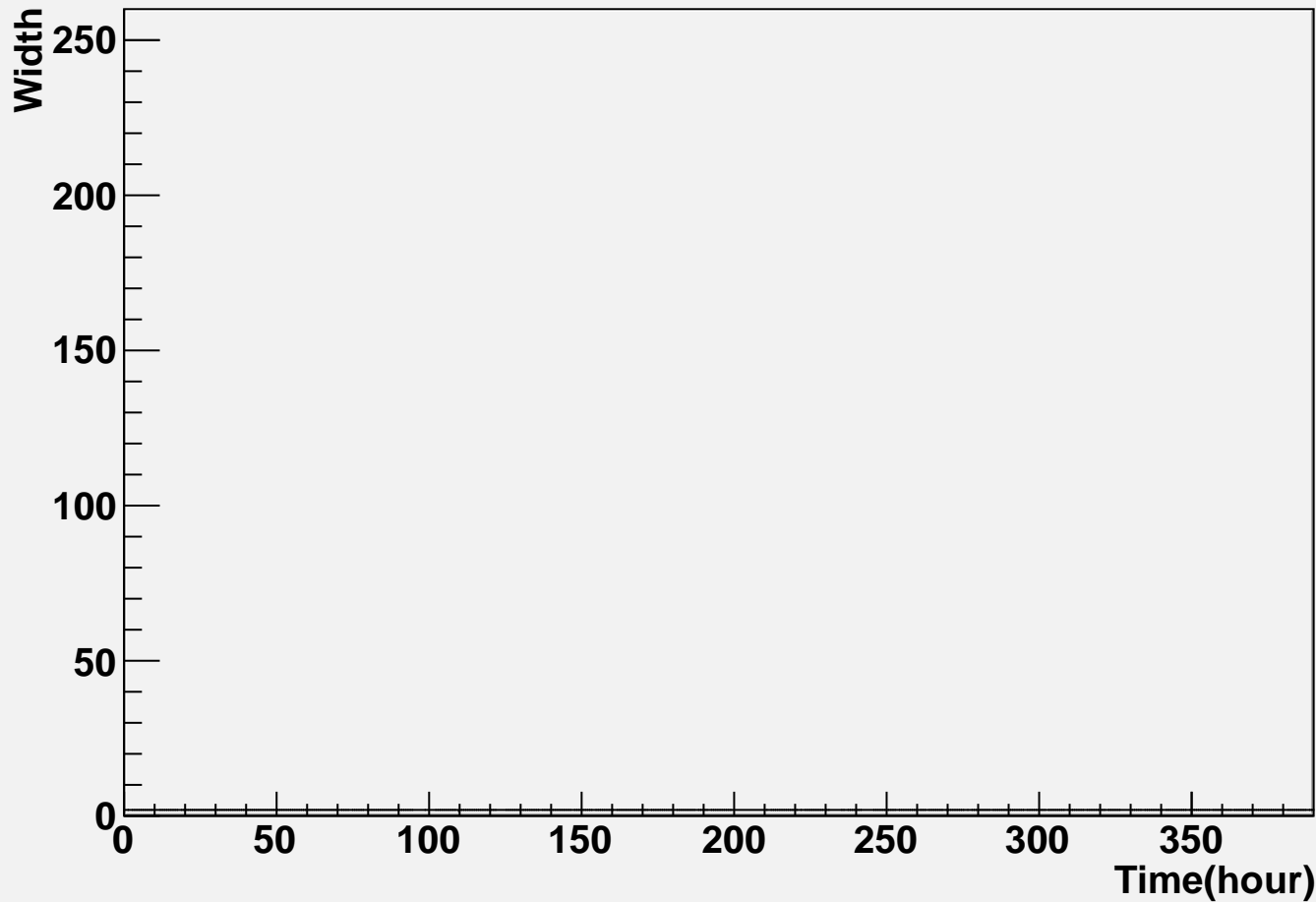
width-S2-SFET2B-Ch2



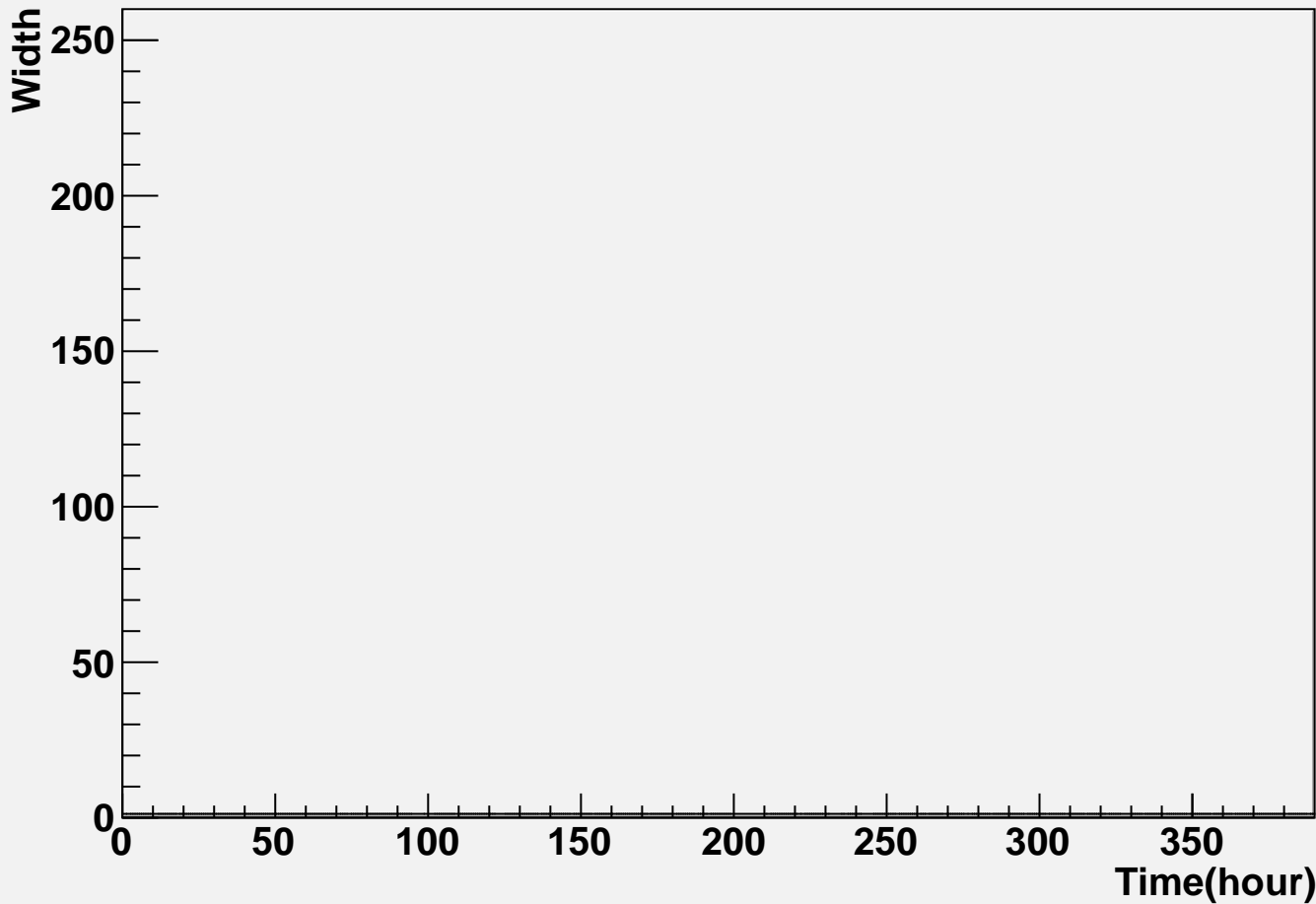
width-S2-SFET2B-Ch3



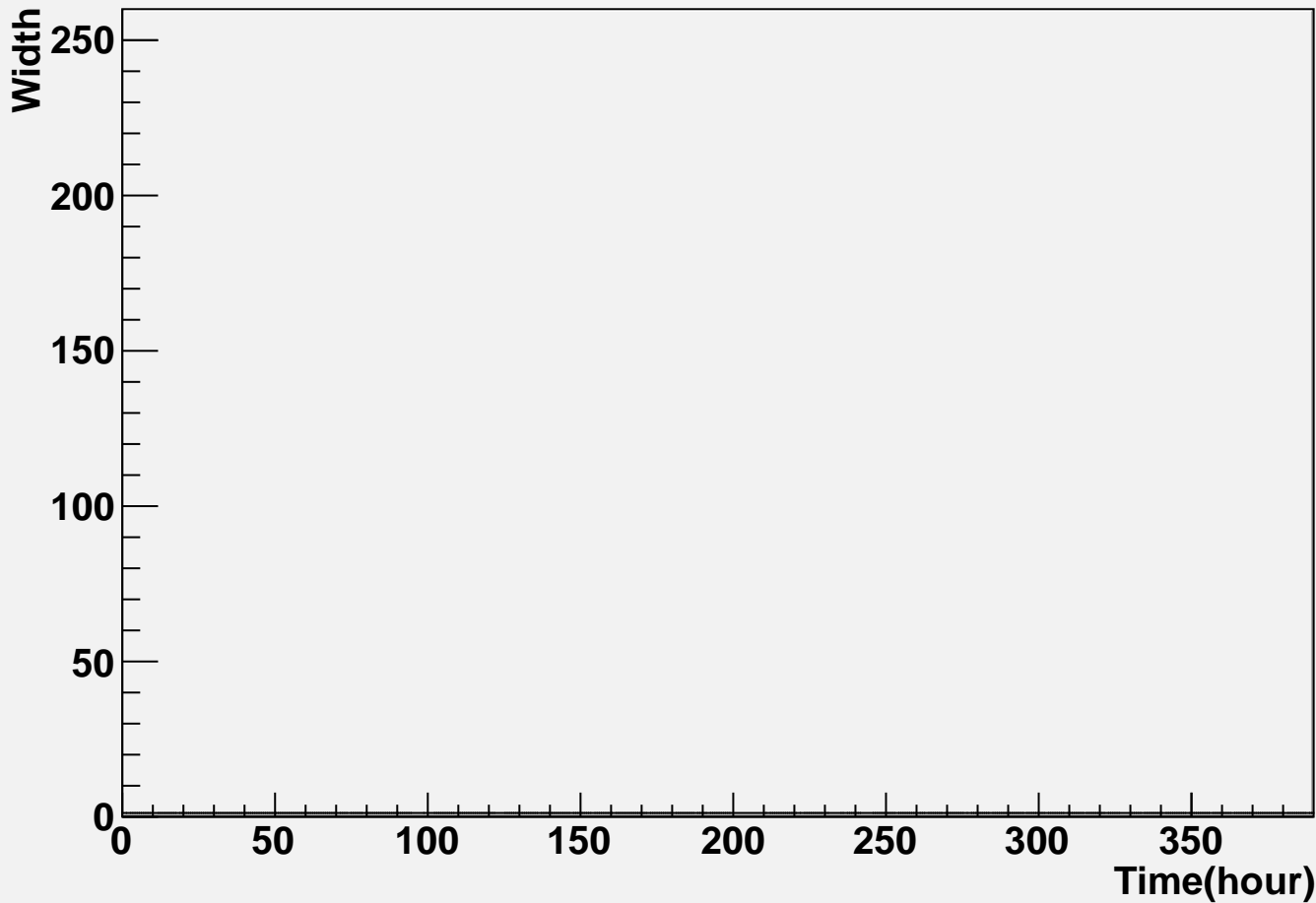
width-S2-SFET2B-Ch4



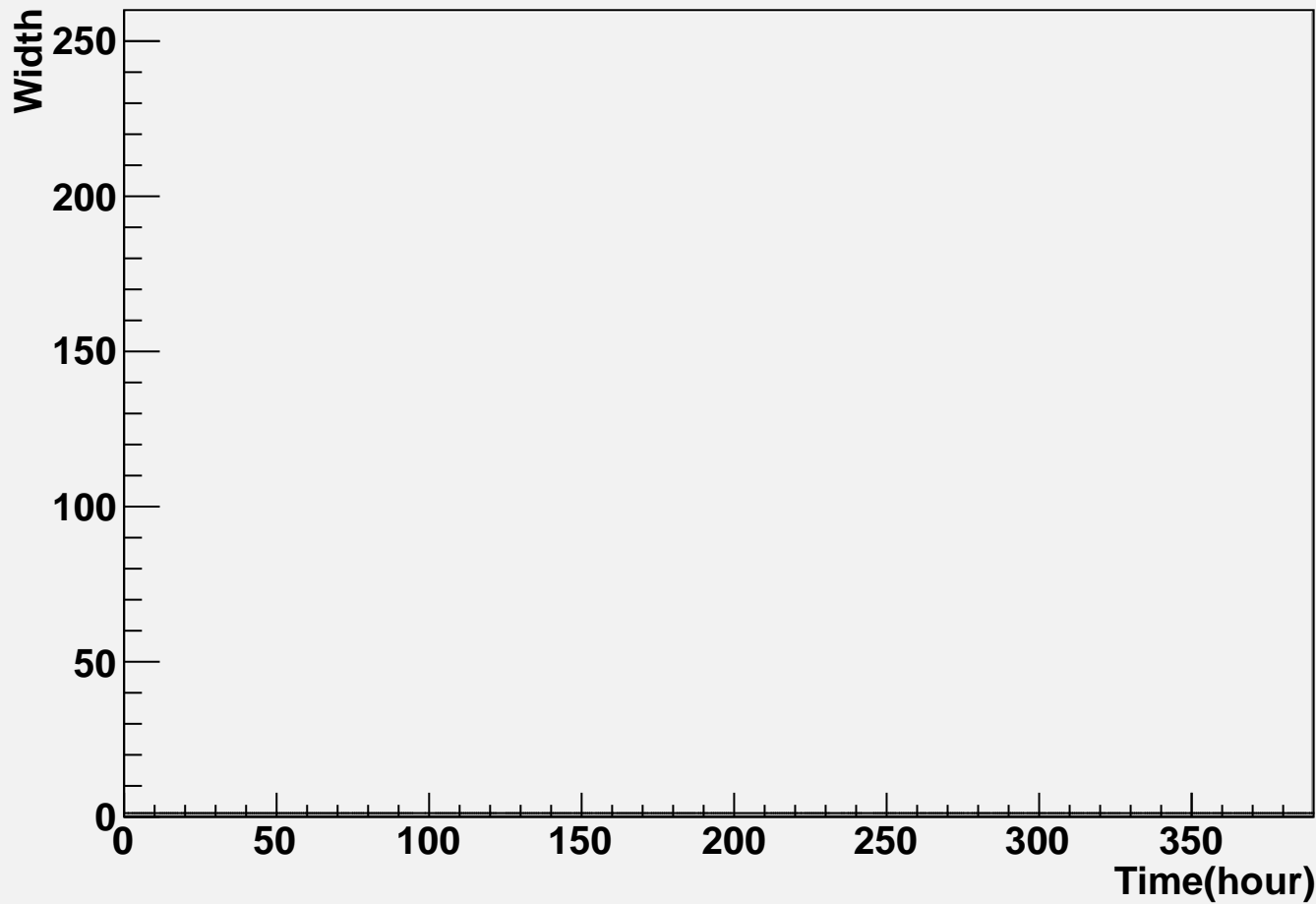
width-S2-SFET2B-Ch5



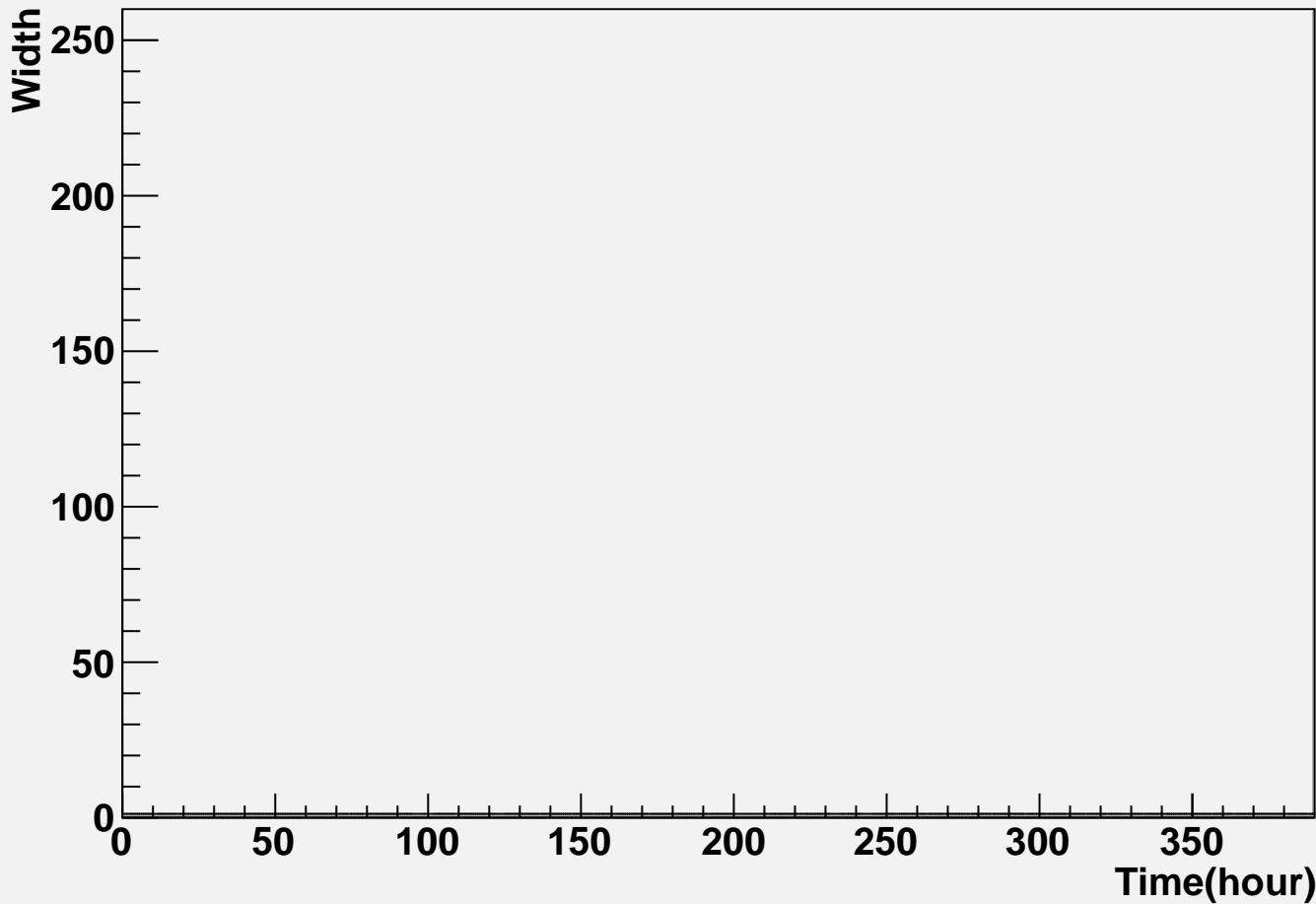
width-S2-SFET2B-Ch6



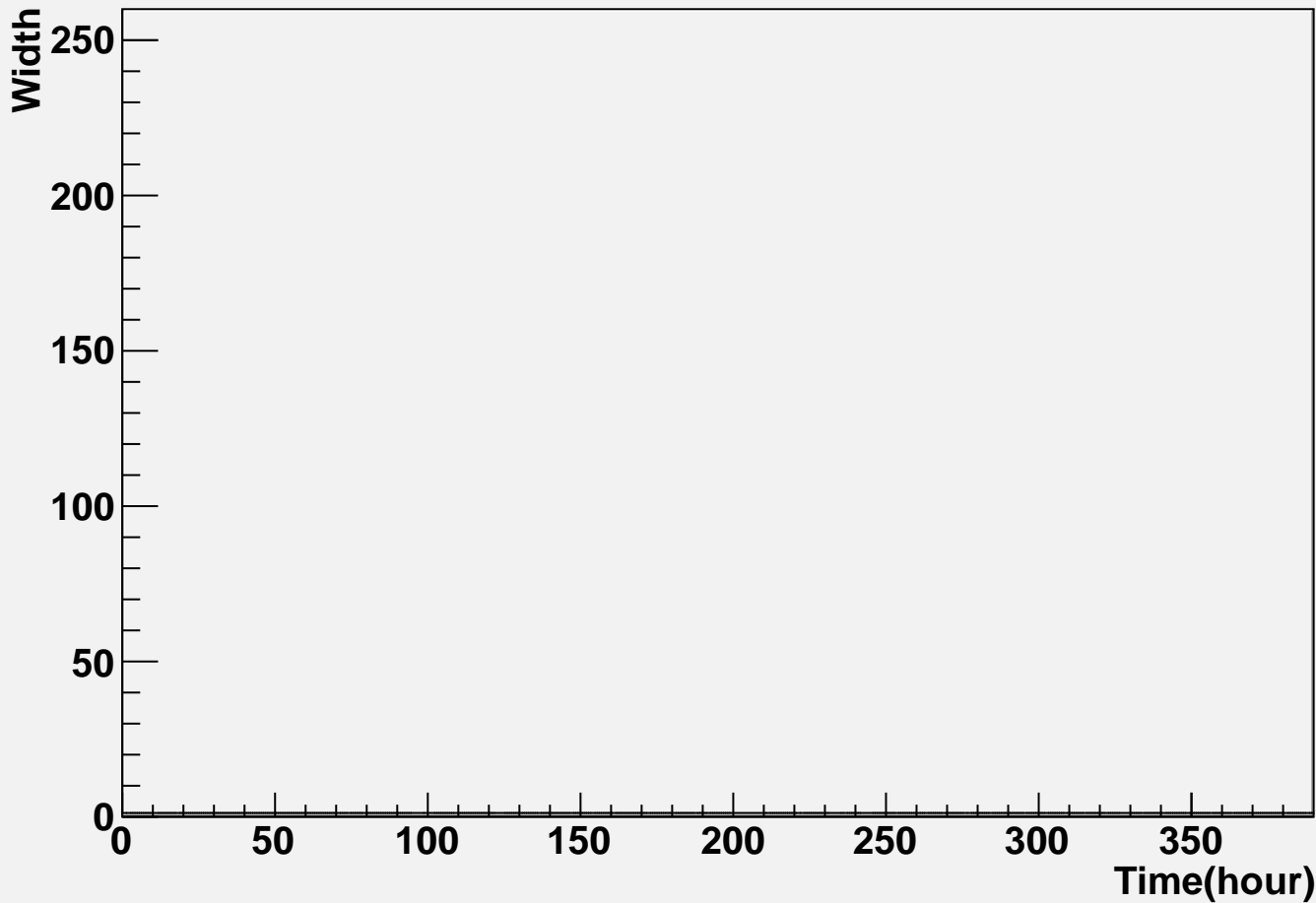
width-S2-SFET2B-Ch7



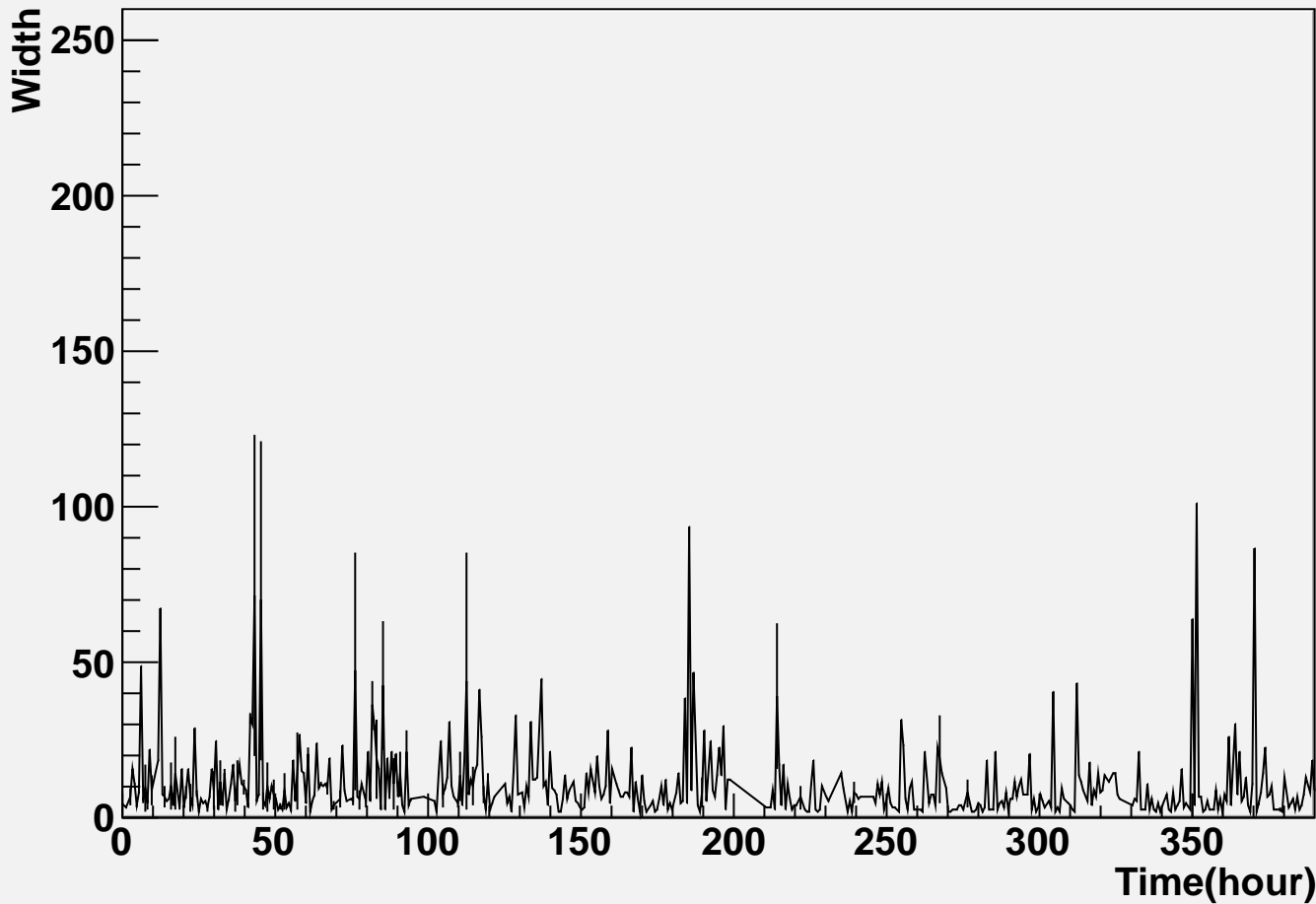
width-S2-SFET2B-Ch8



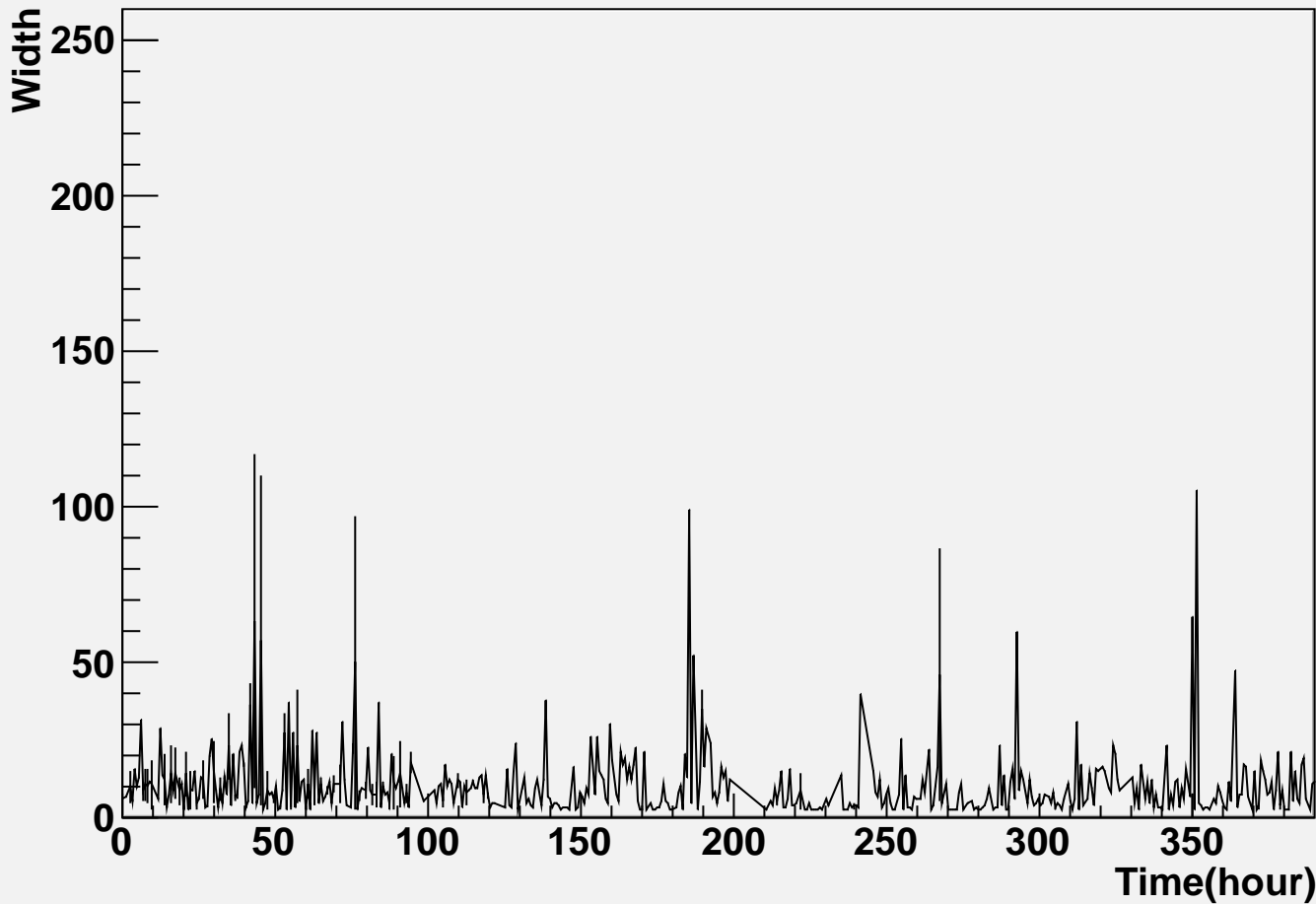
width-S2-SFET2B-Ch9



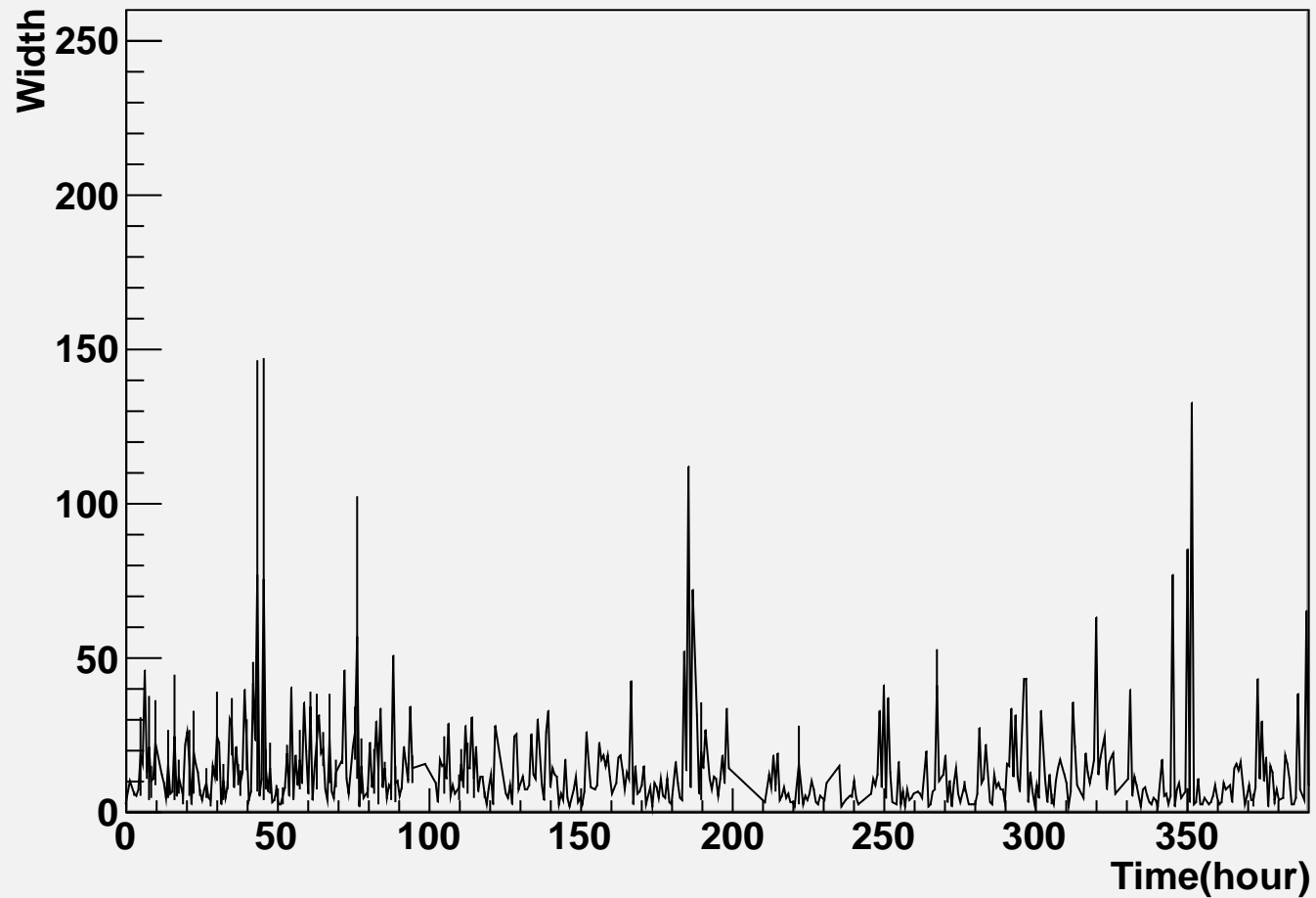
width-S2-SFET2C-Ch0



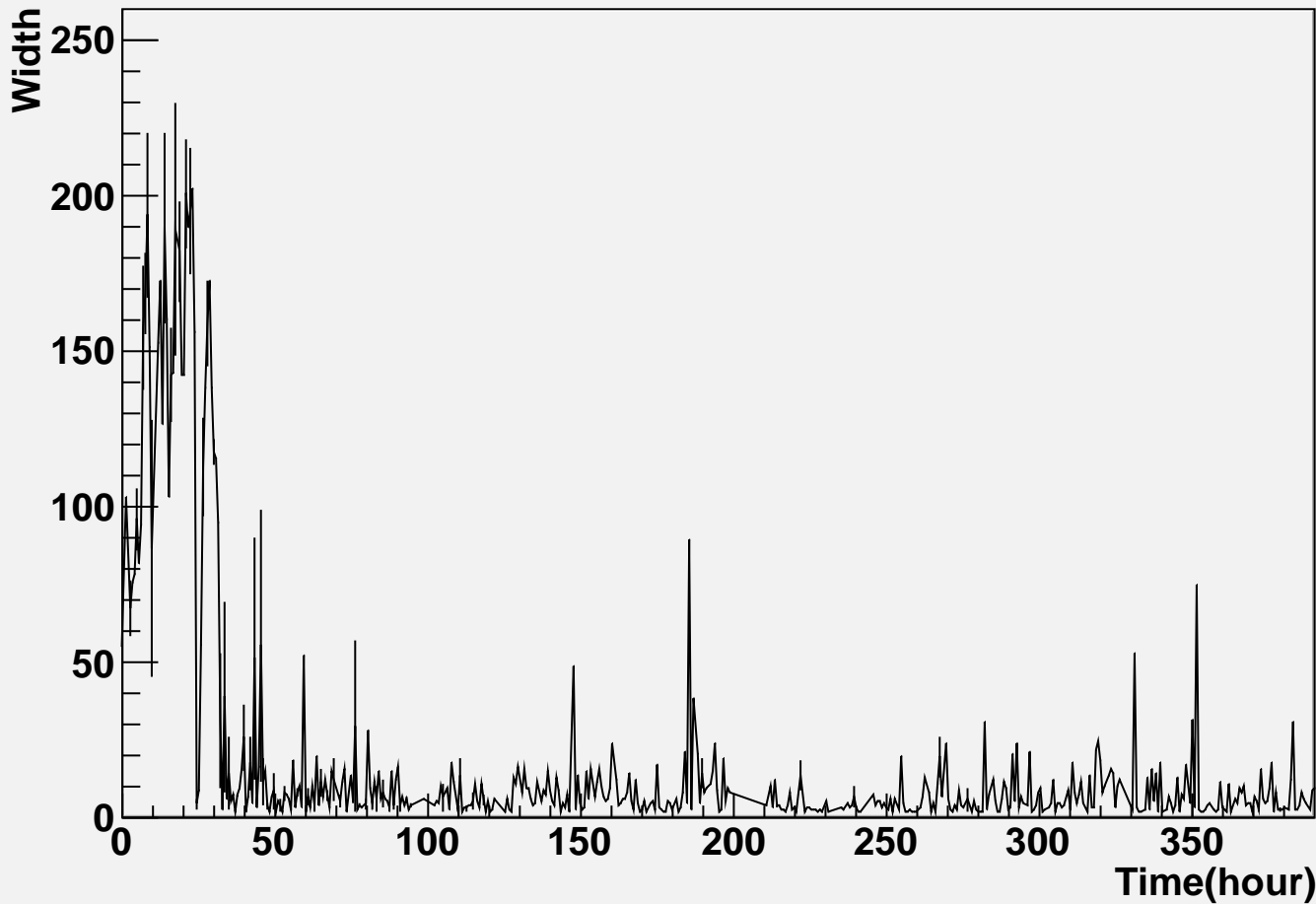
width-S2-SFET2C-Ch1



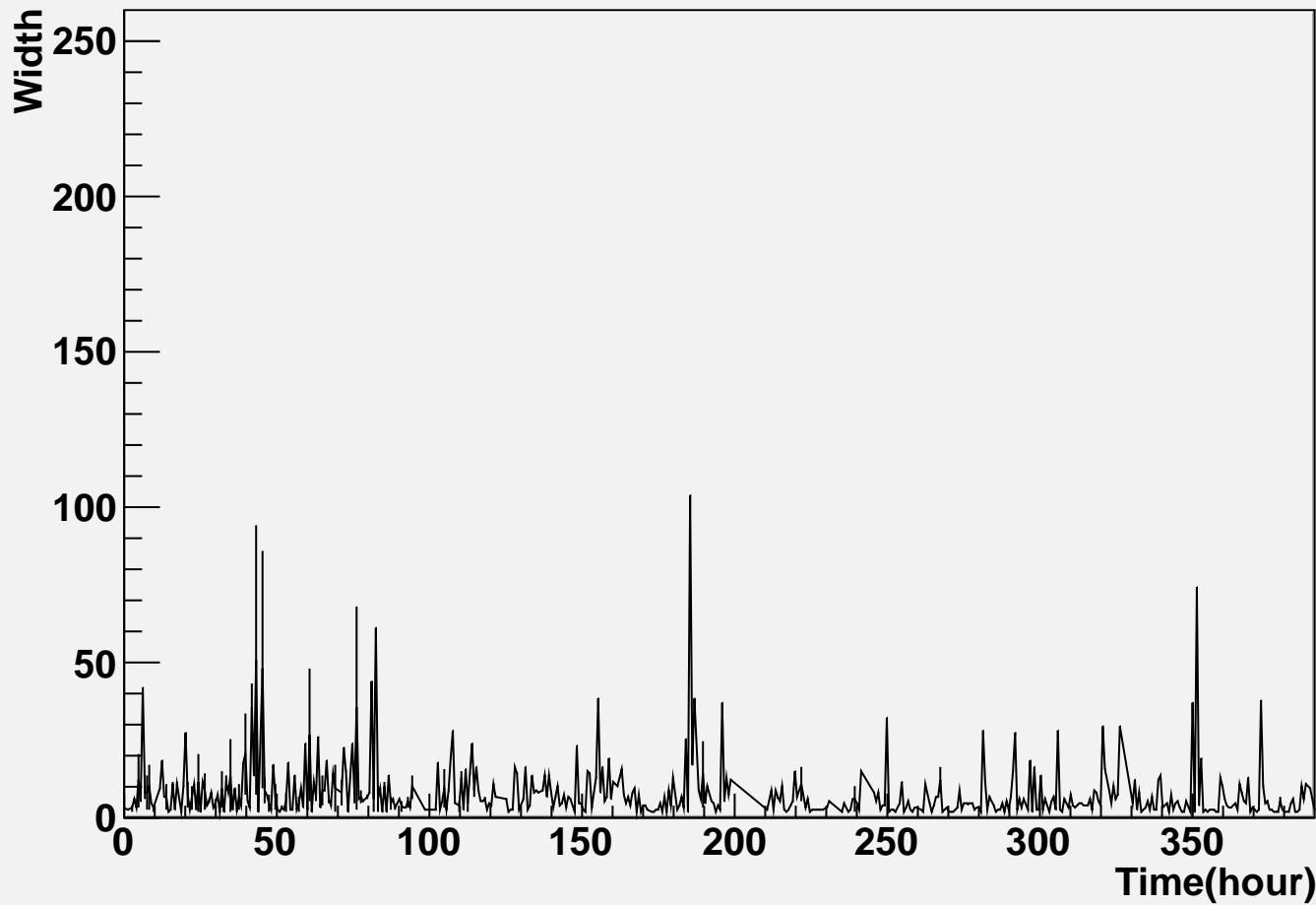
width-S2-SFET2C-Ch2



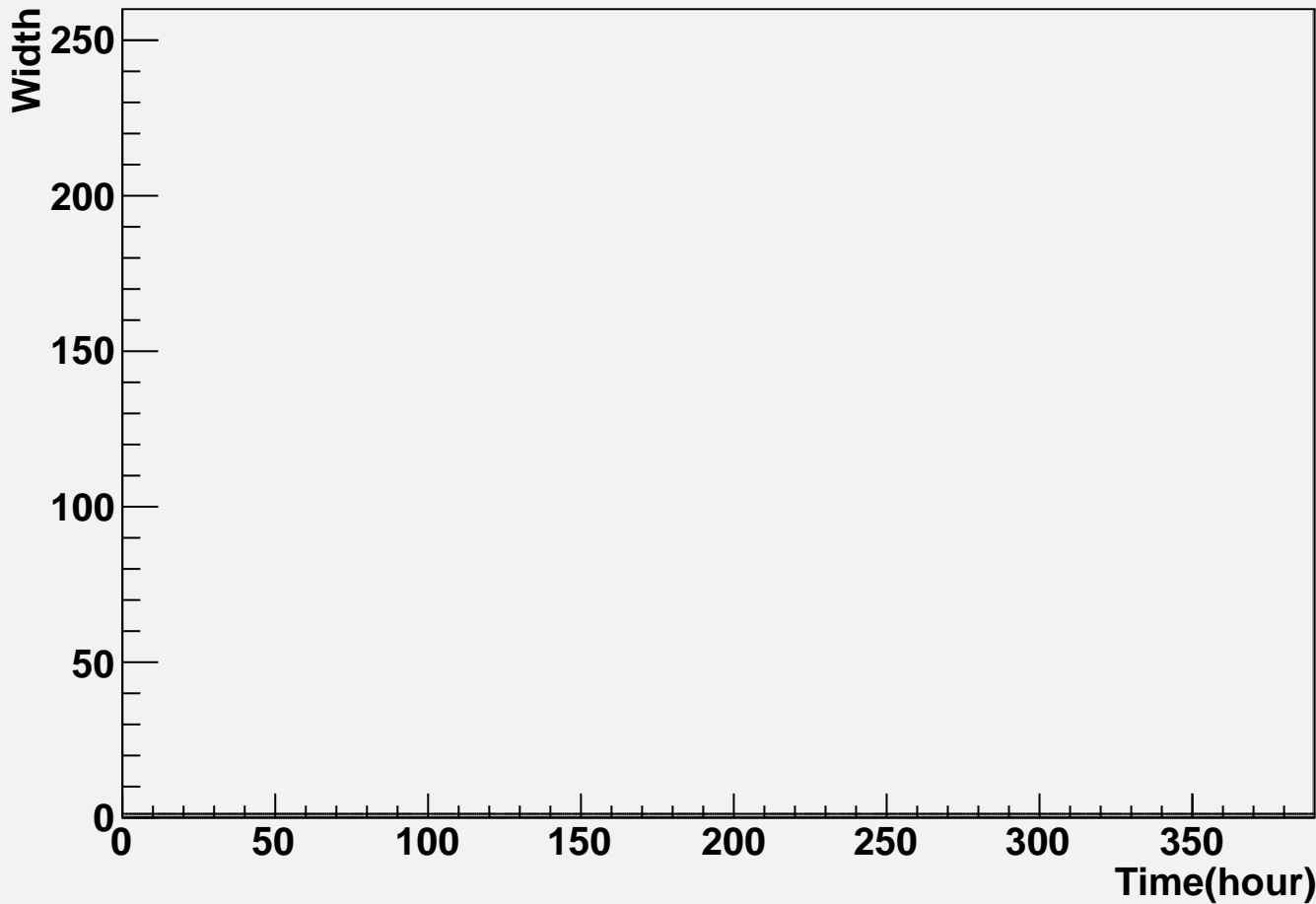
width-S2-SFET2C-Ch3



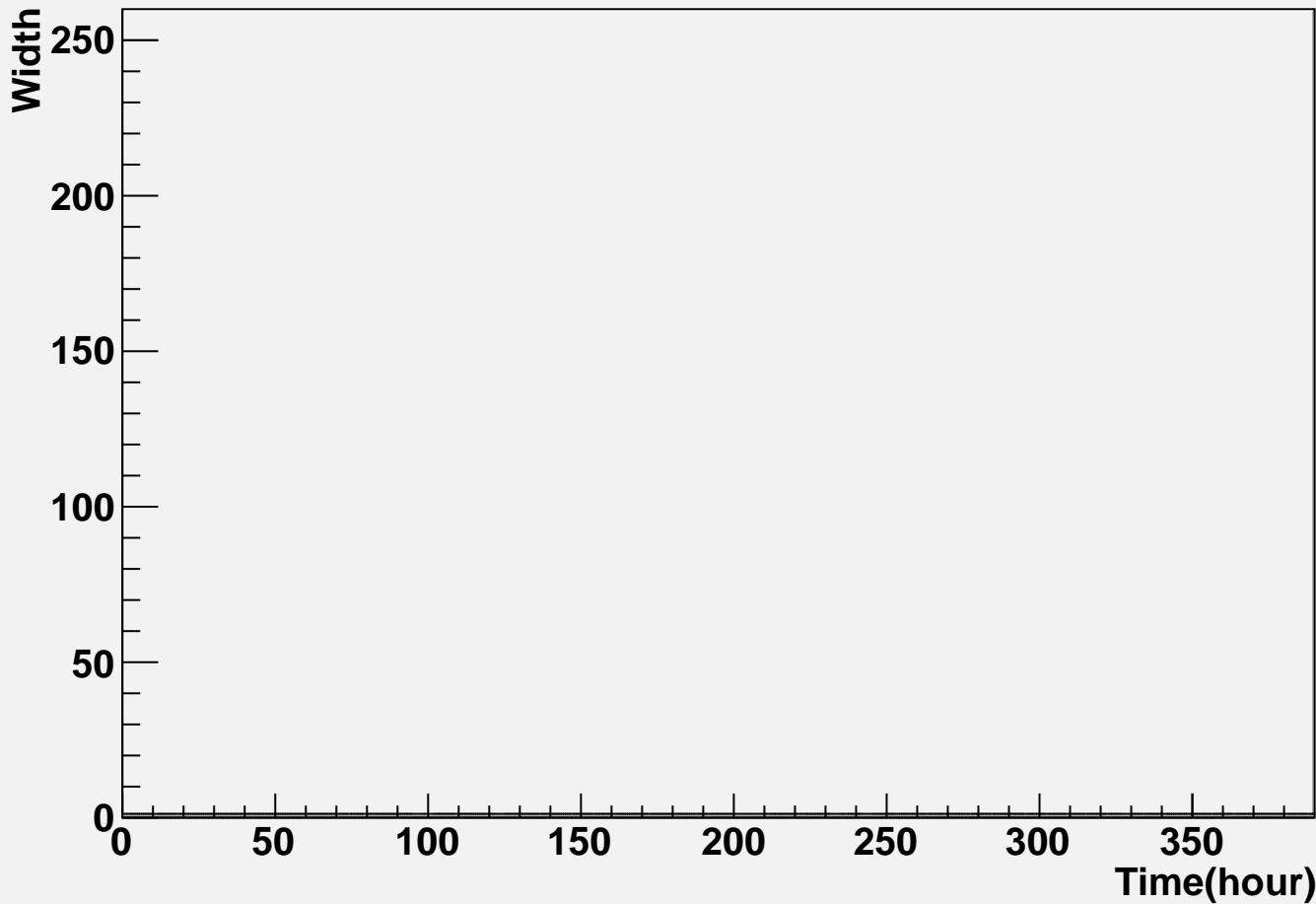
width-S2-SFET2C-Ch4



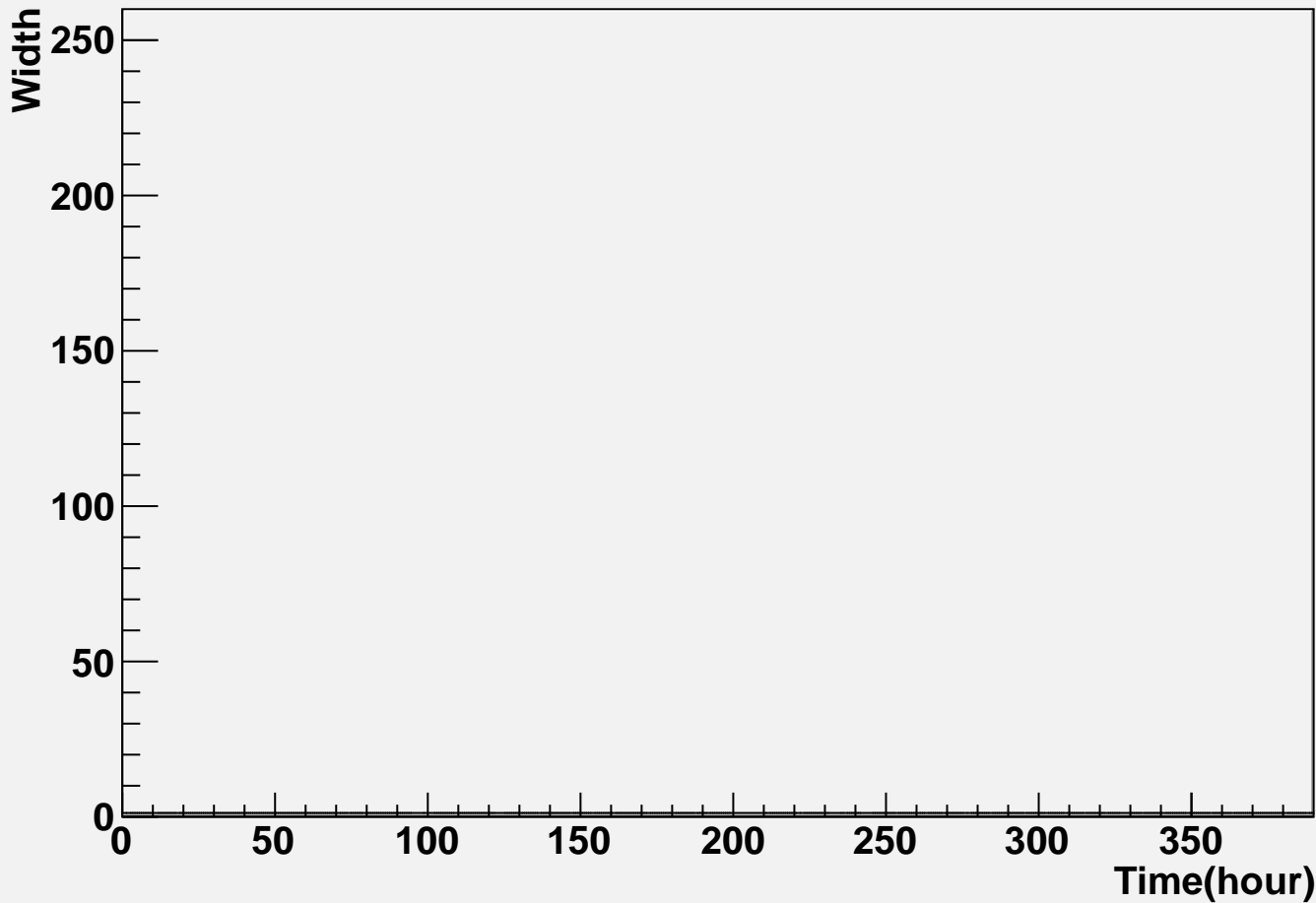
width-S2-SFET2C-Ch5



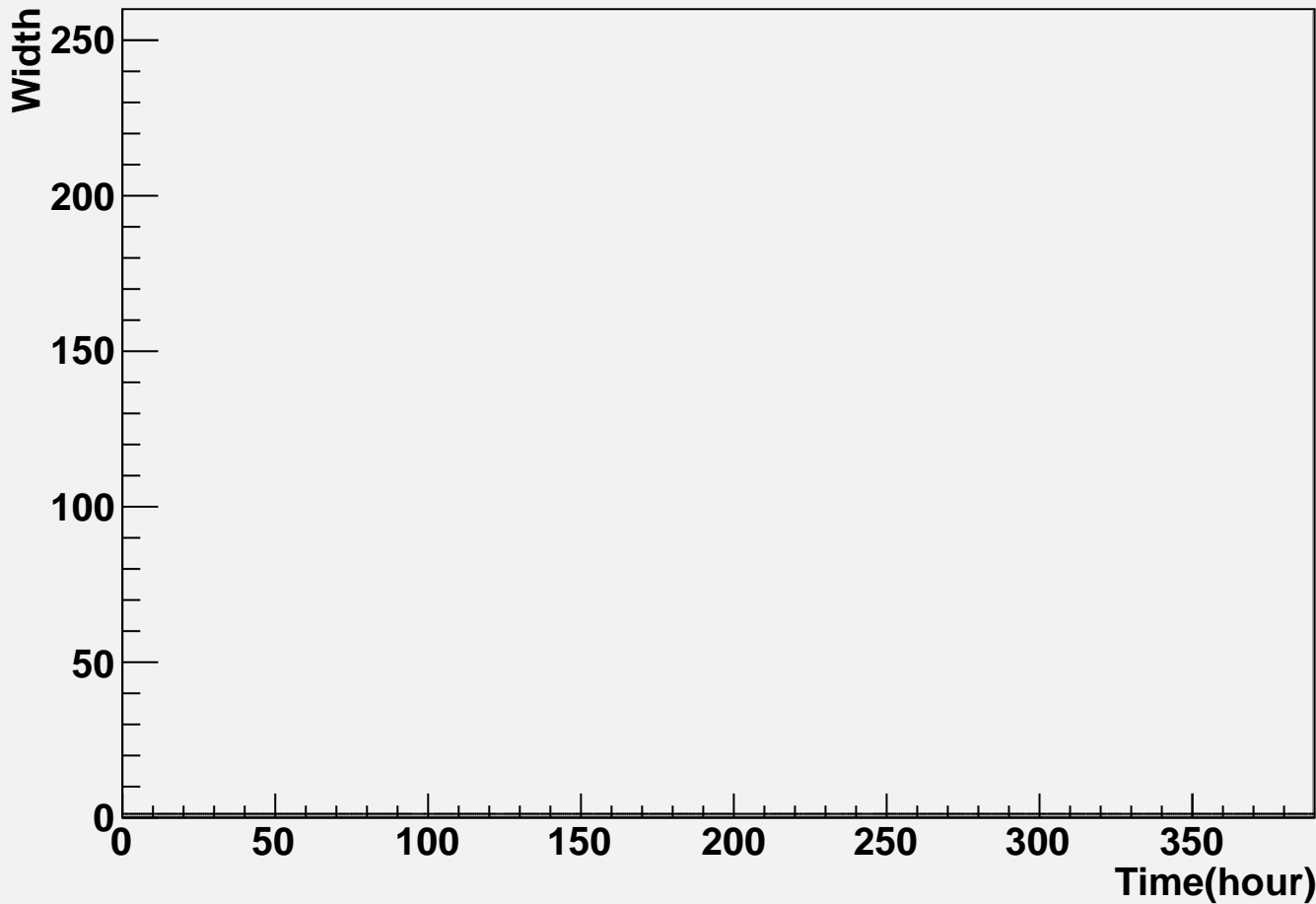
width-S2-SFET2C-Ch6



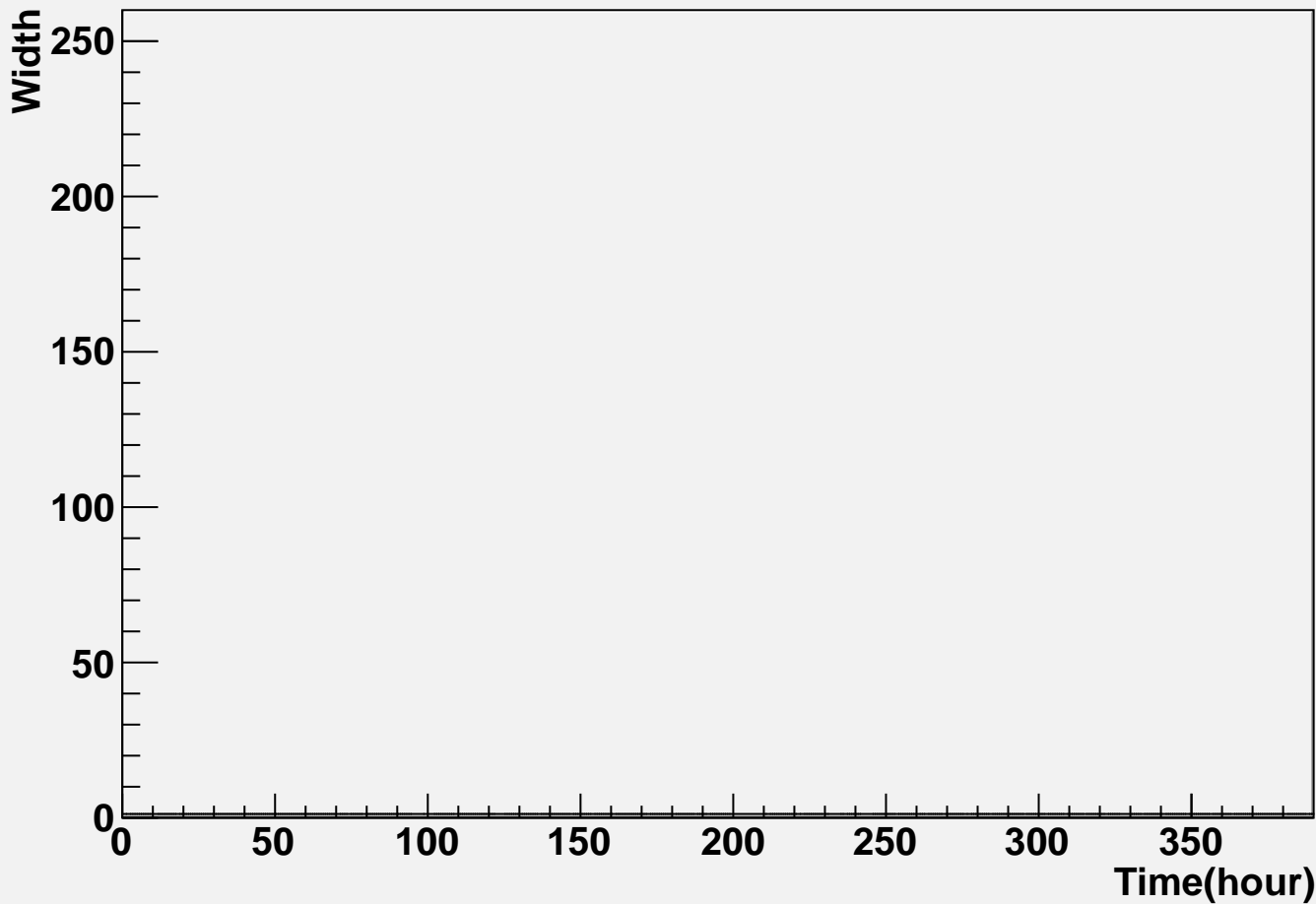
width-S2-SFET2C-Ch7



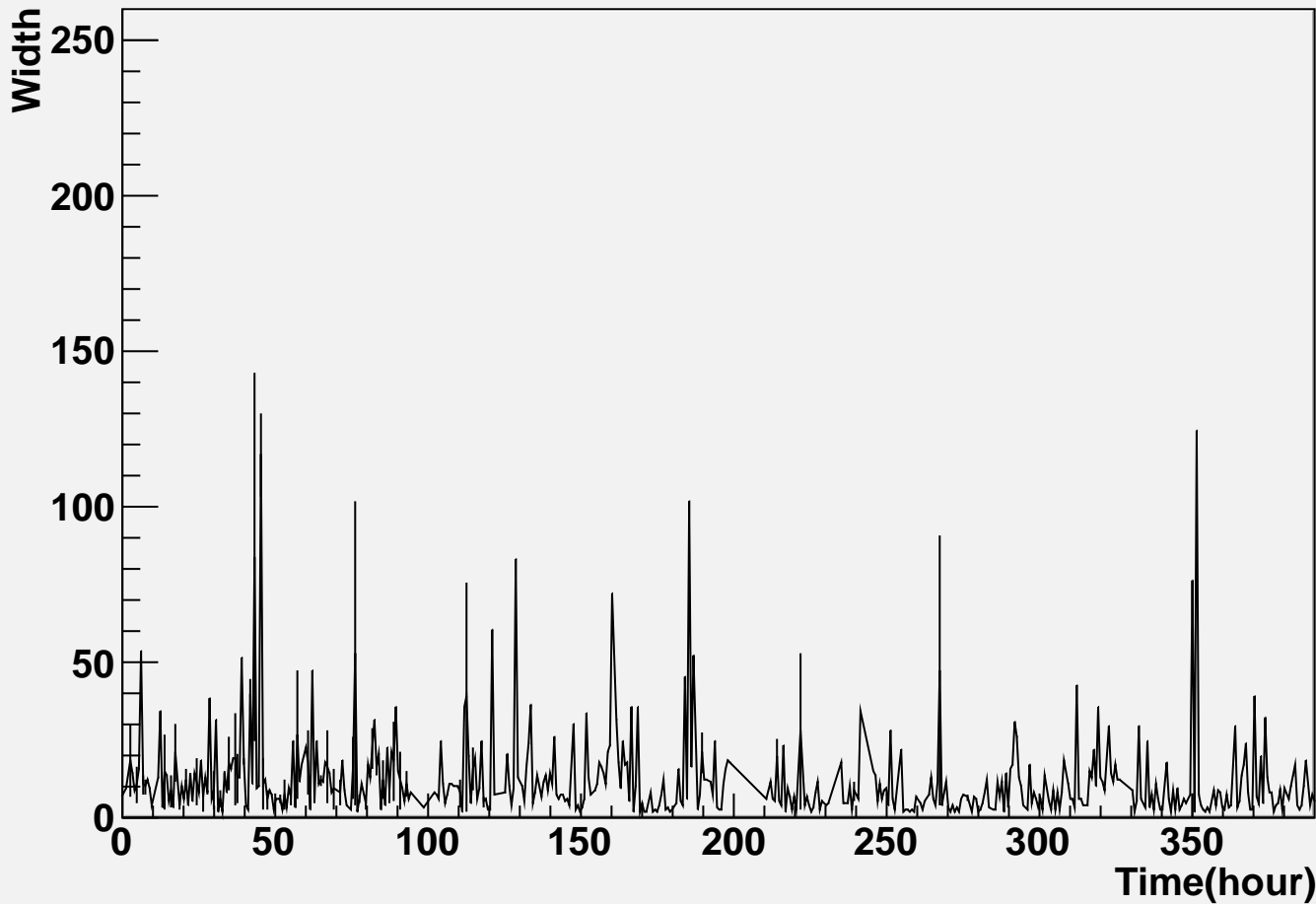
width-S2-SFET2C-Ch8



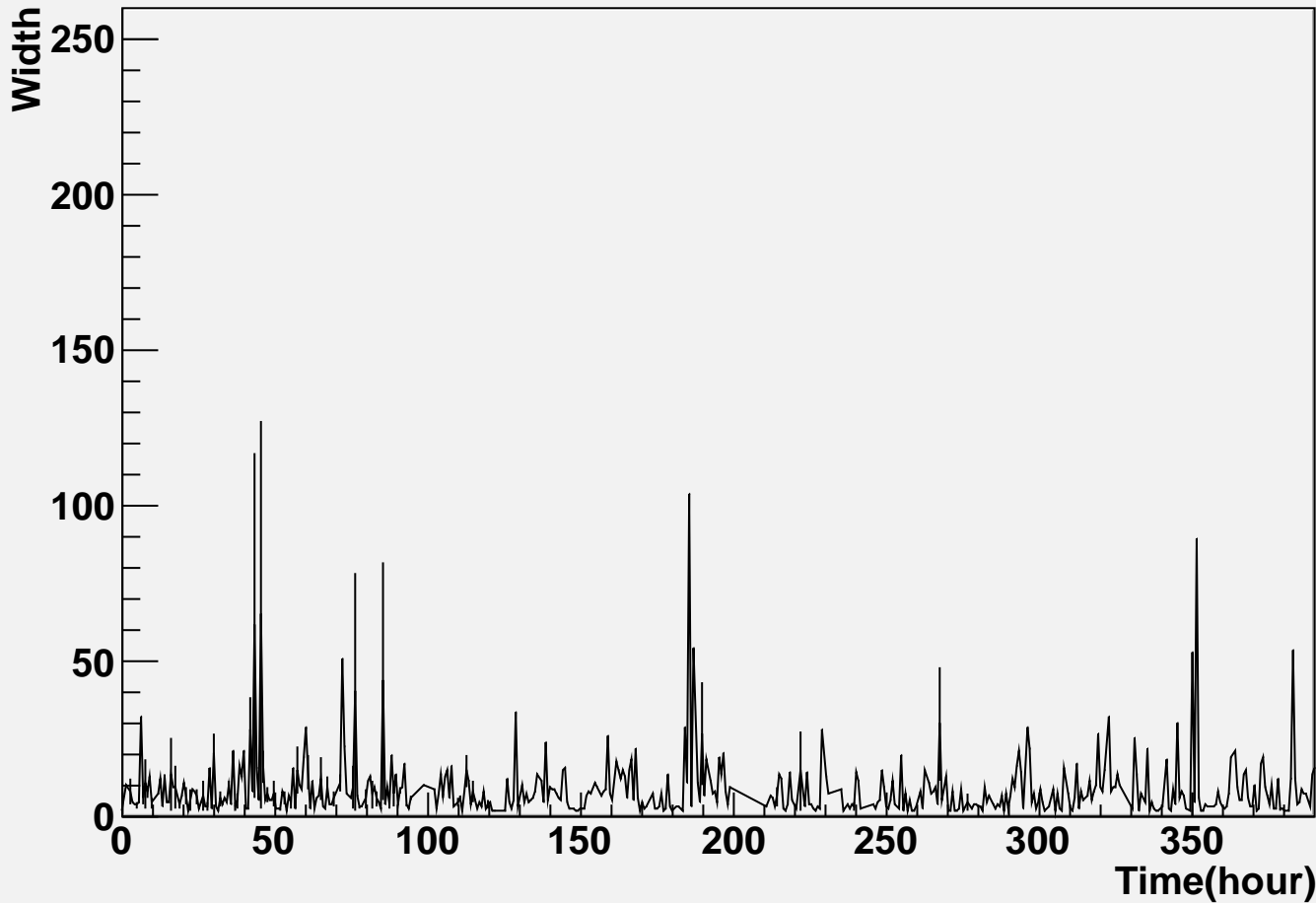
width-S2-SFET2C-Ch9



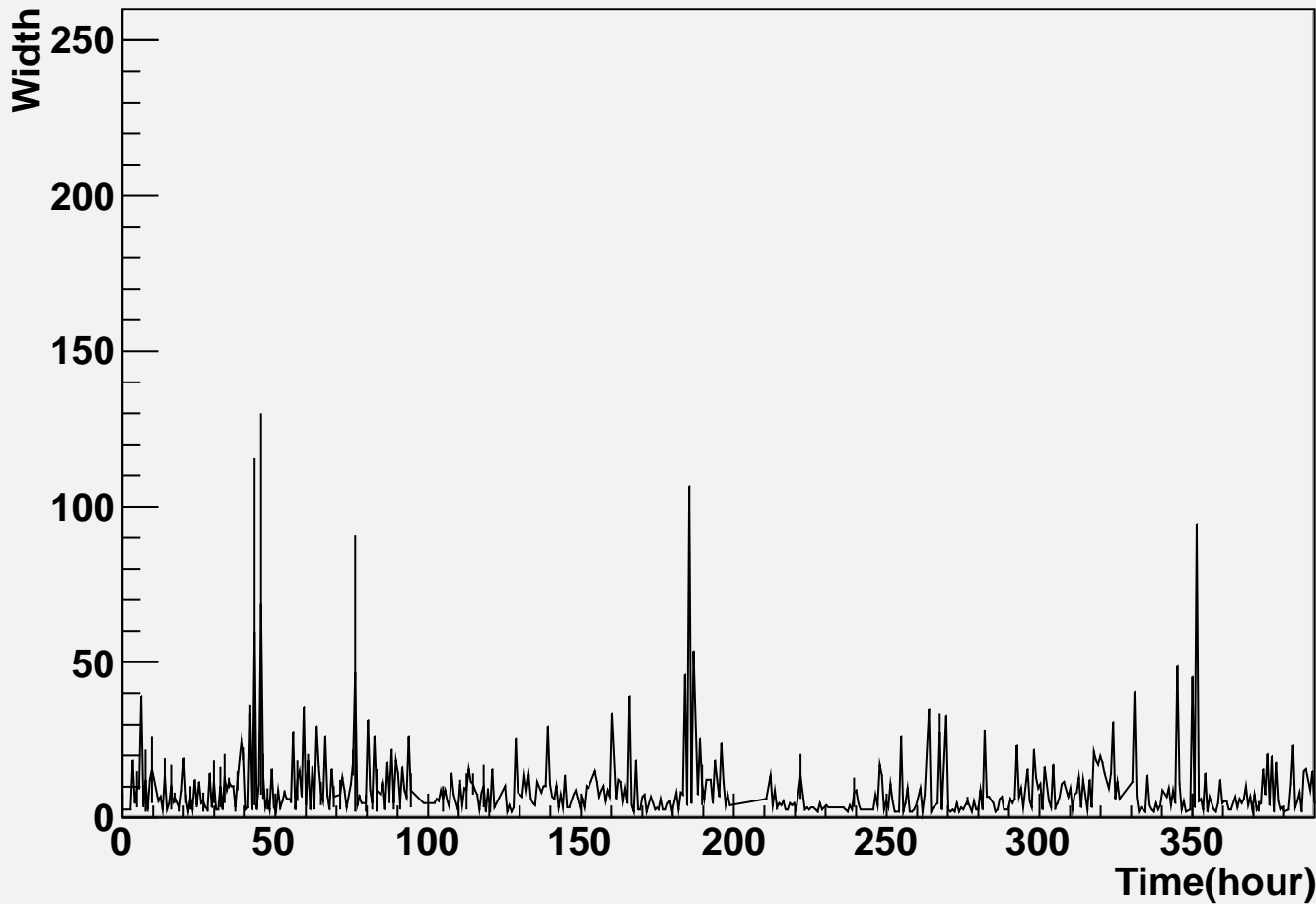
width-S2-SFET2D-Ch0



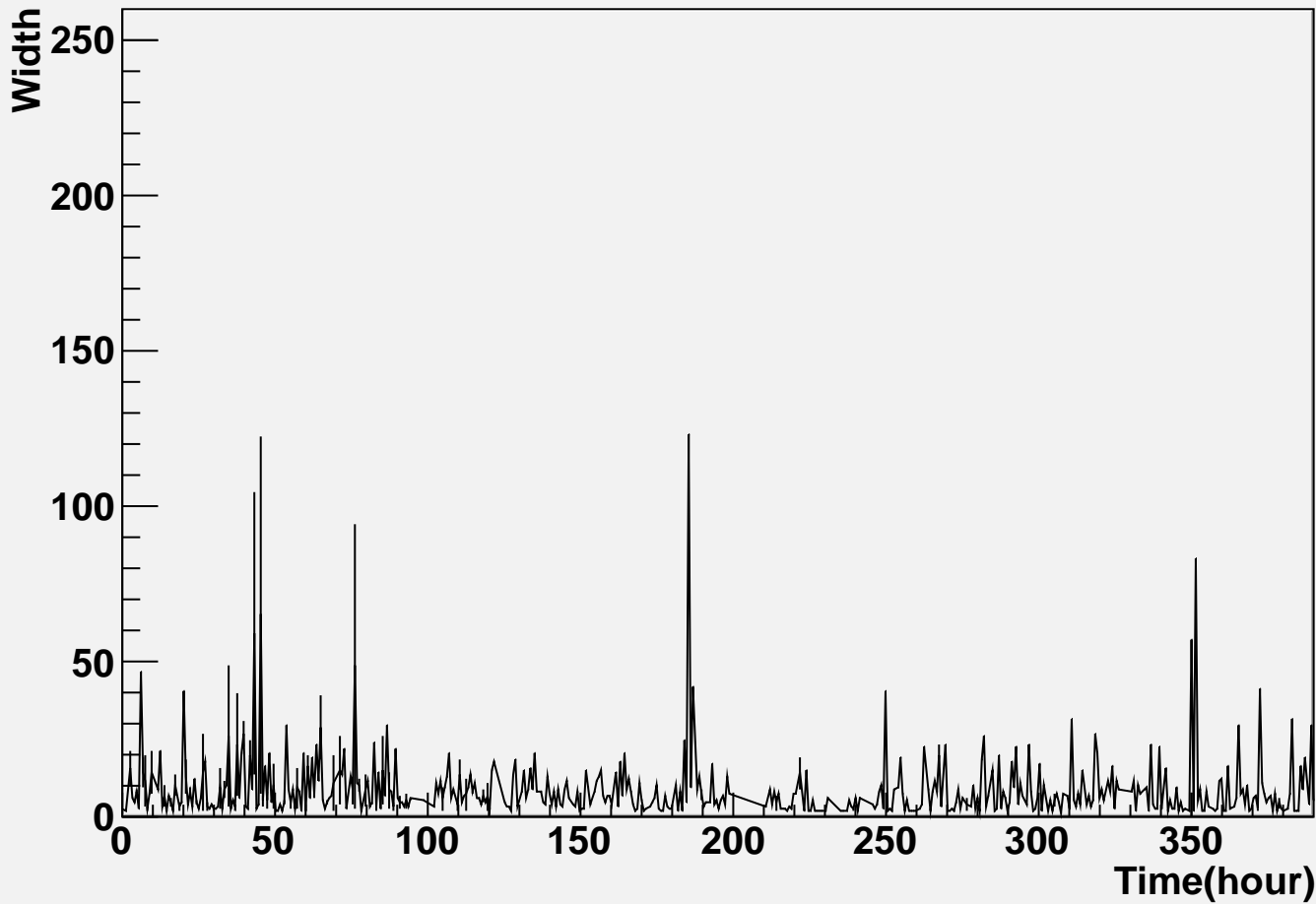
width-S2-SFET2D-Ch1



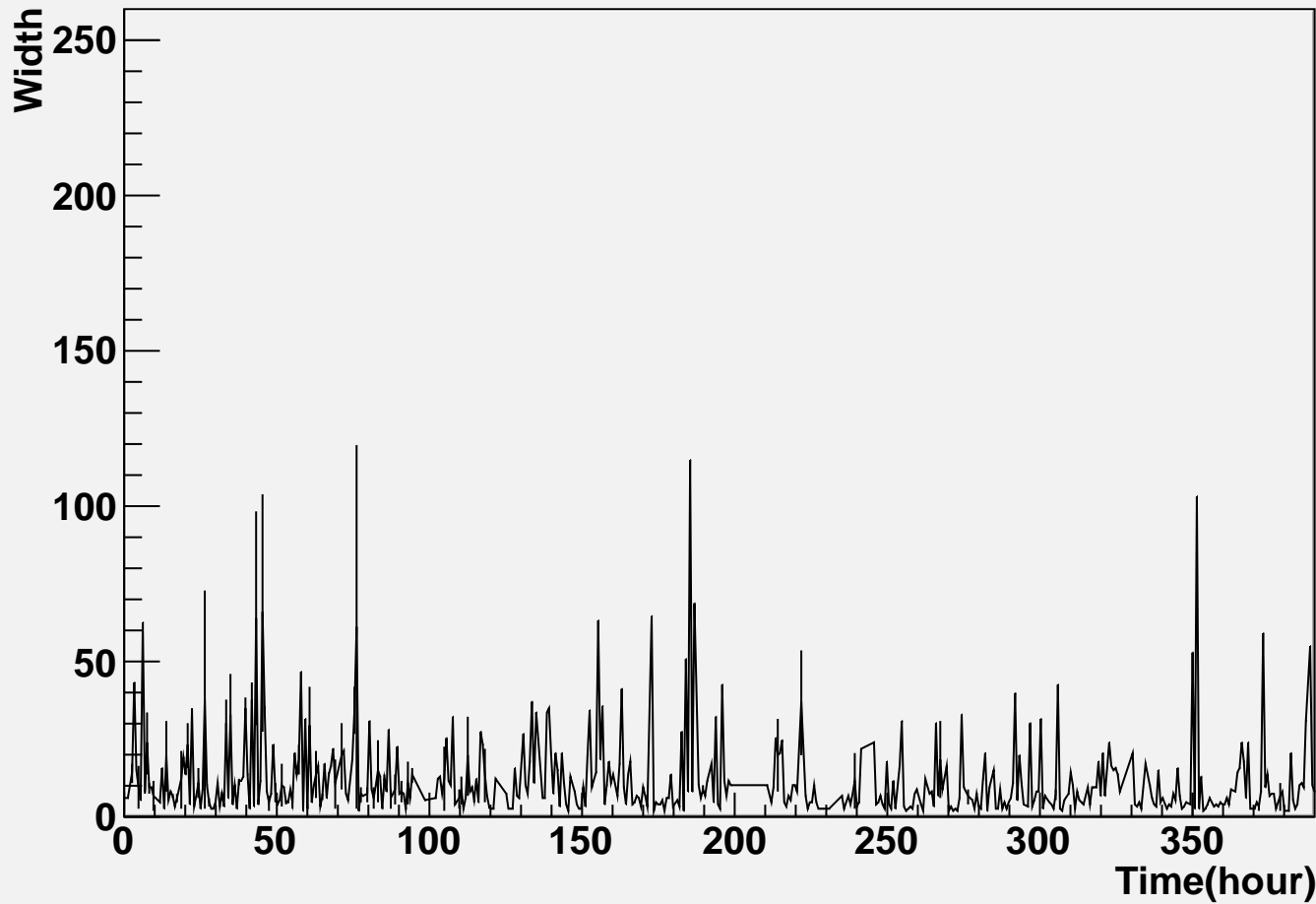
width-S2-SFET2D-Ch2



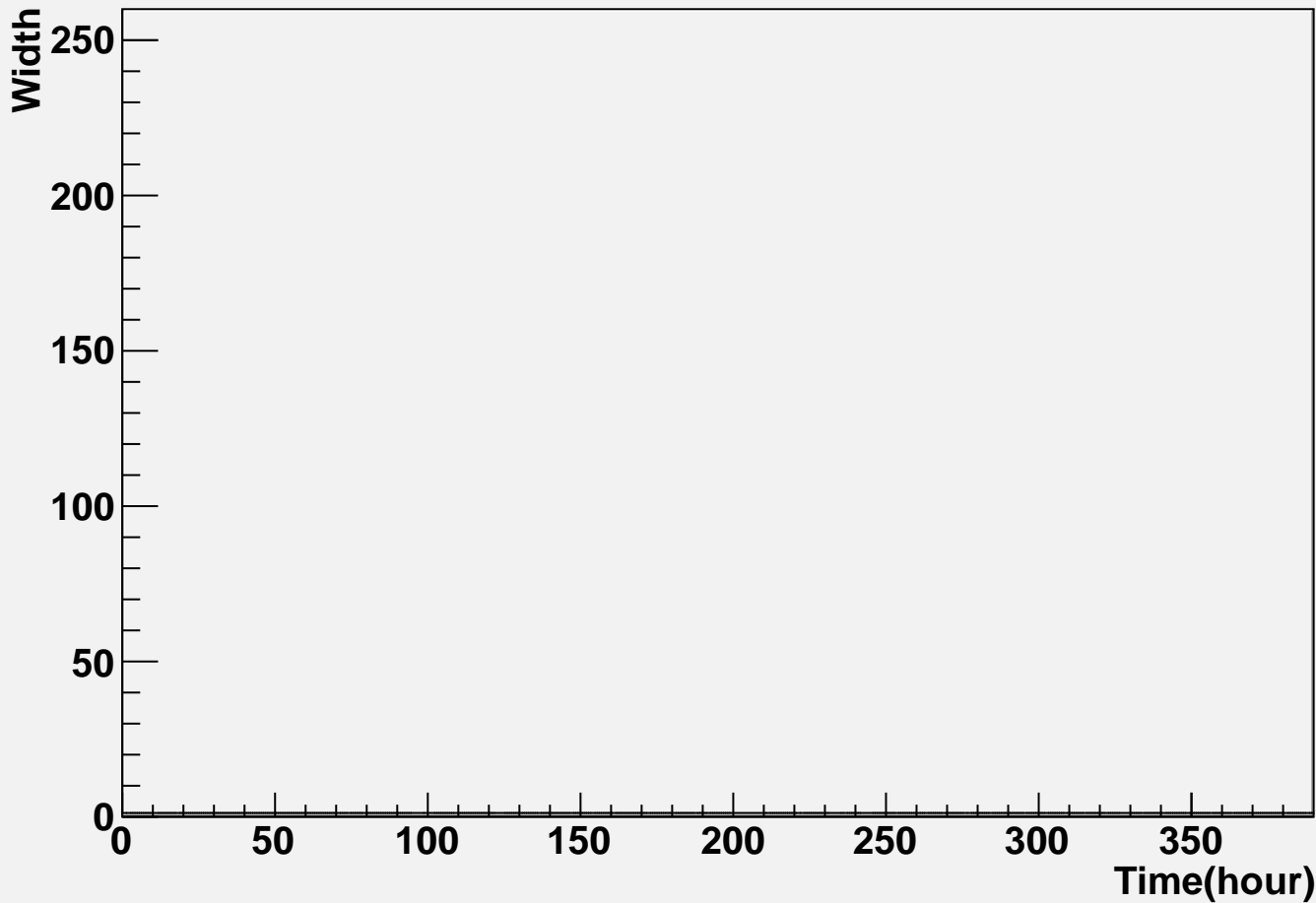
width-S2-SFET2D-Ch3



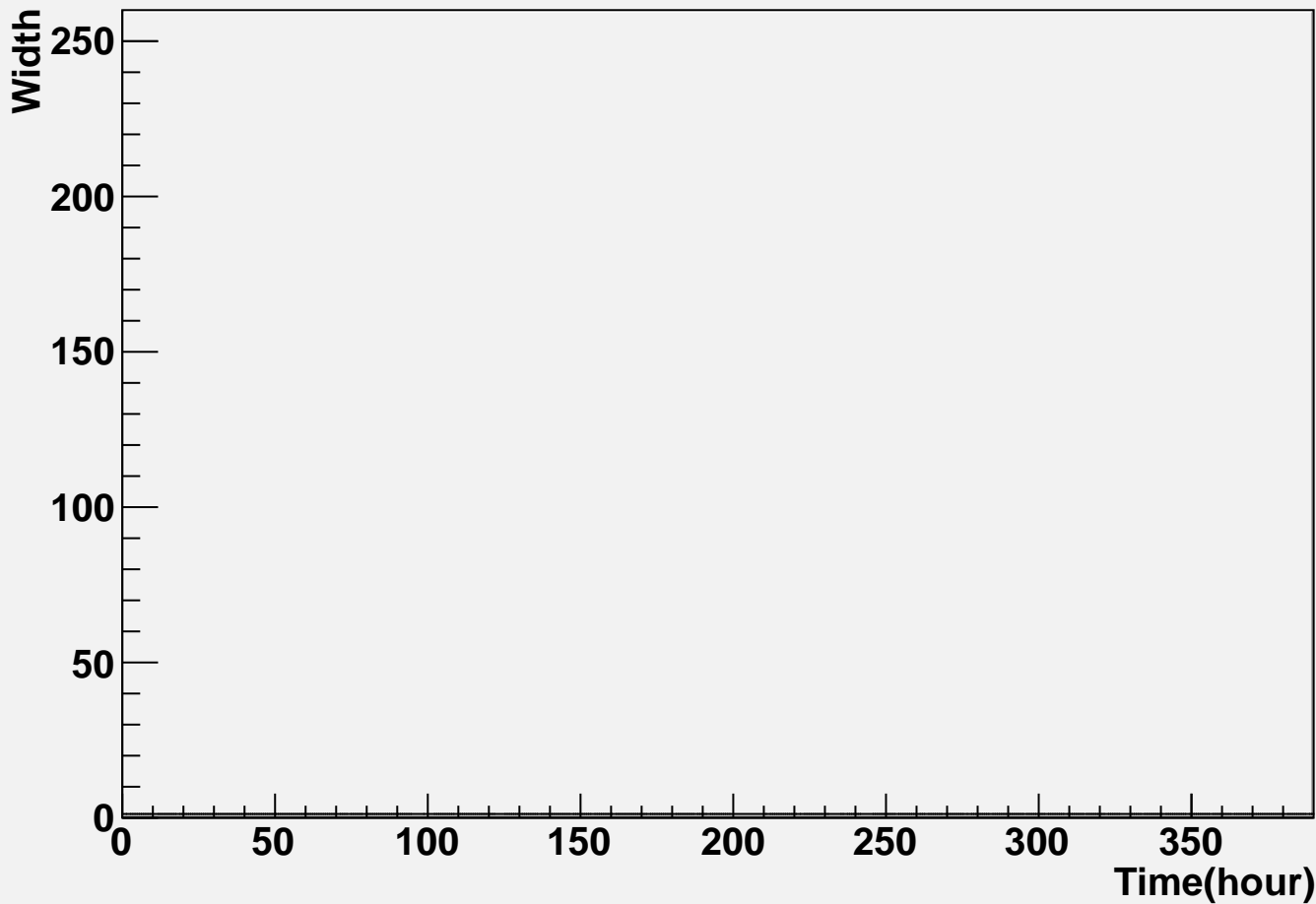
width-S2-SFET2D-Ch4



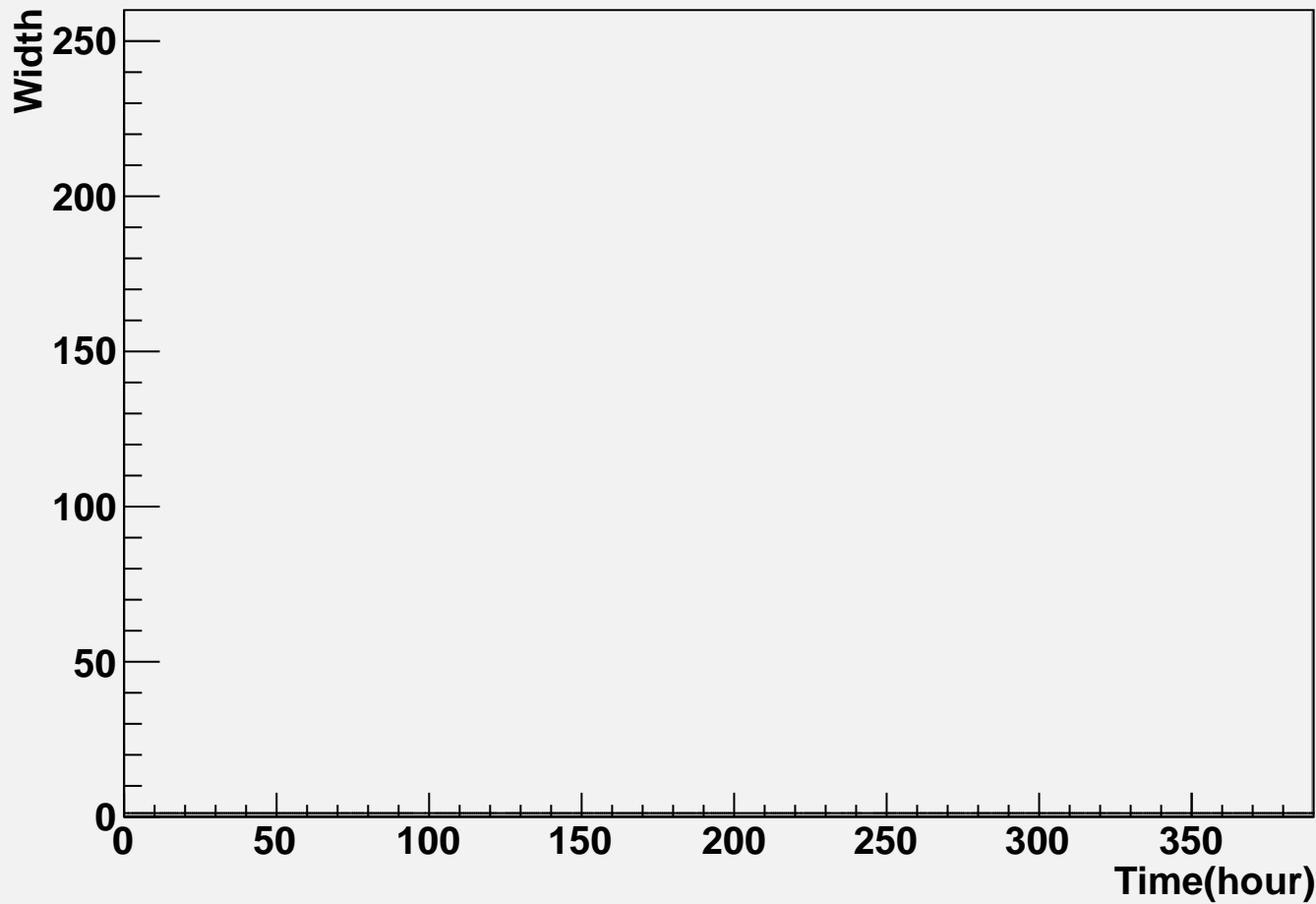
width-S2-SFET2D-Ch5



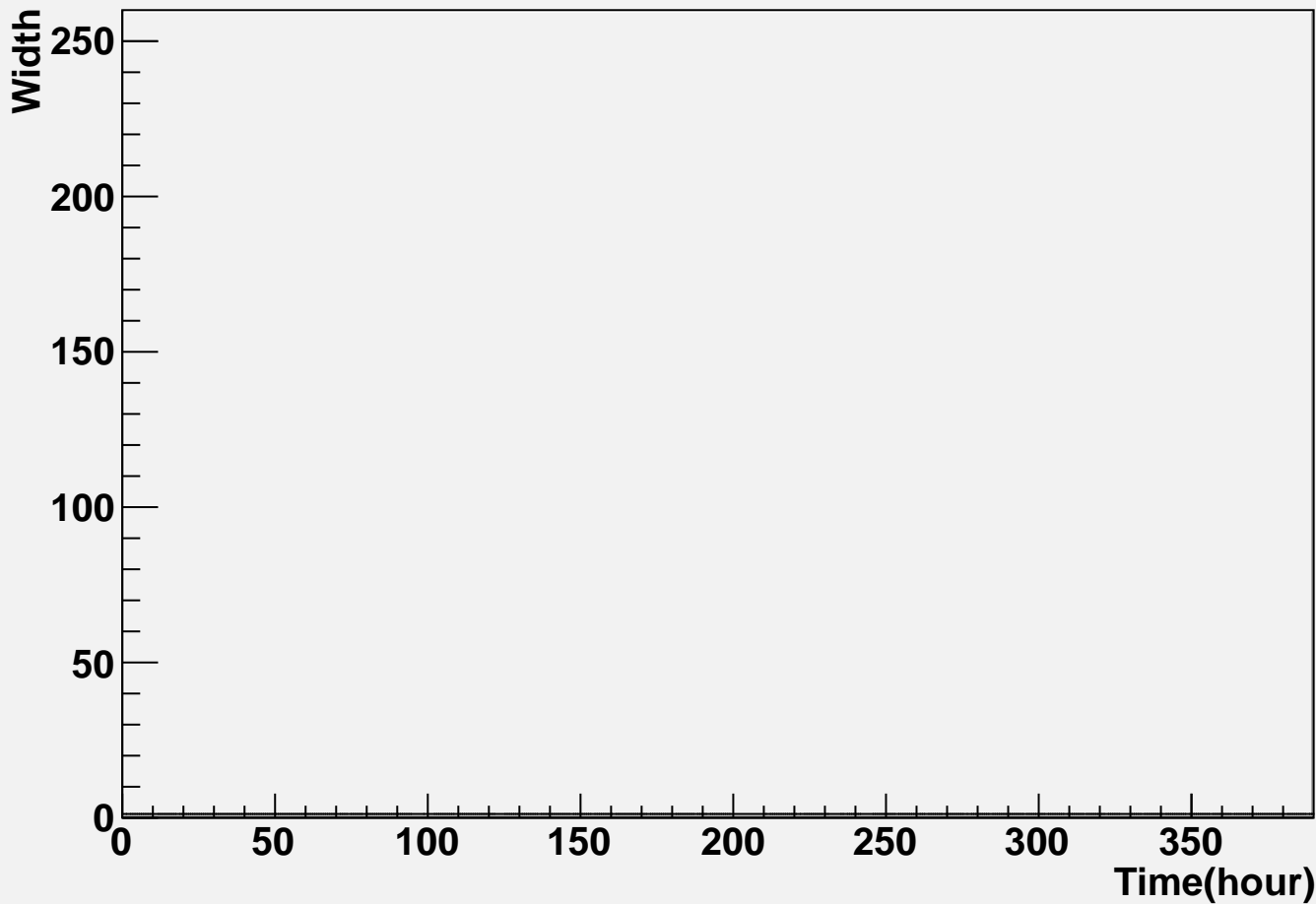
width-S2-SFET2D-Ch6



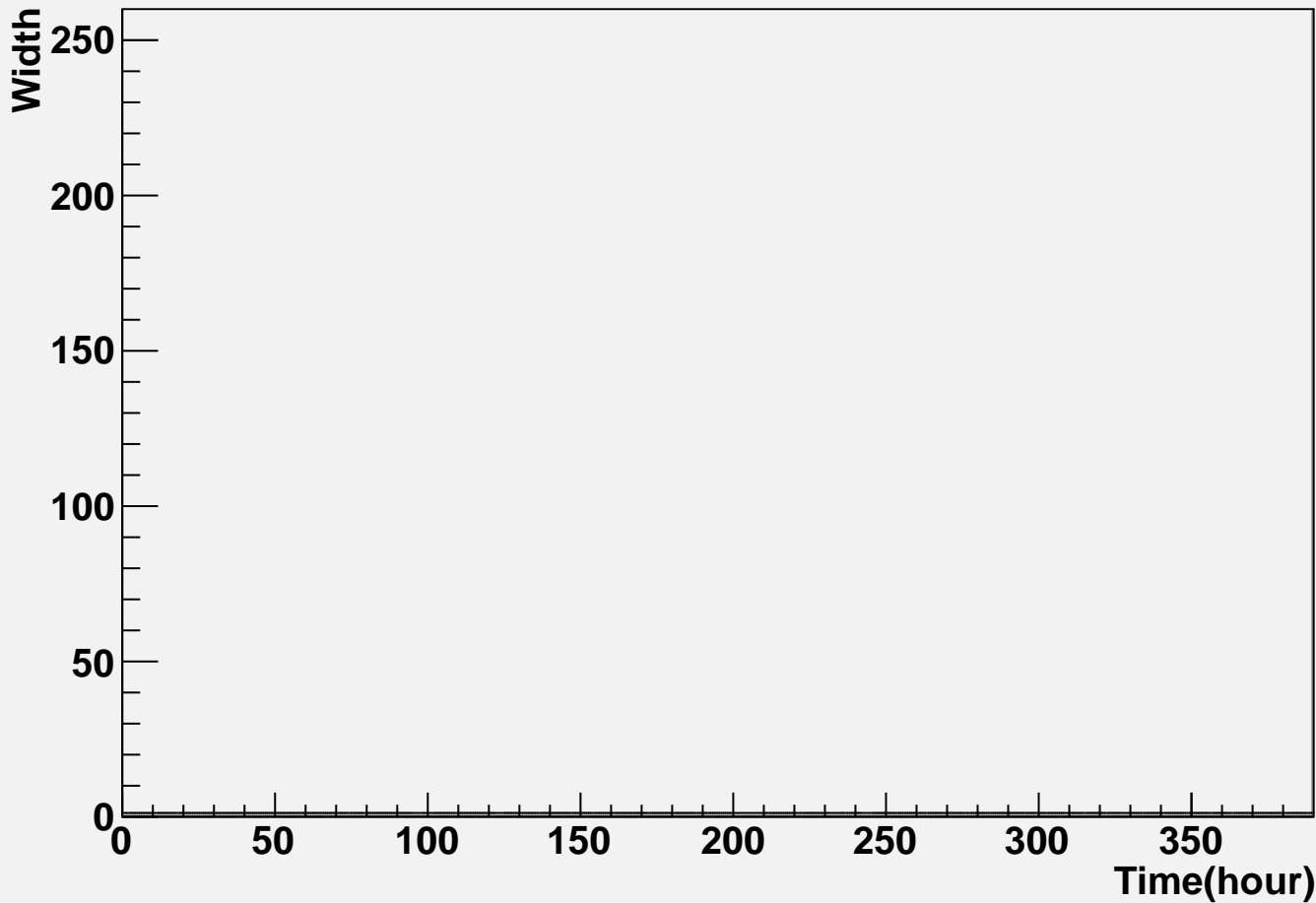
width-S2-SFET2D-Ch7



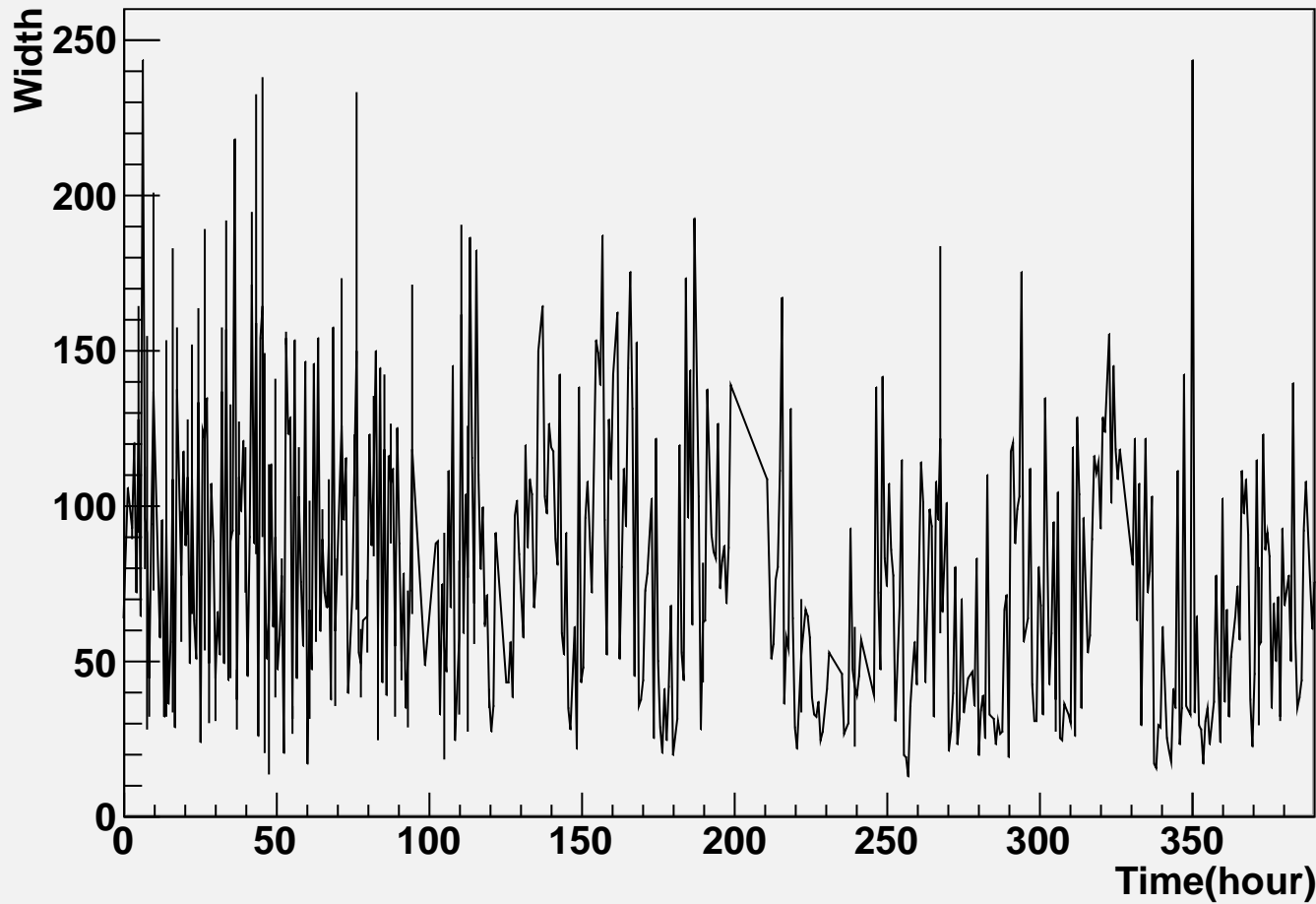
width-S2-SFET2D-Ch8



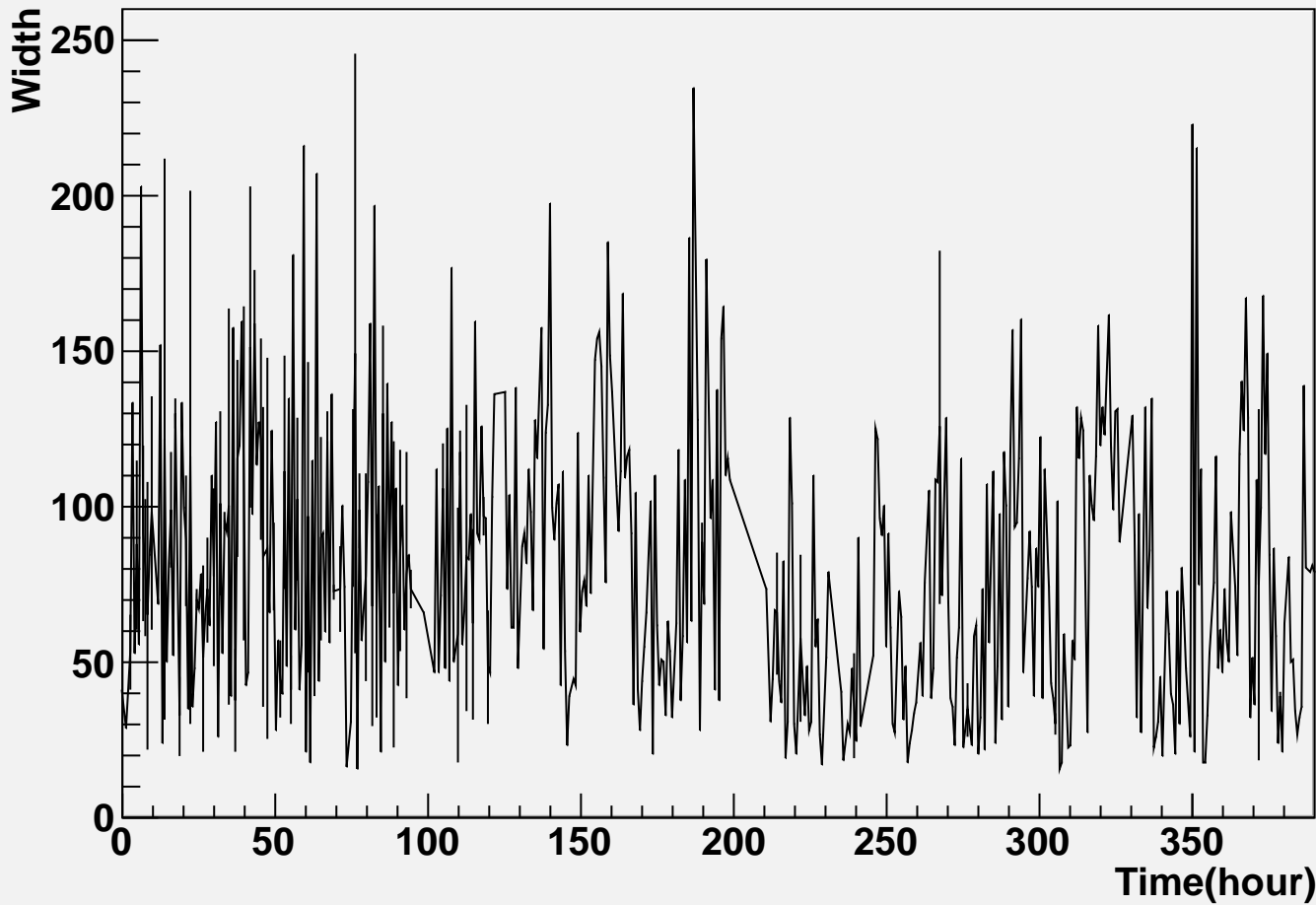
width-S2-SFET2D-Ch9



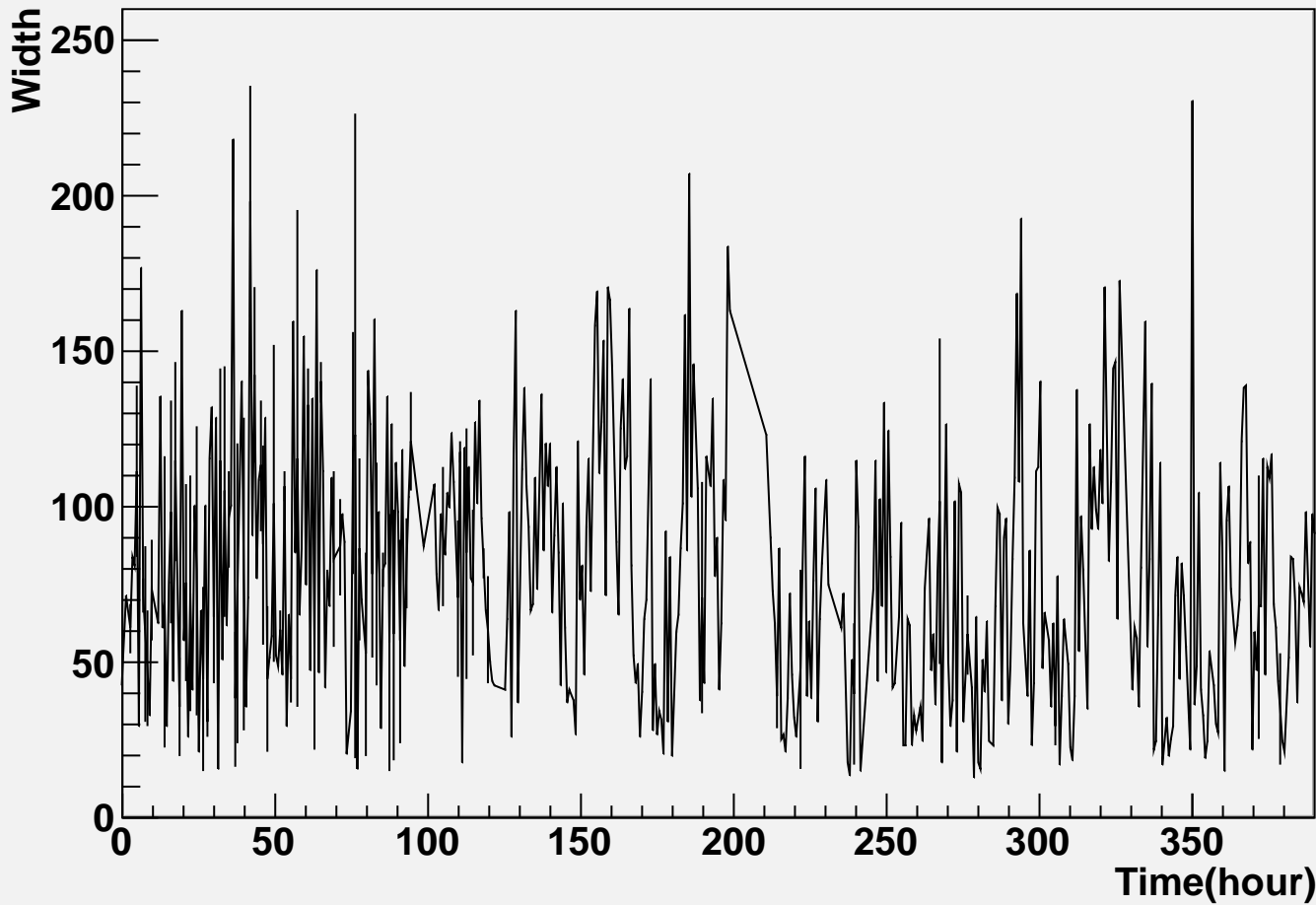
width-S2-SFEA2-Ch0



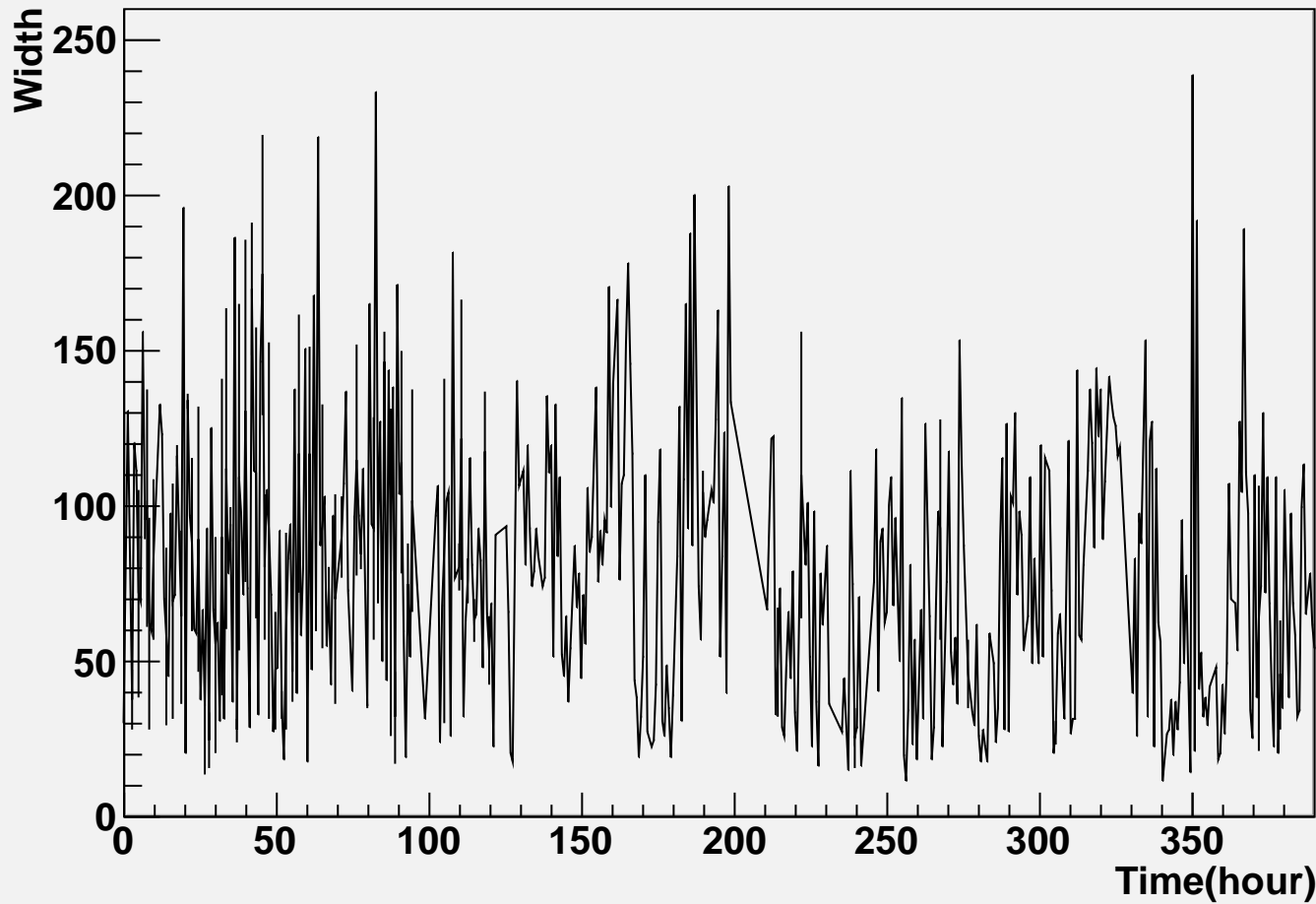
width-S2-SFEA2-Ch1



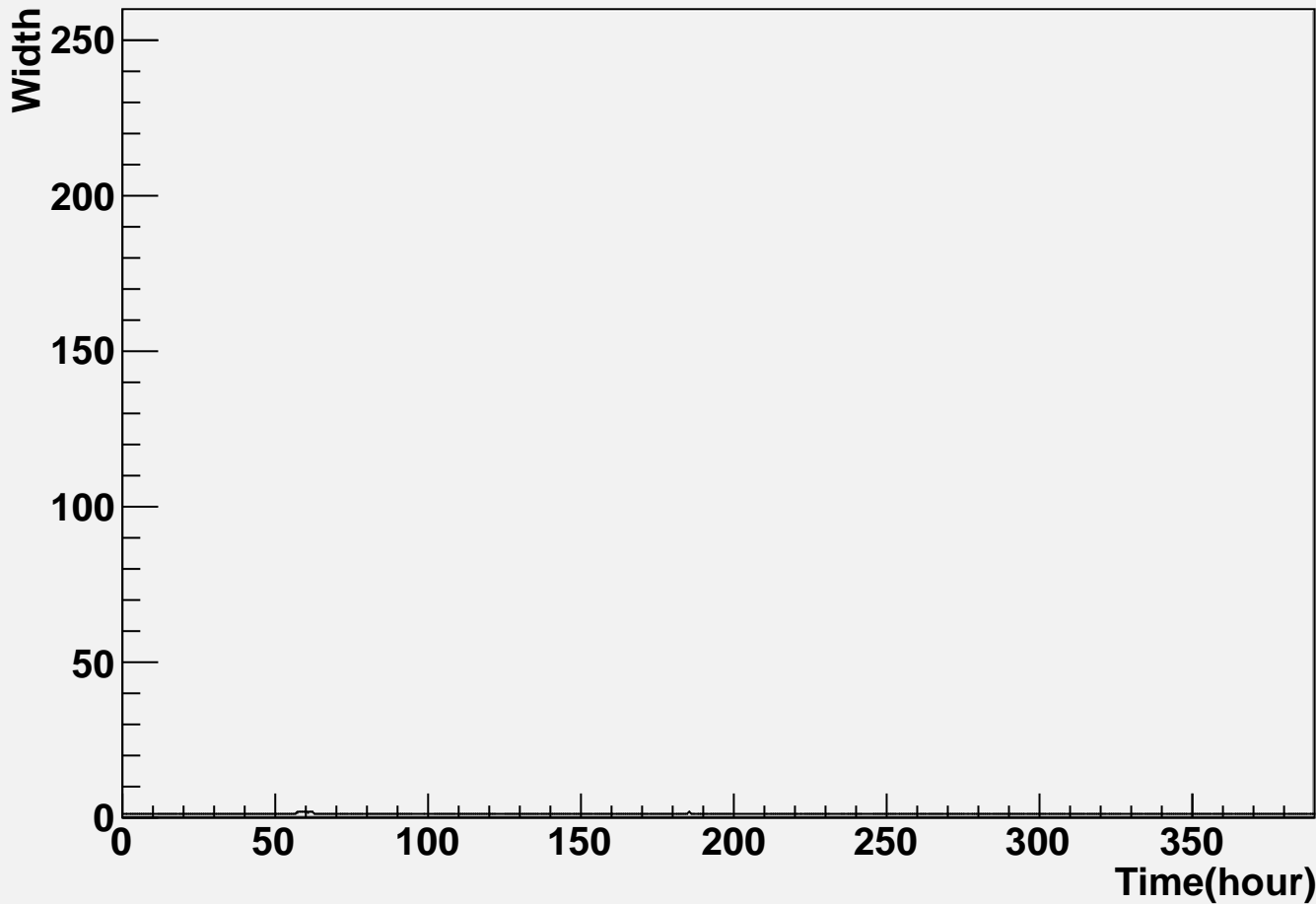
width-S2-SFEA2-Ch2



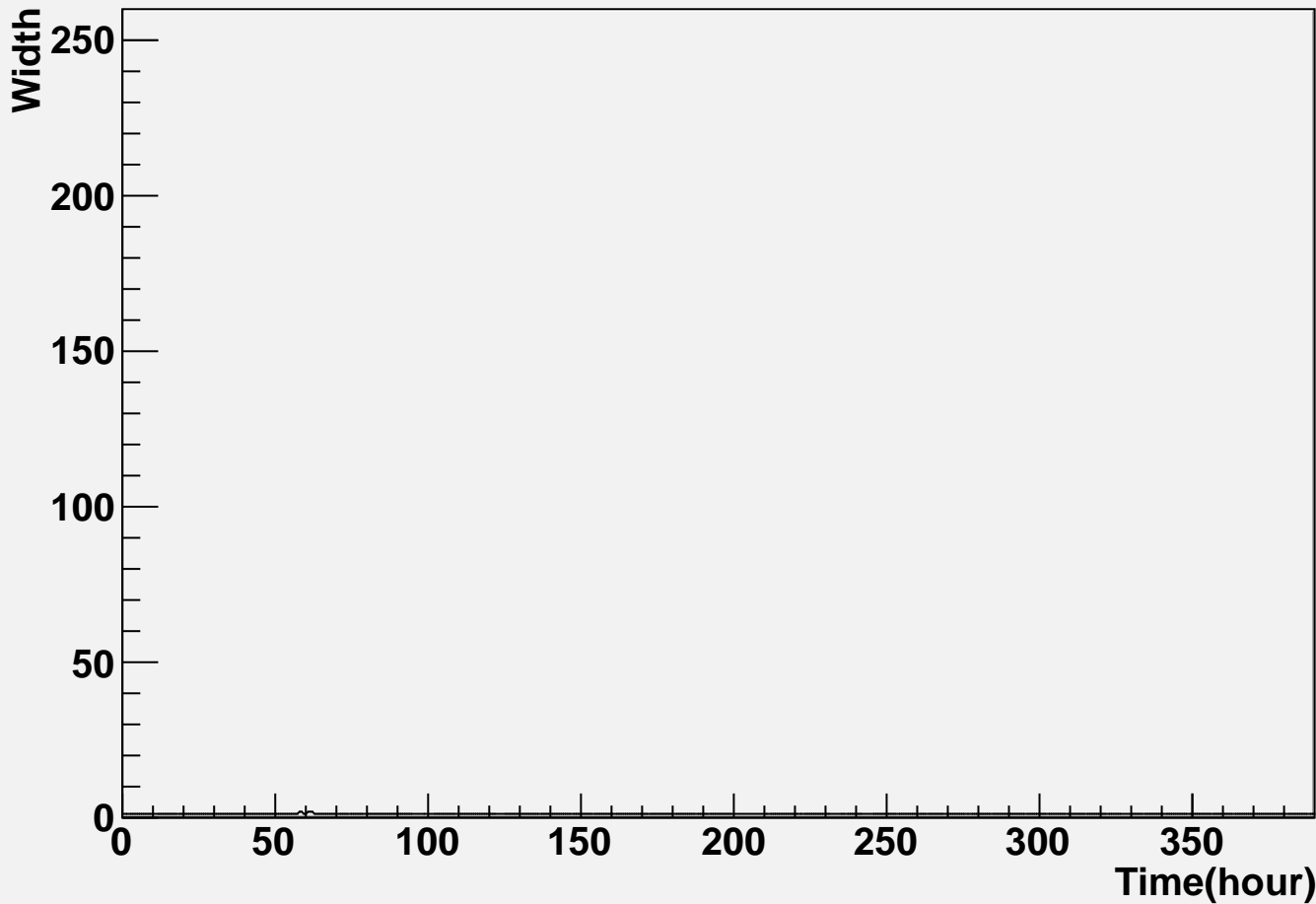
width-S2-SFEA2-Ch3



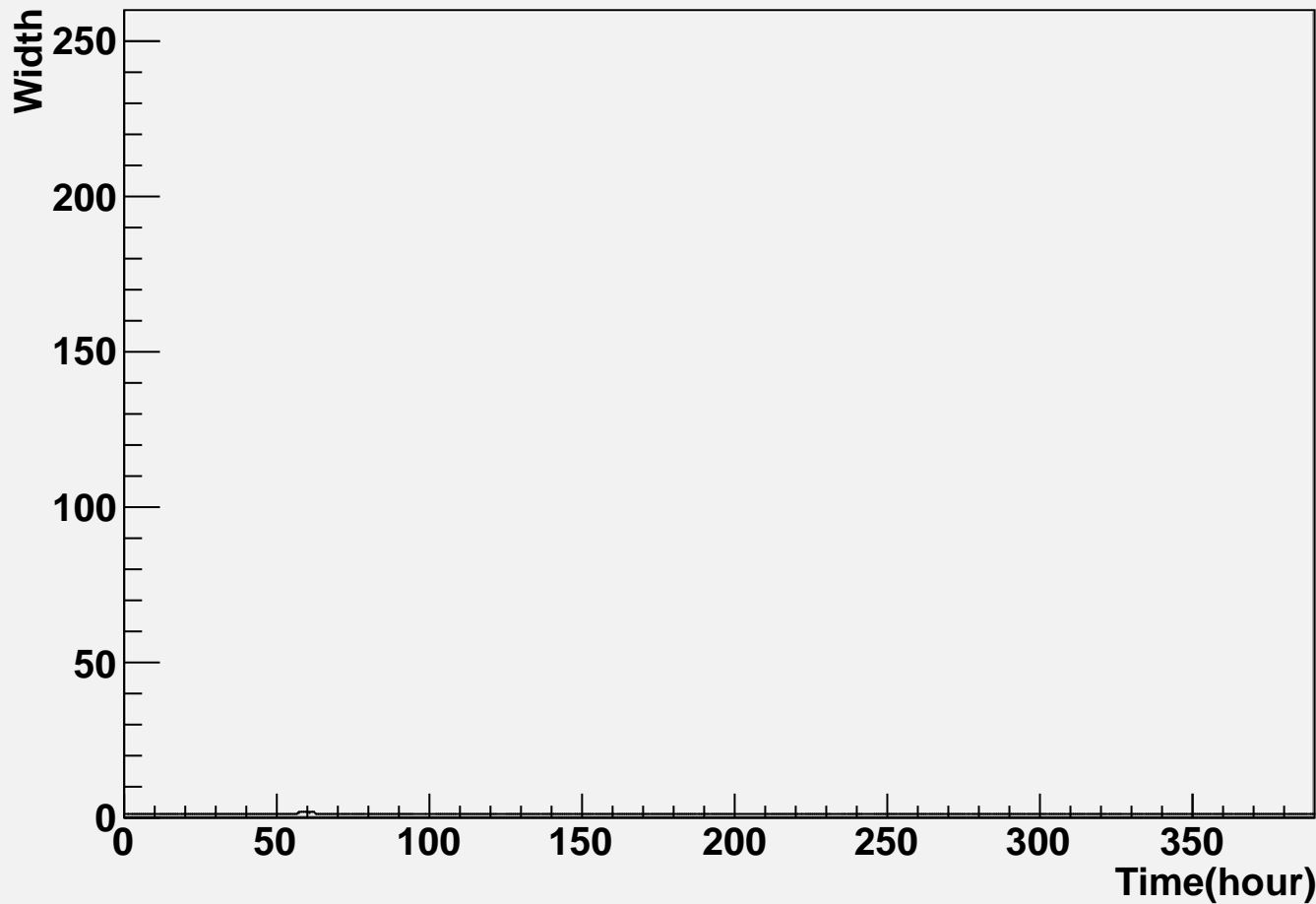
width-S2-SFEA2-Ch4



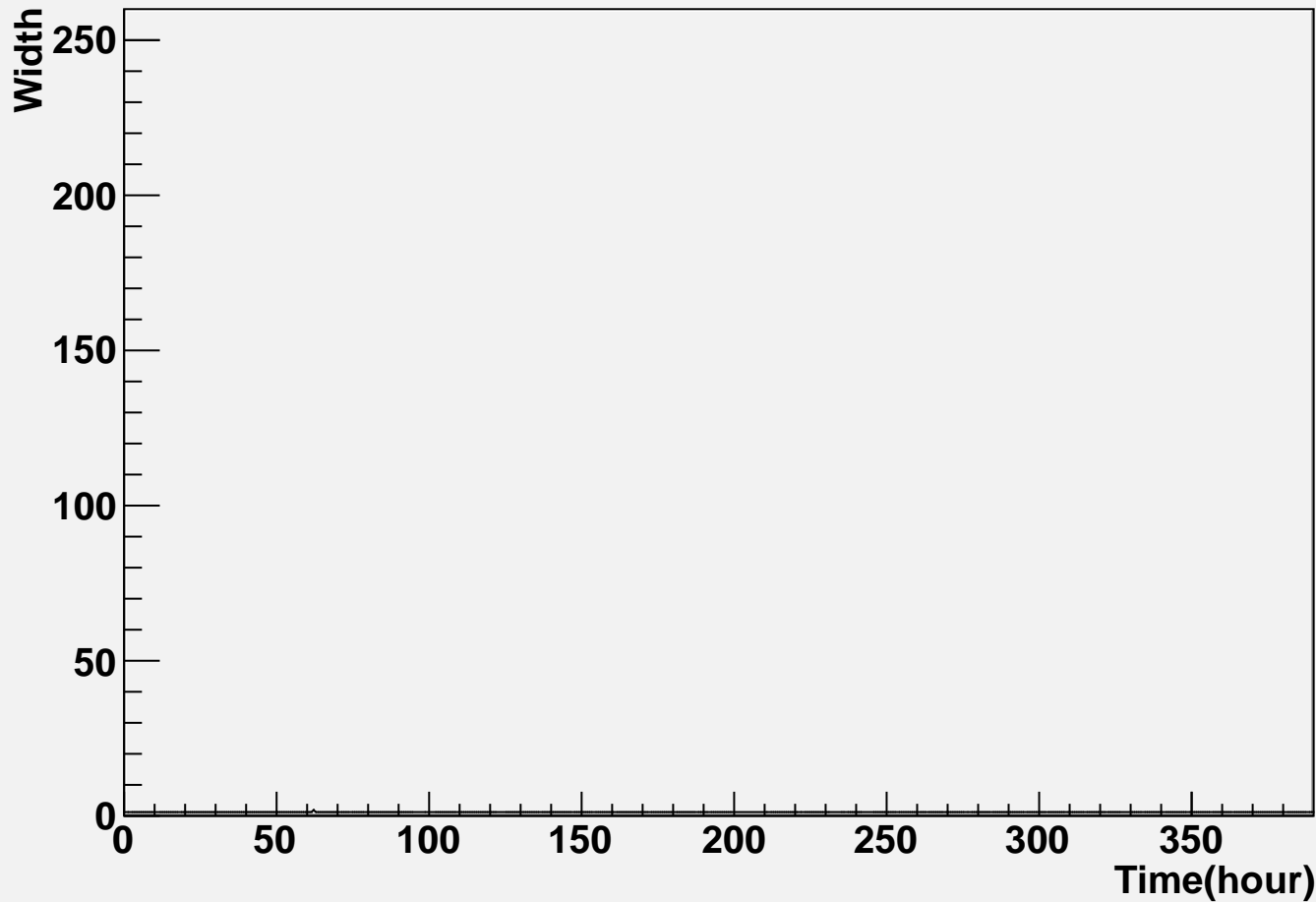
width-S2-SFEA2-Ch5



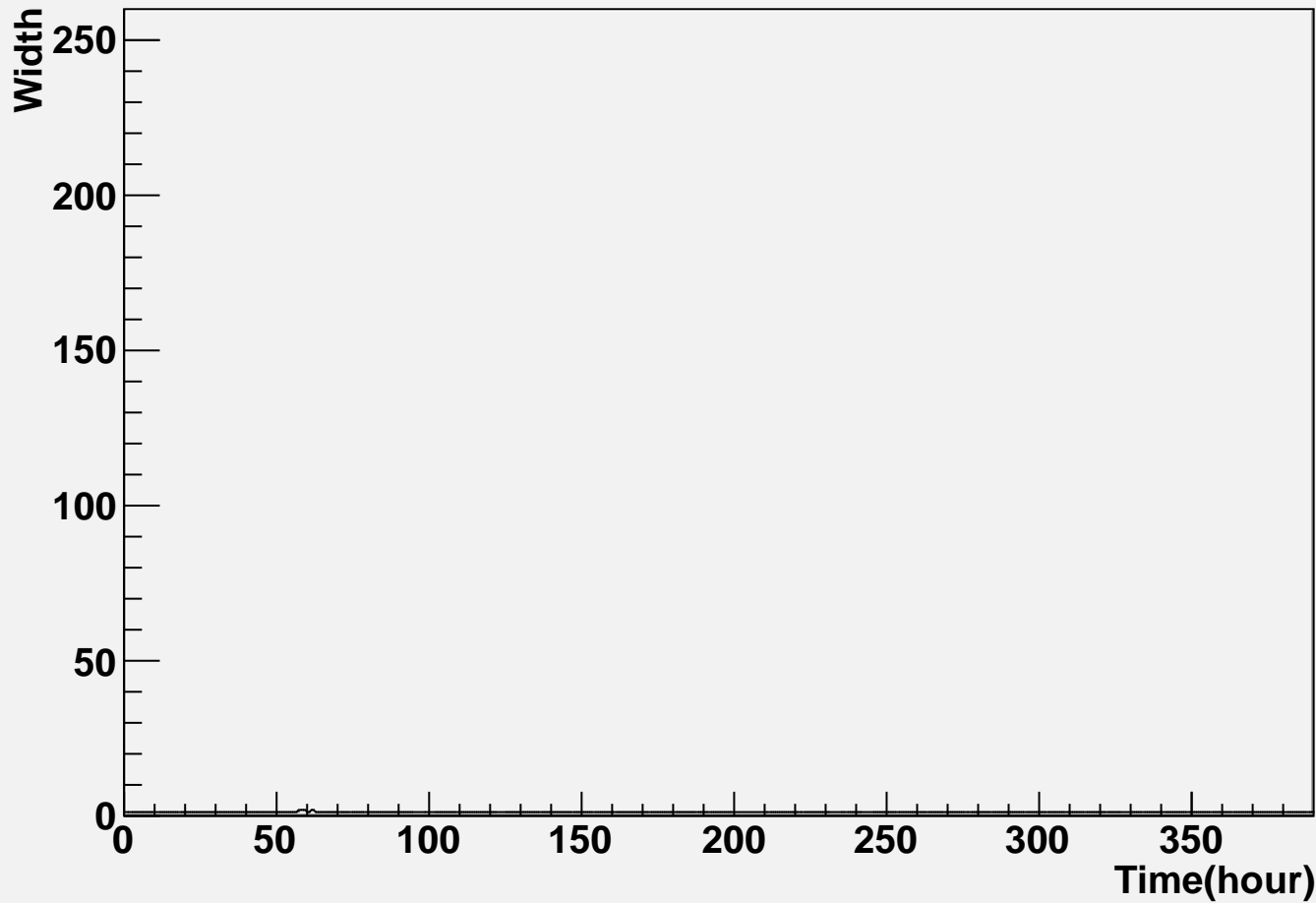
width-S2-SFEA2-Ch6



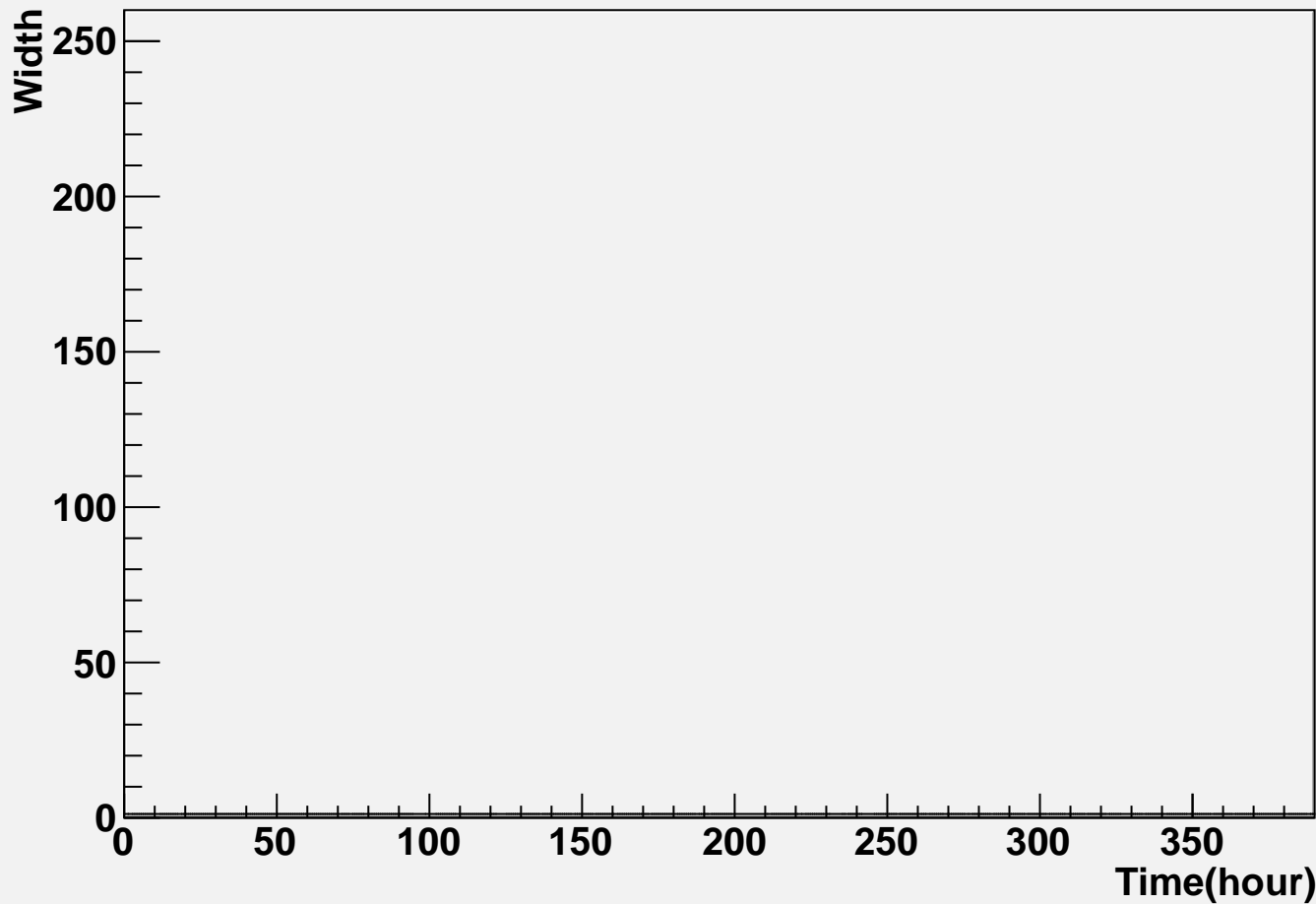
width-S2-SFEA2-Ch7



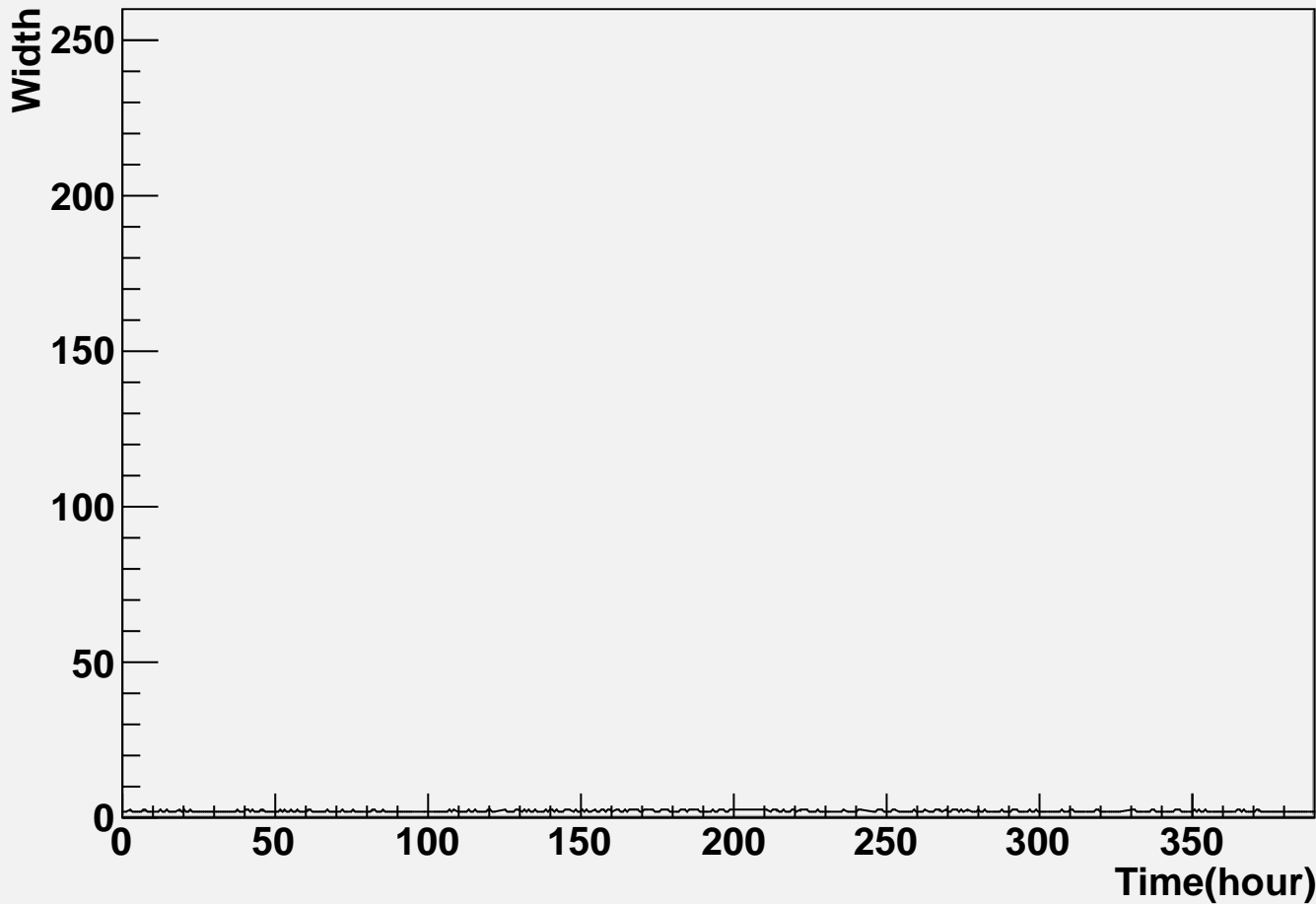
width-S2-SFEA2-Ch8



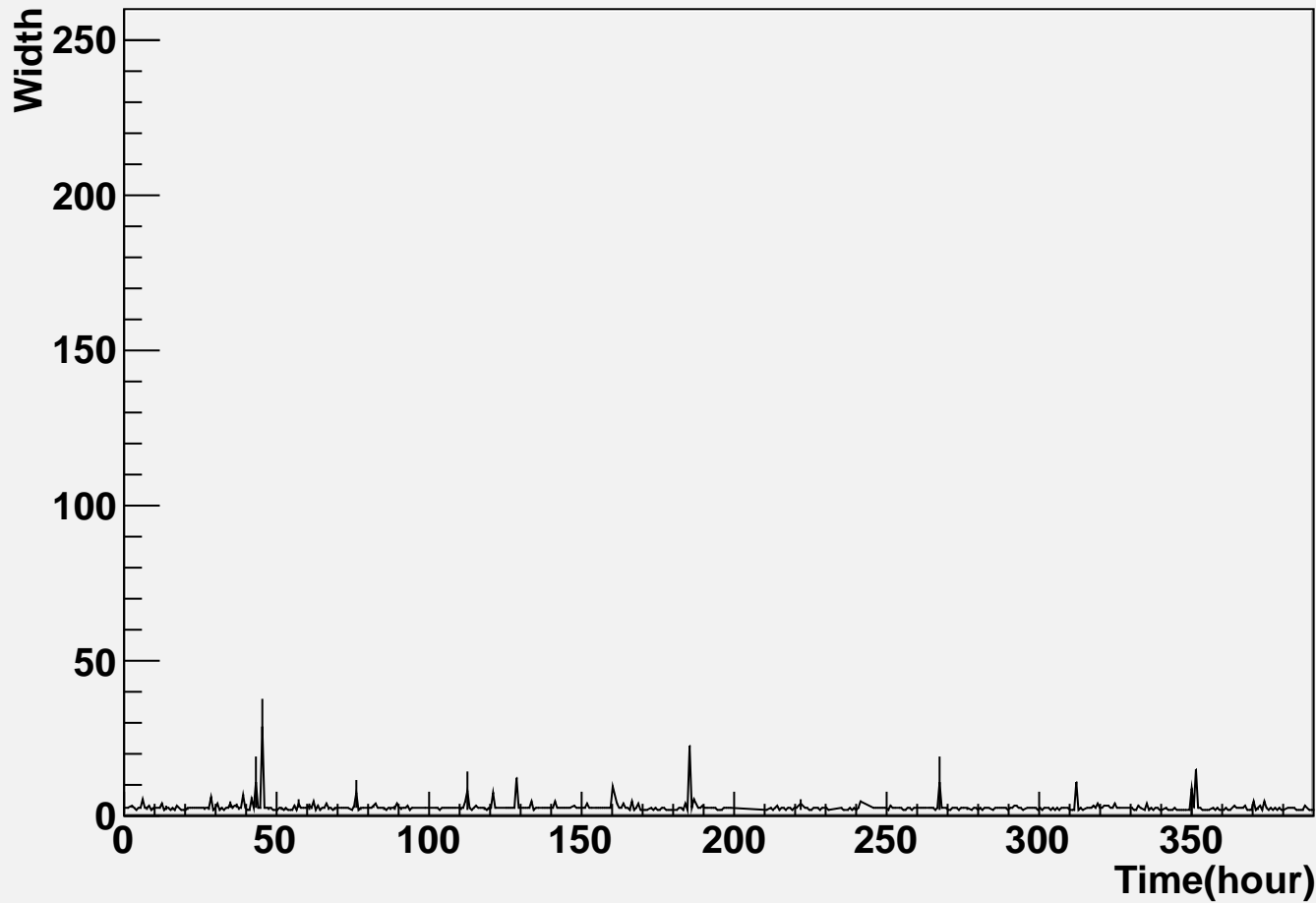
width-S2-SFEA2-Ch9



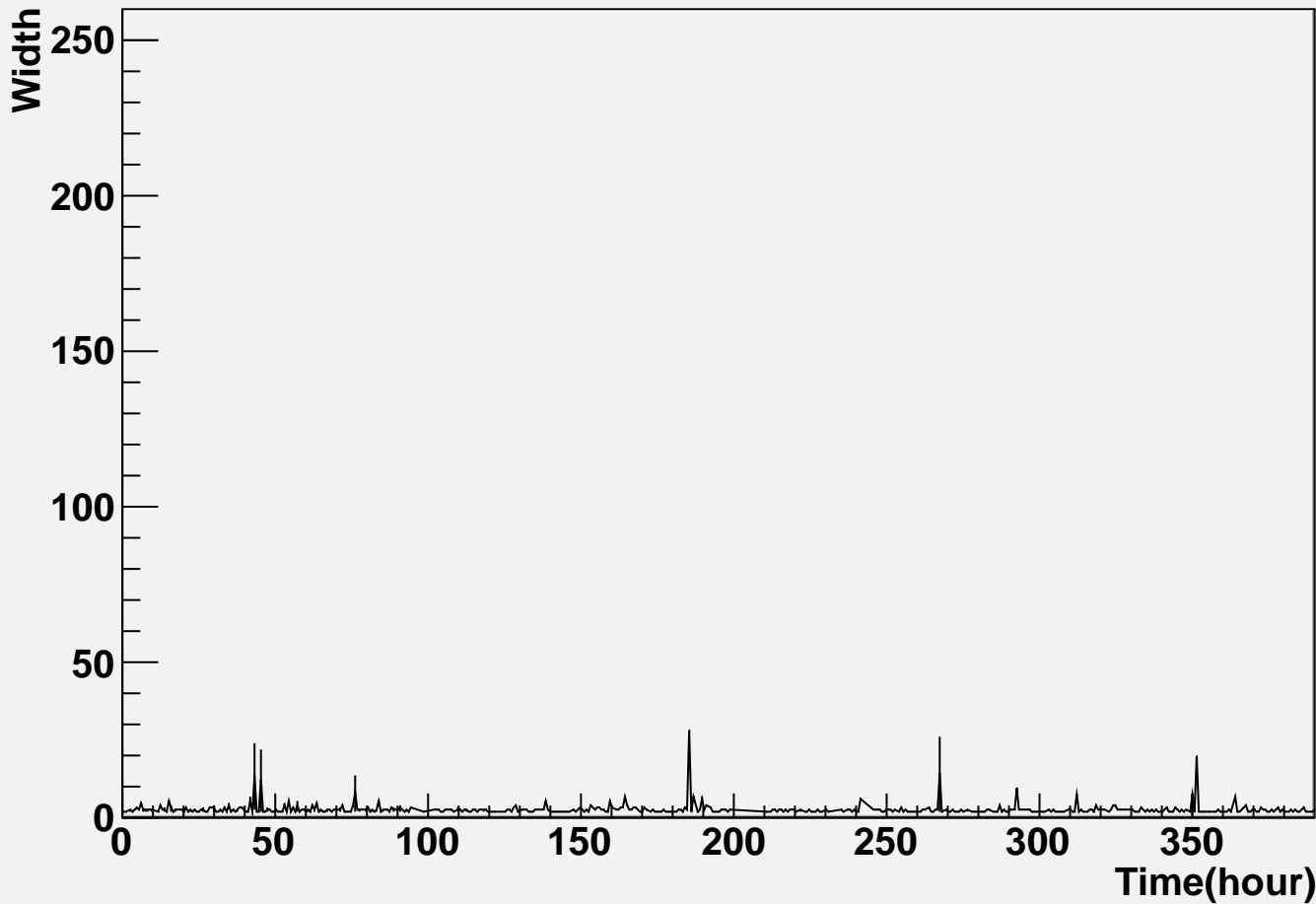
width-S2-SFEC00-Ch0



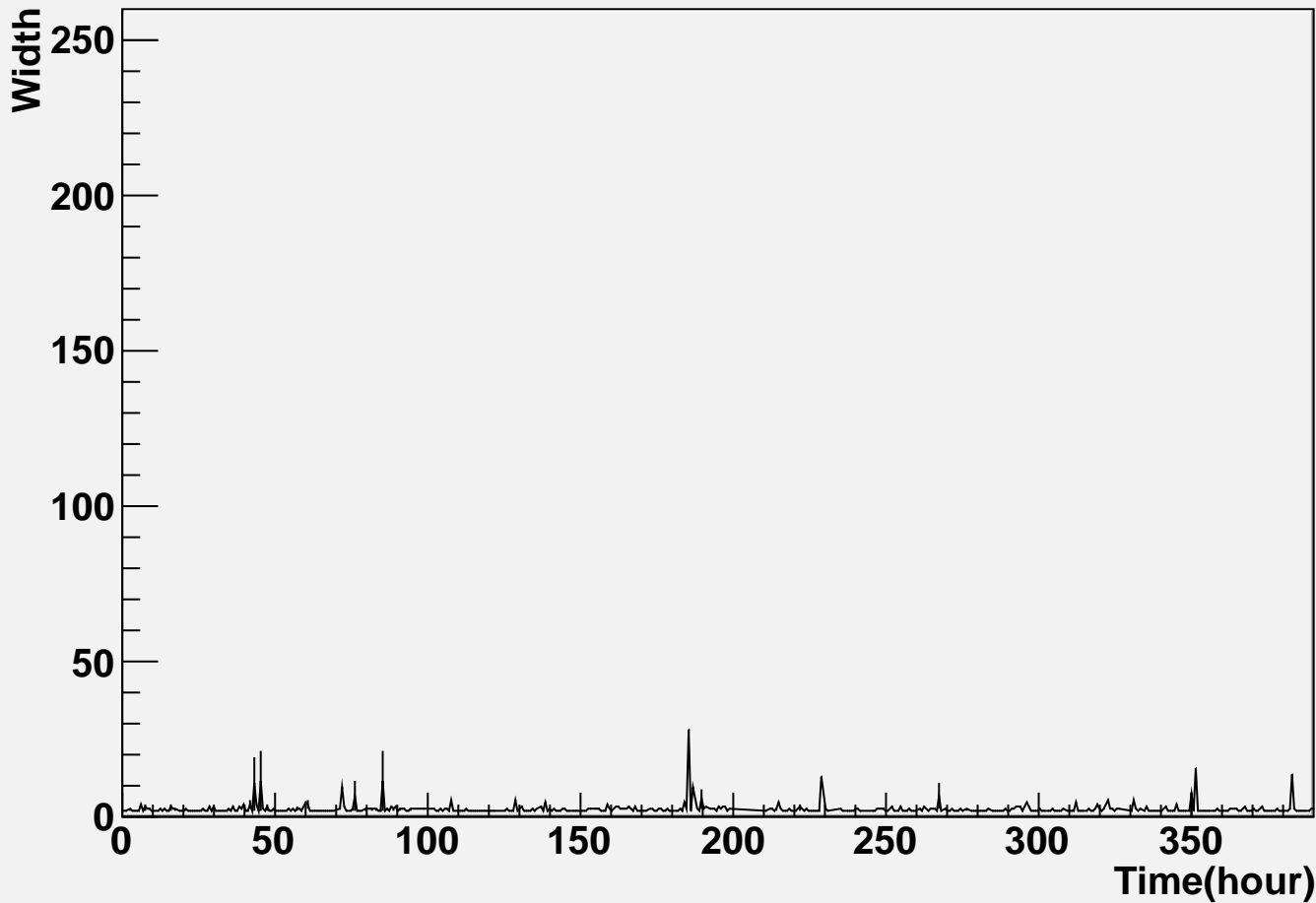
width-S2-SFEC00-Ch1



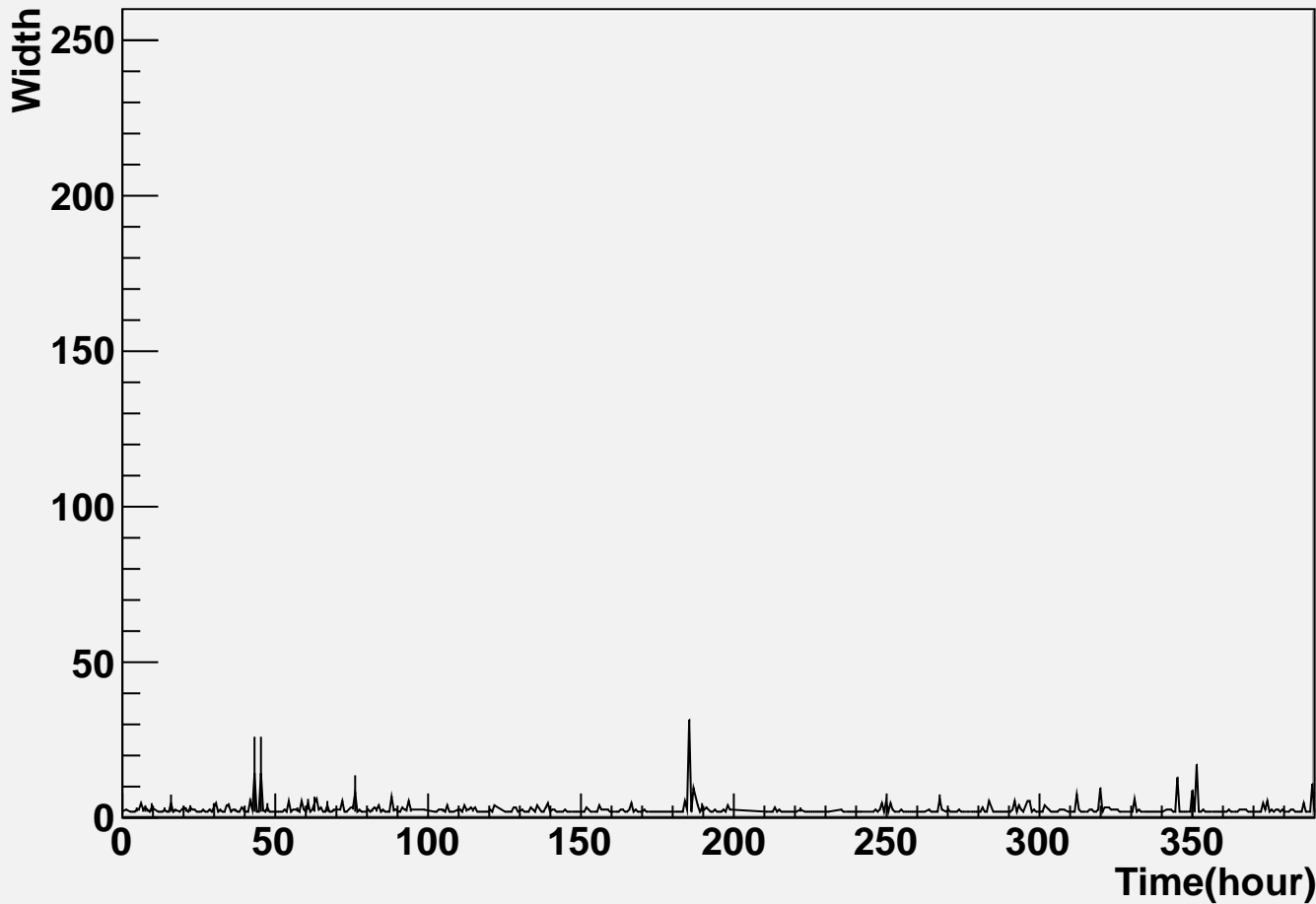
width-S2-SFEC00-Ch2



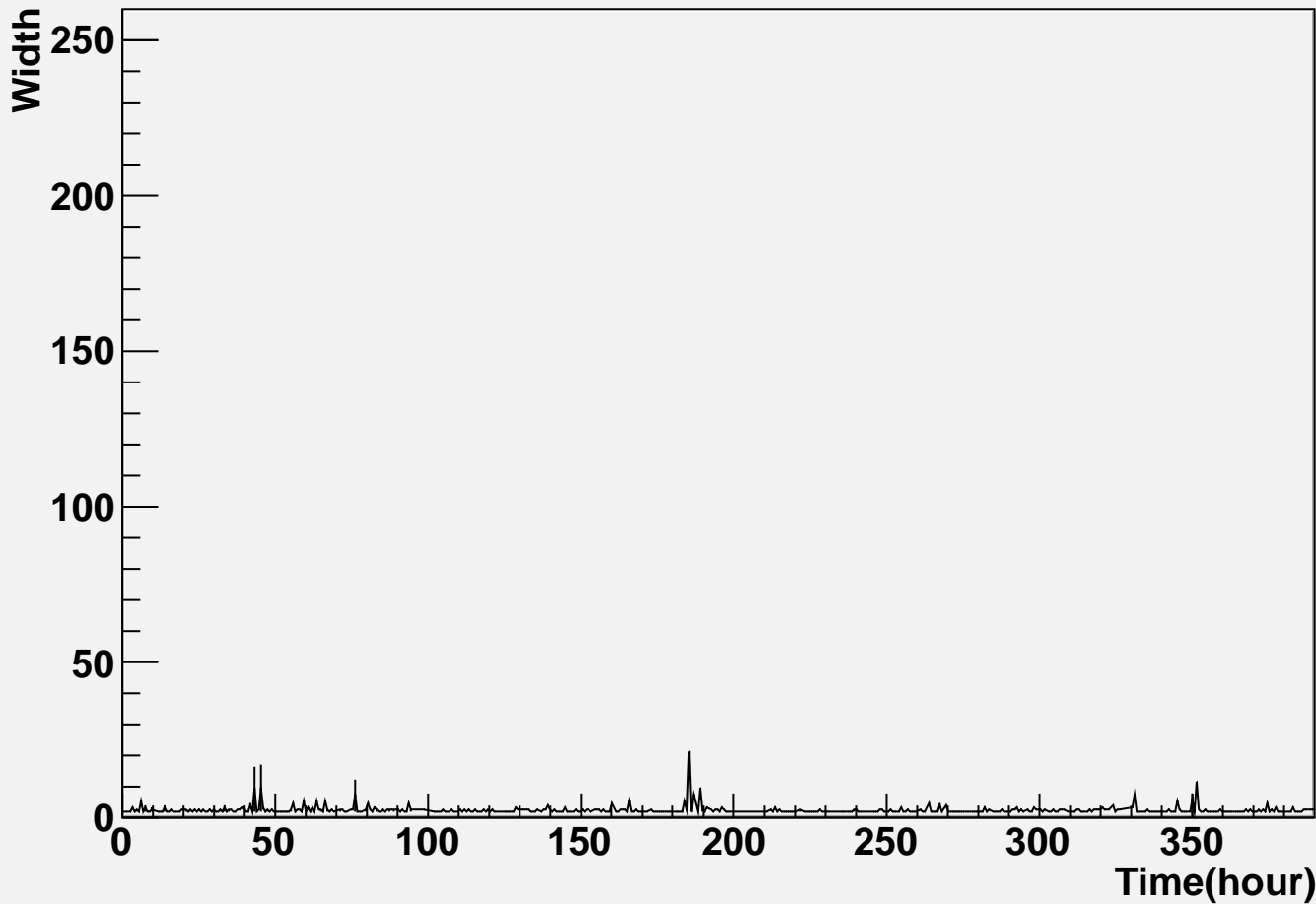
width-S2-SFEC00-Ch3



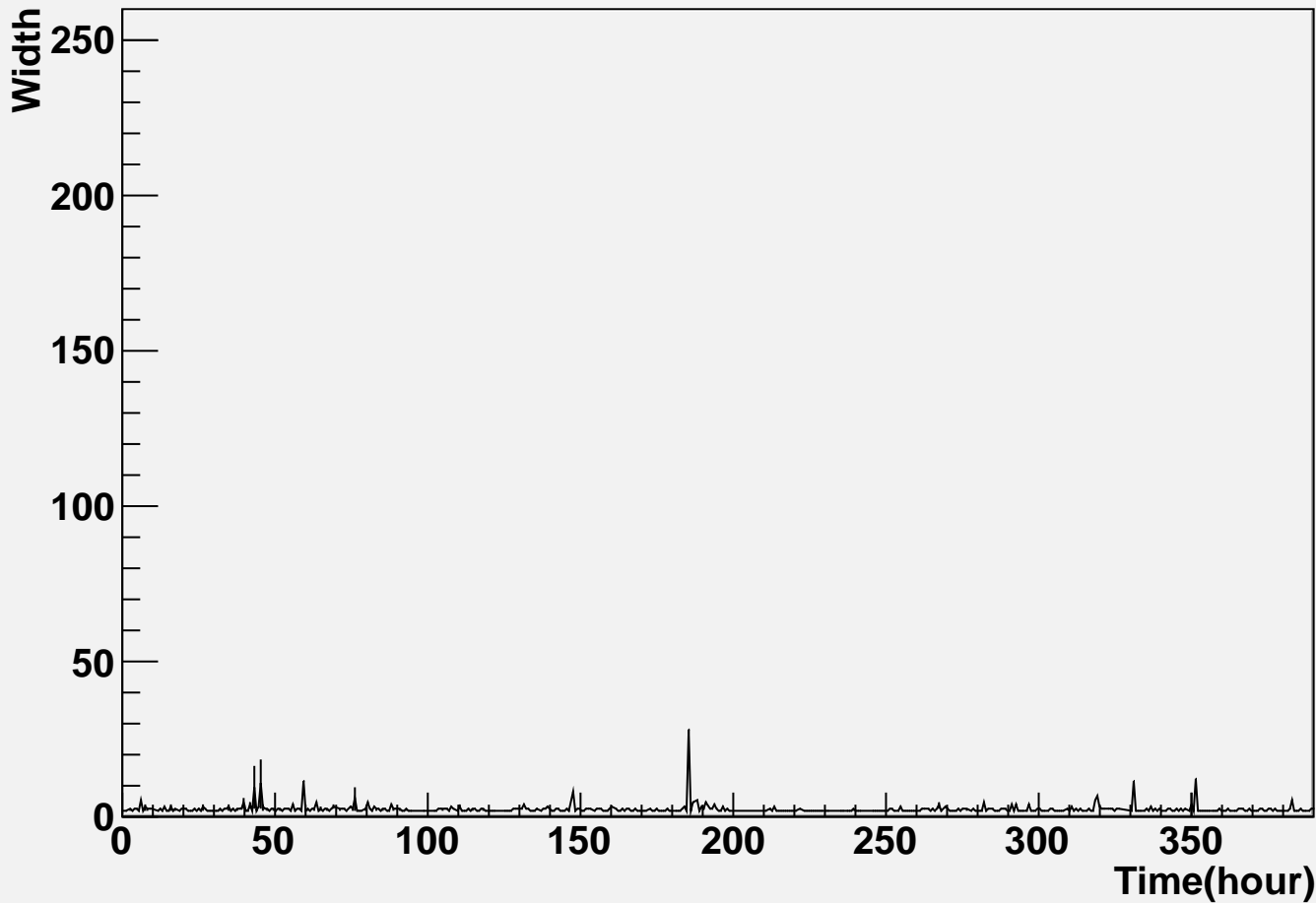
width-S2-SFEC00-Ch4



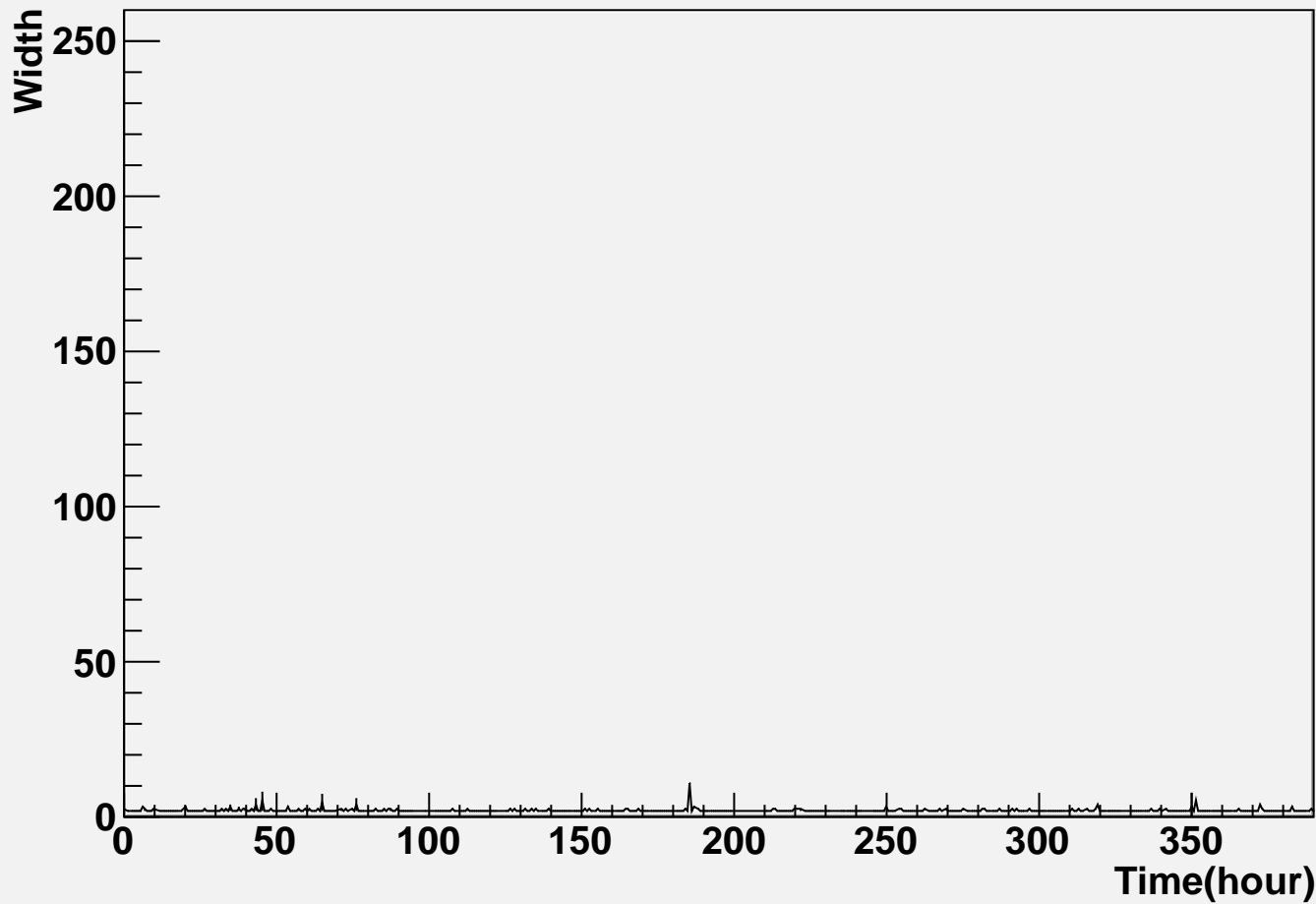
width-S2-SFEC00-Ch5



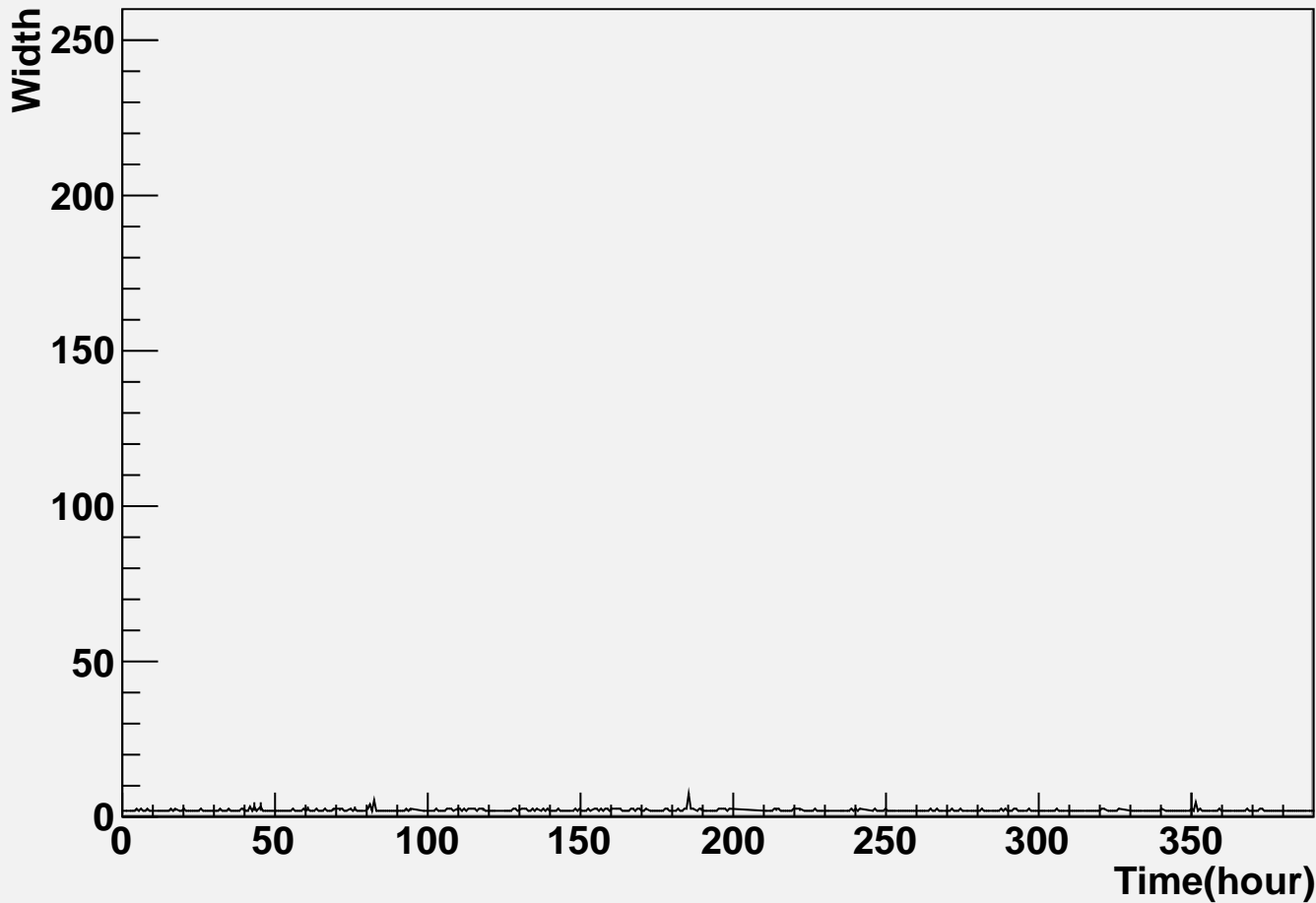
width-S2-SFEC00-Ch6



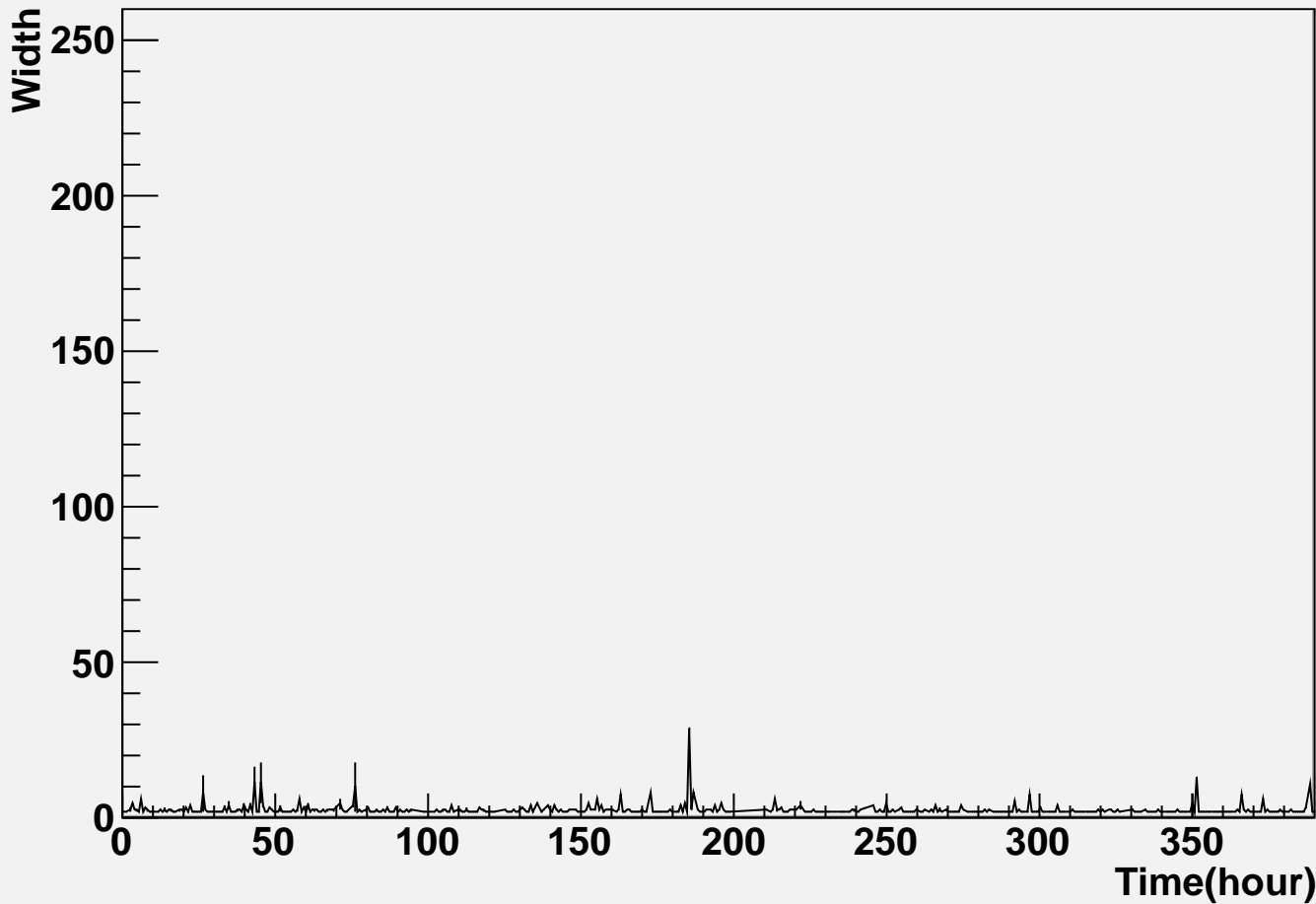
width-S2-SFEC00-Ch7



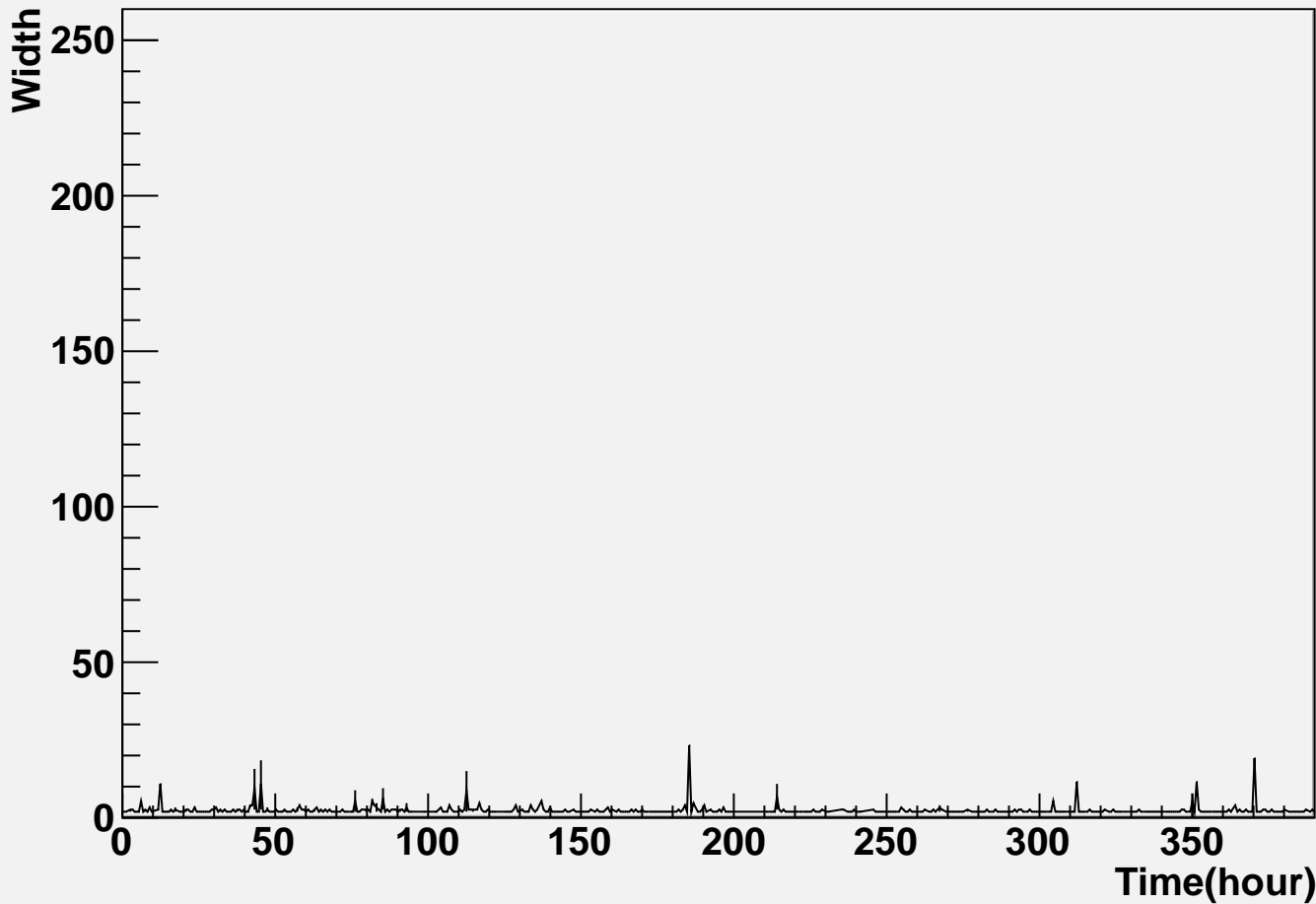
width-S2-SFEC00-Ch8



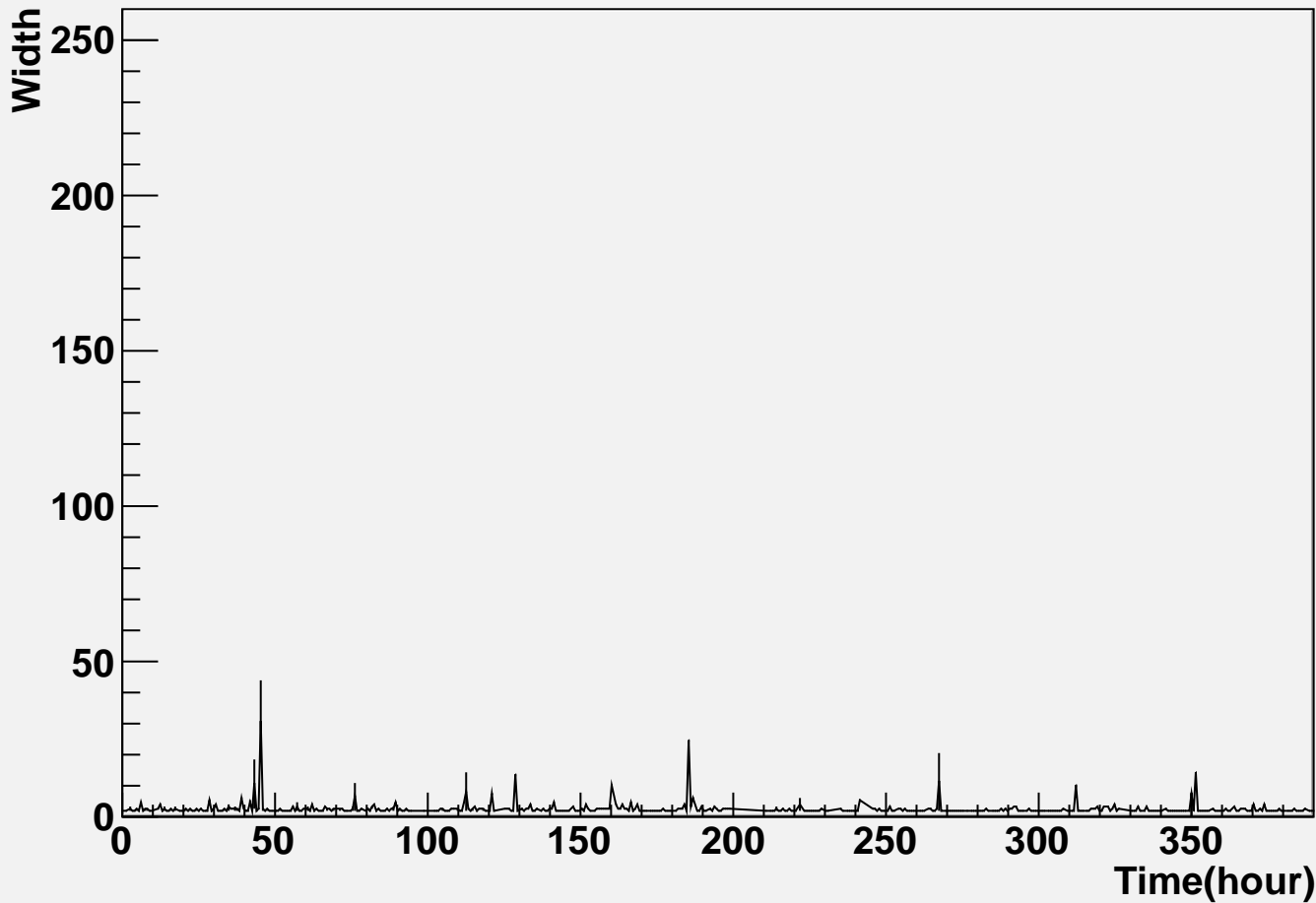
width-S2-SFEC00-Ch9



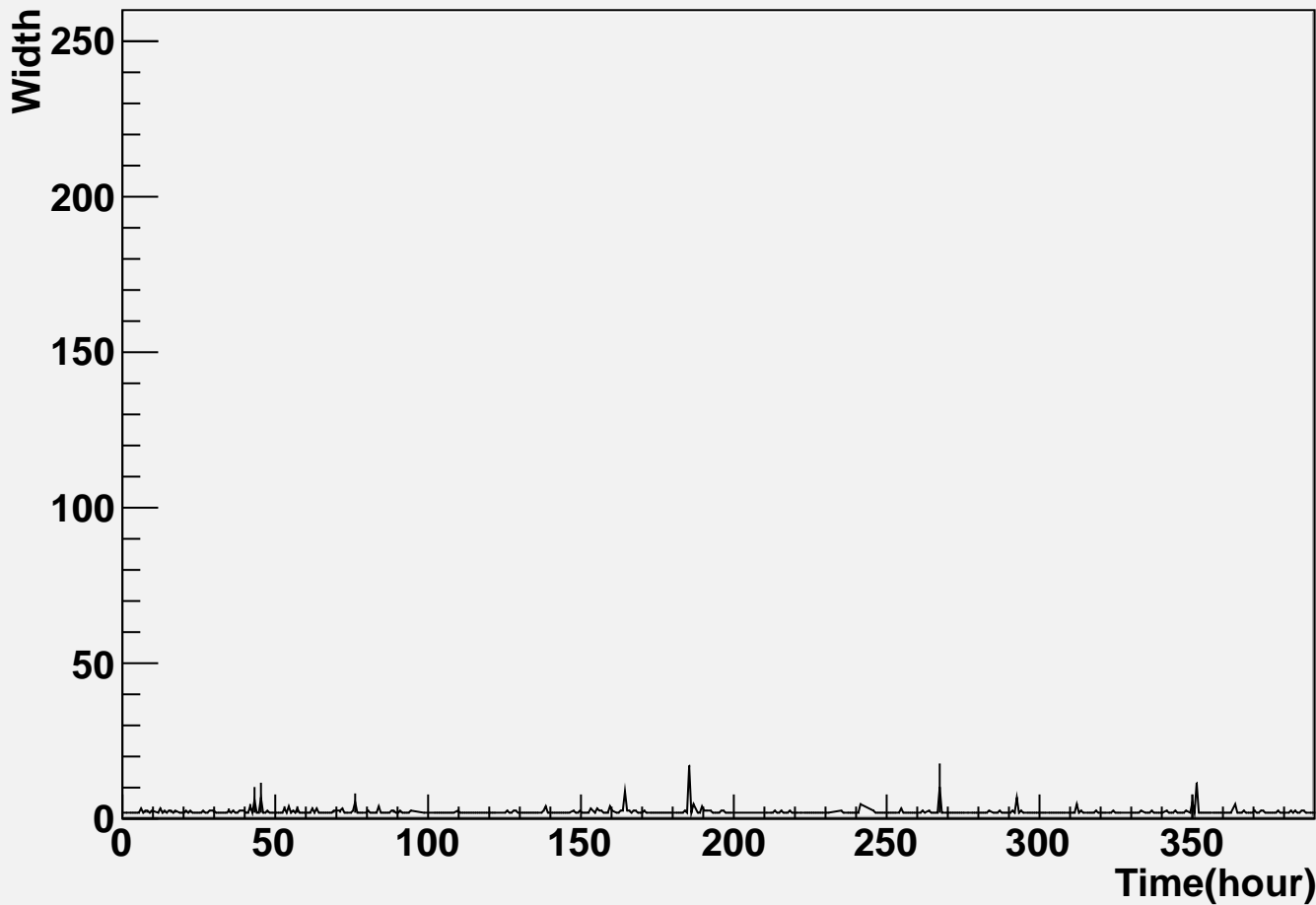
width-S2-SFEC01-Ch0



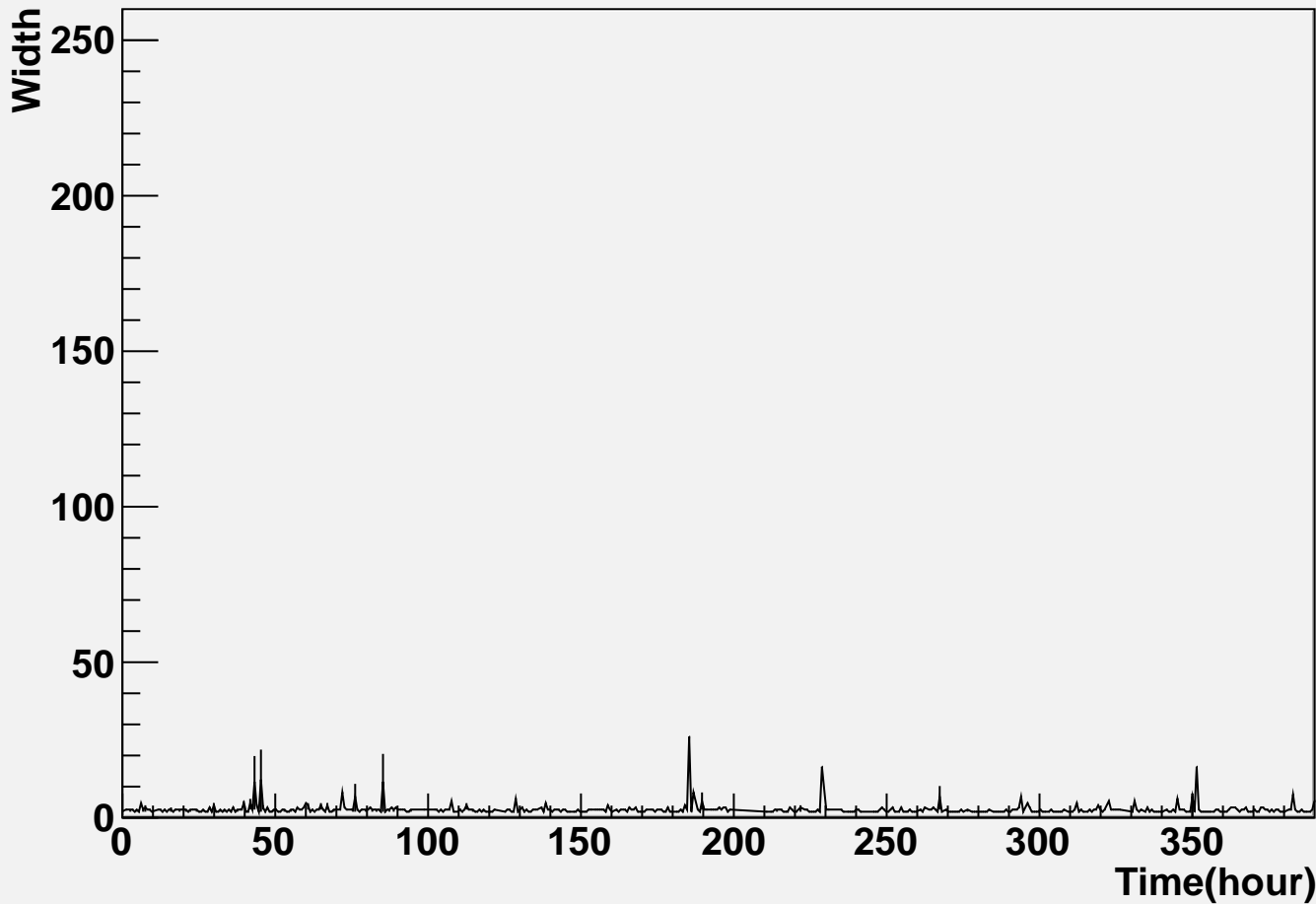
width-S2-SFEC01-Ch1



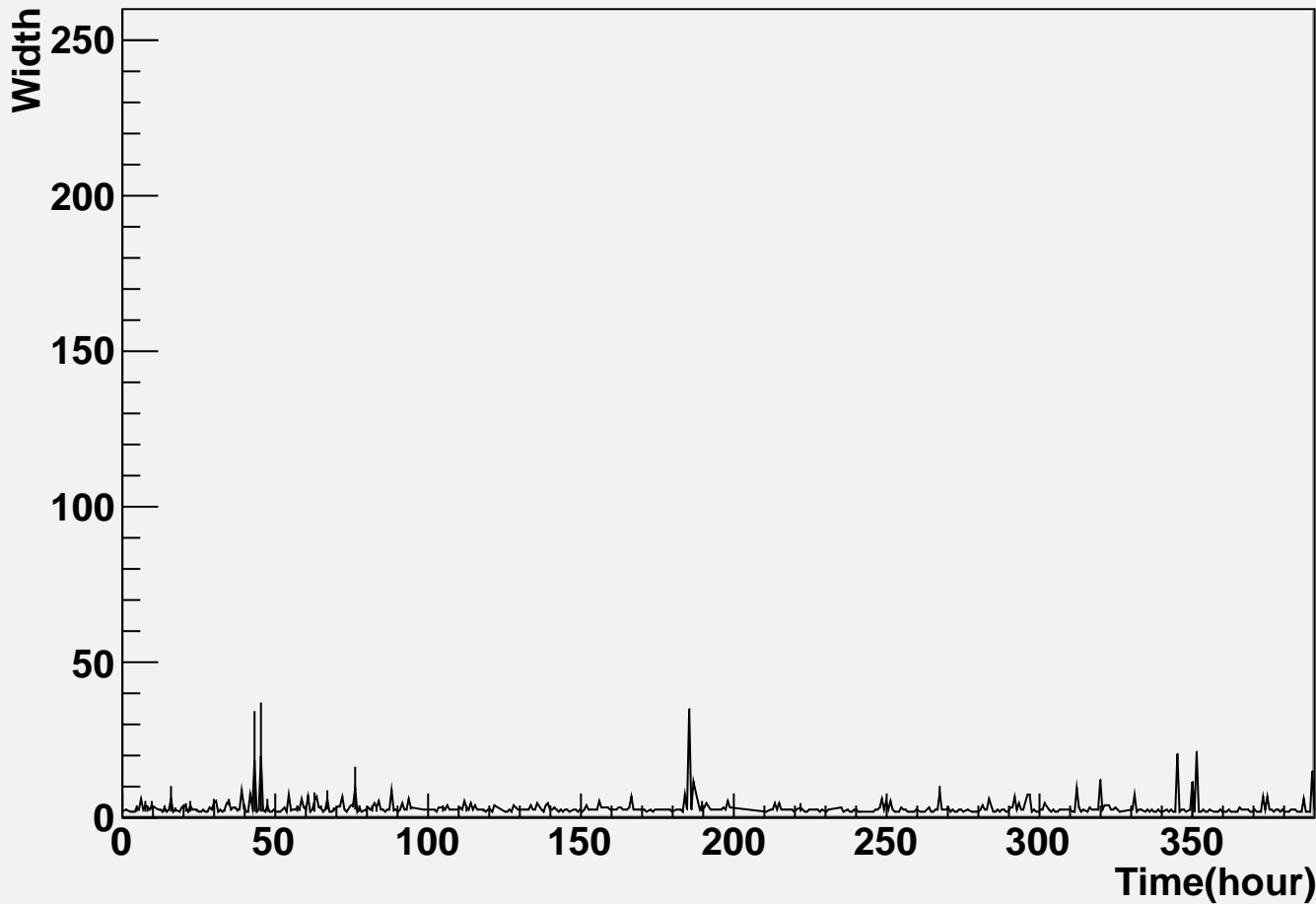
width-S2-SFEC01-Ch2



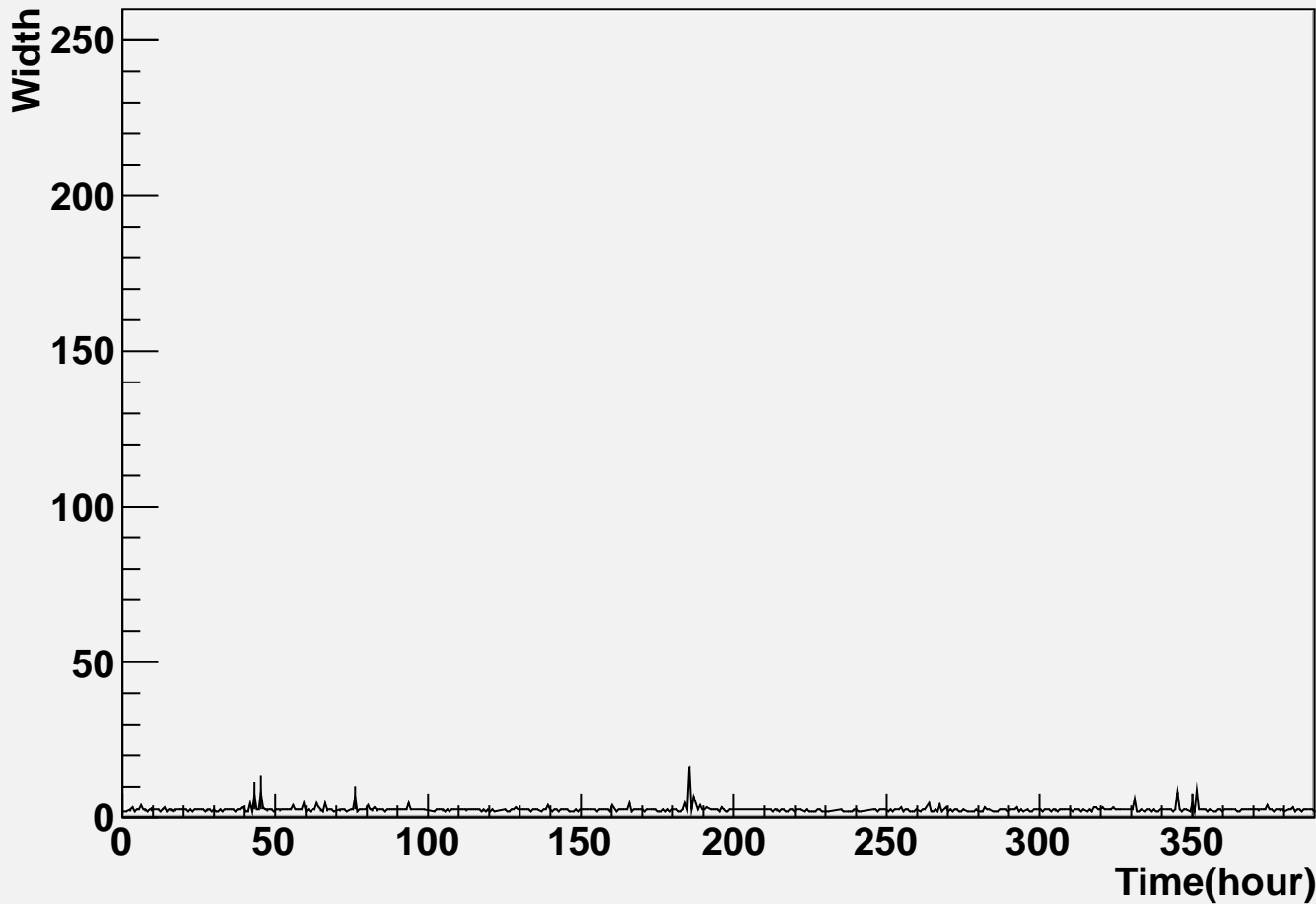
width-S2-SFEC01-Ch3



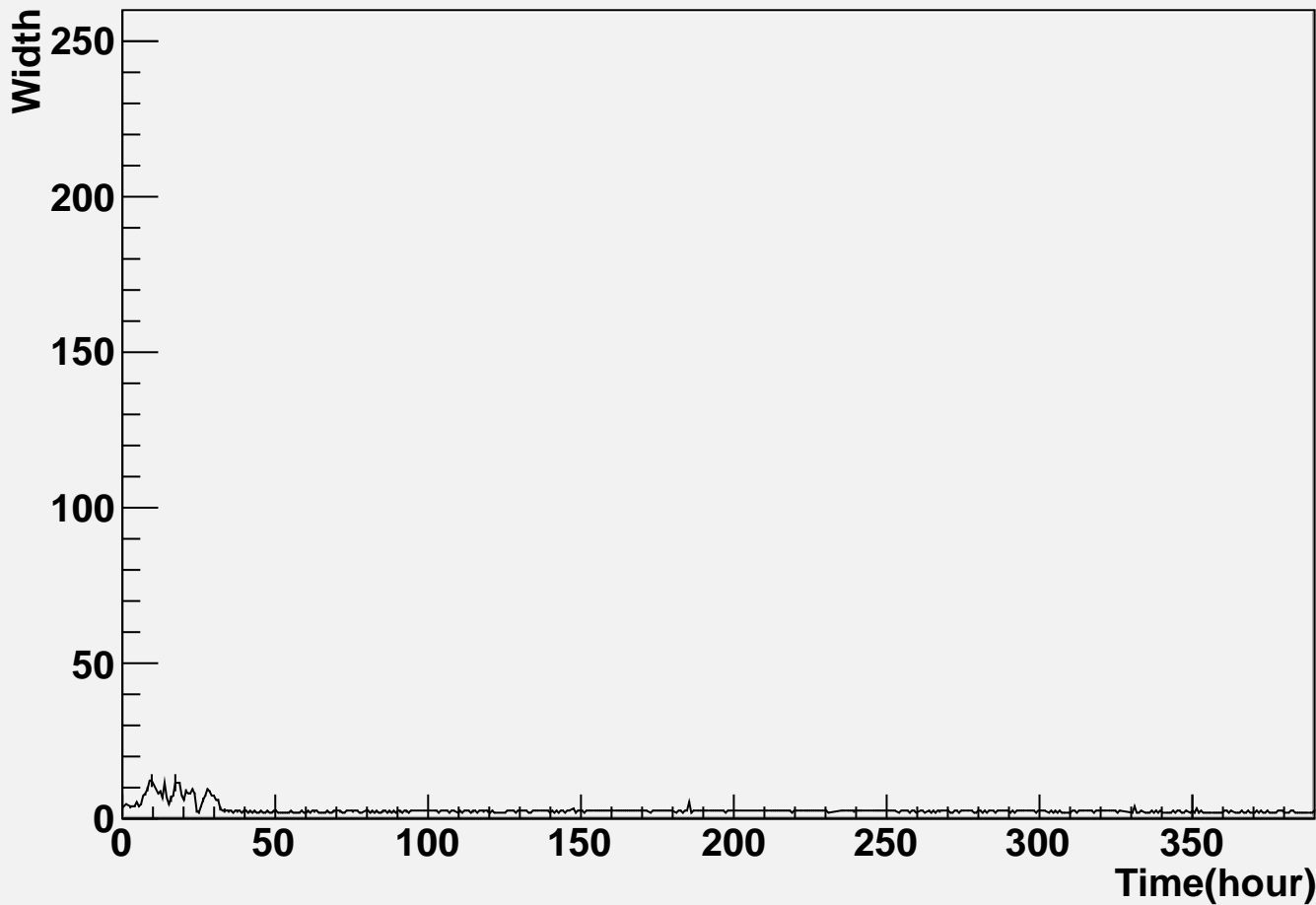
width-S2-SFEC01-Ch4



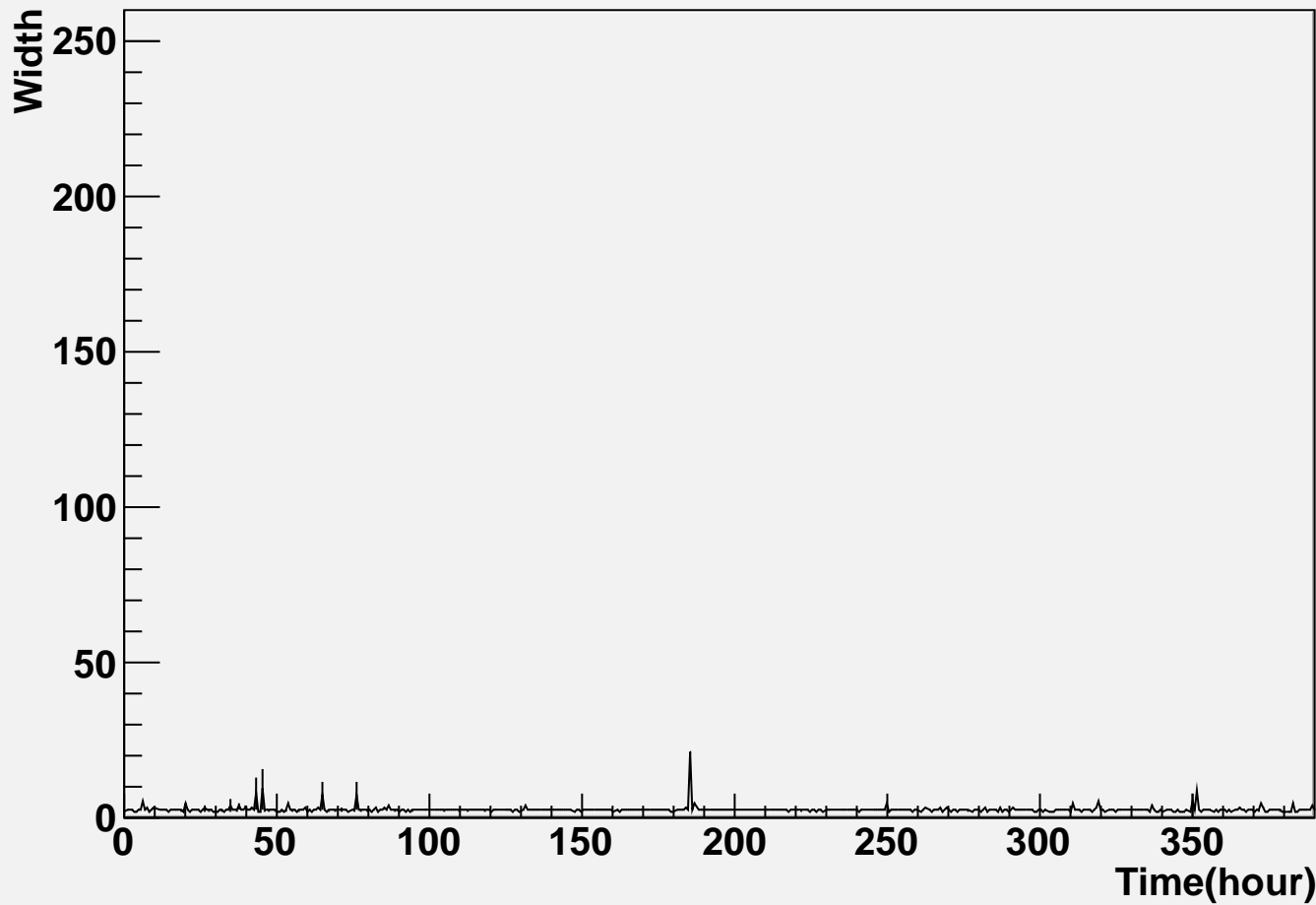
width-S2-SFEC01-Ch5



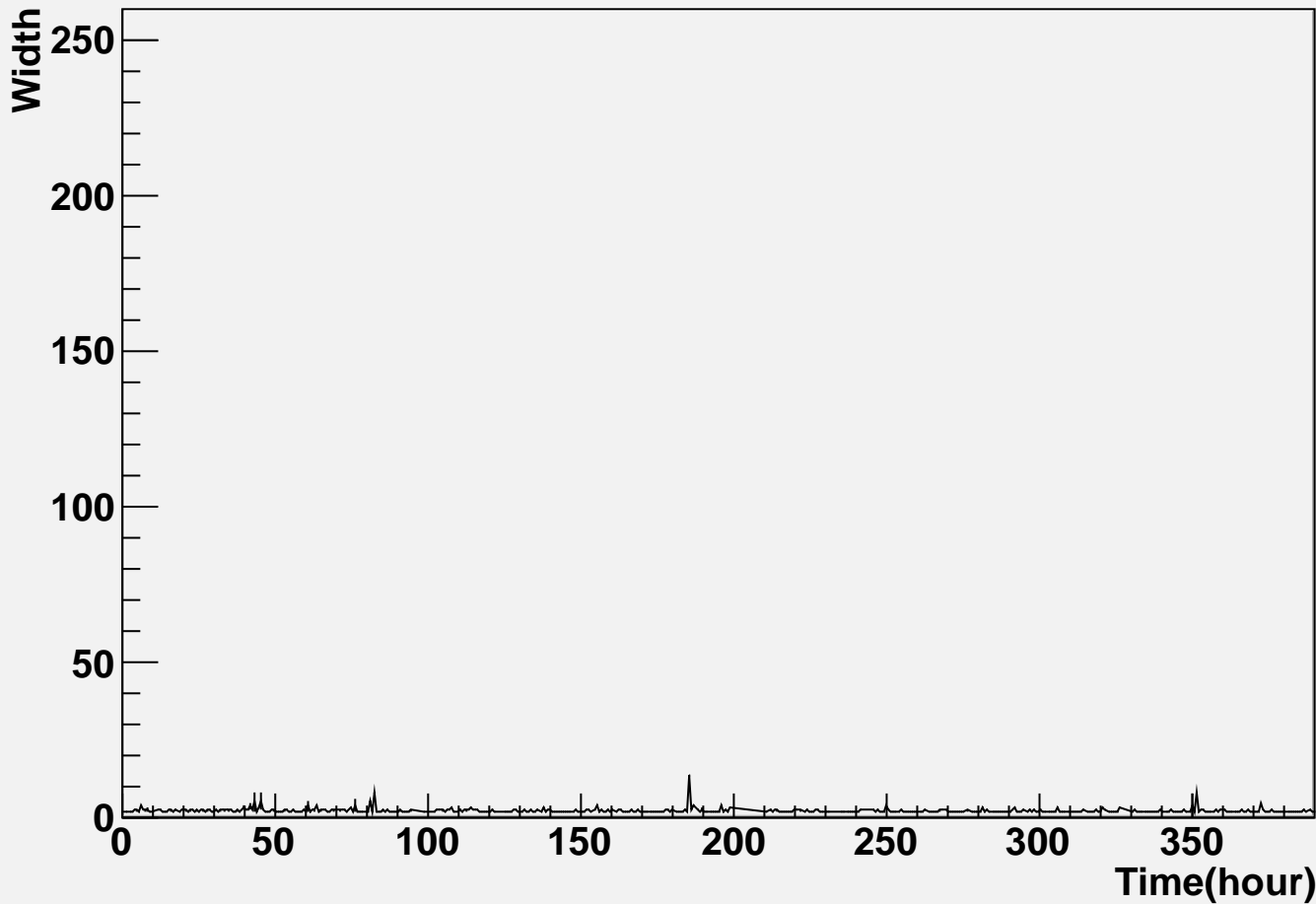
width-S2-SFEC01-Ch6



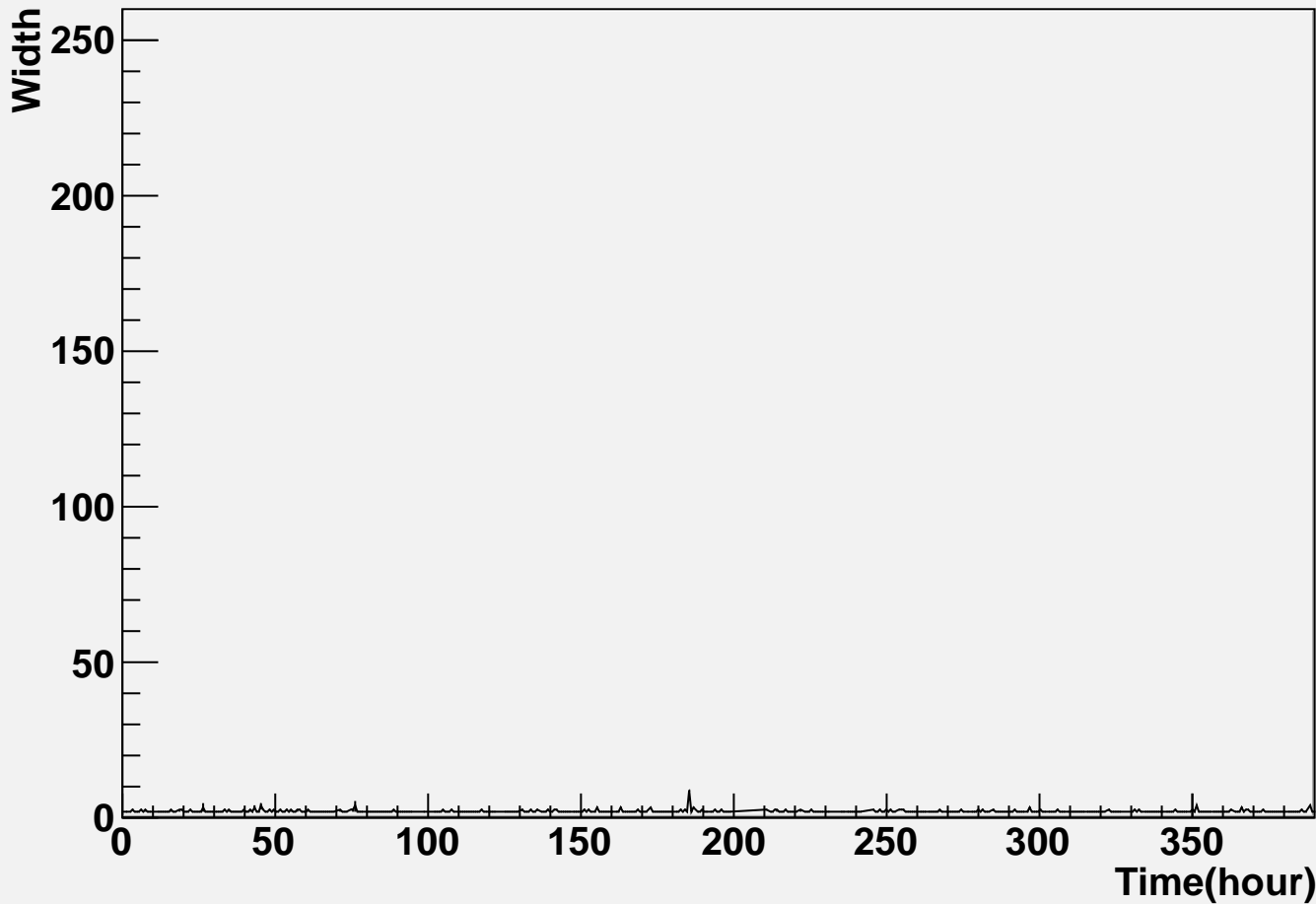
width-S2-SFEC01-Ch7



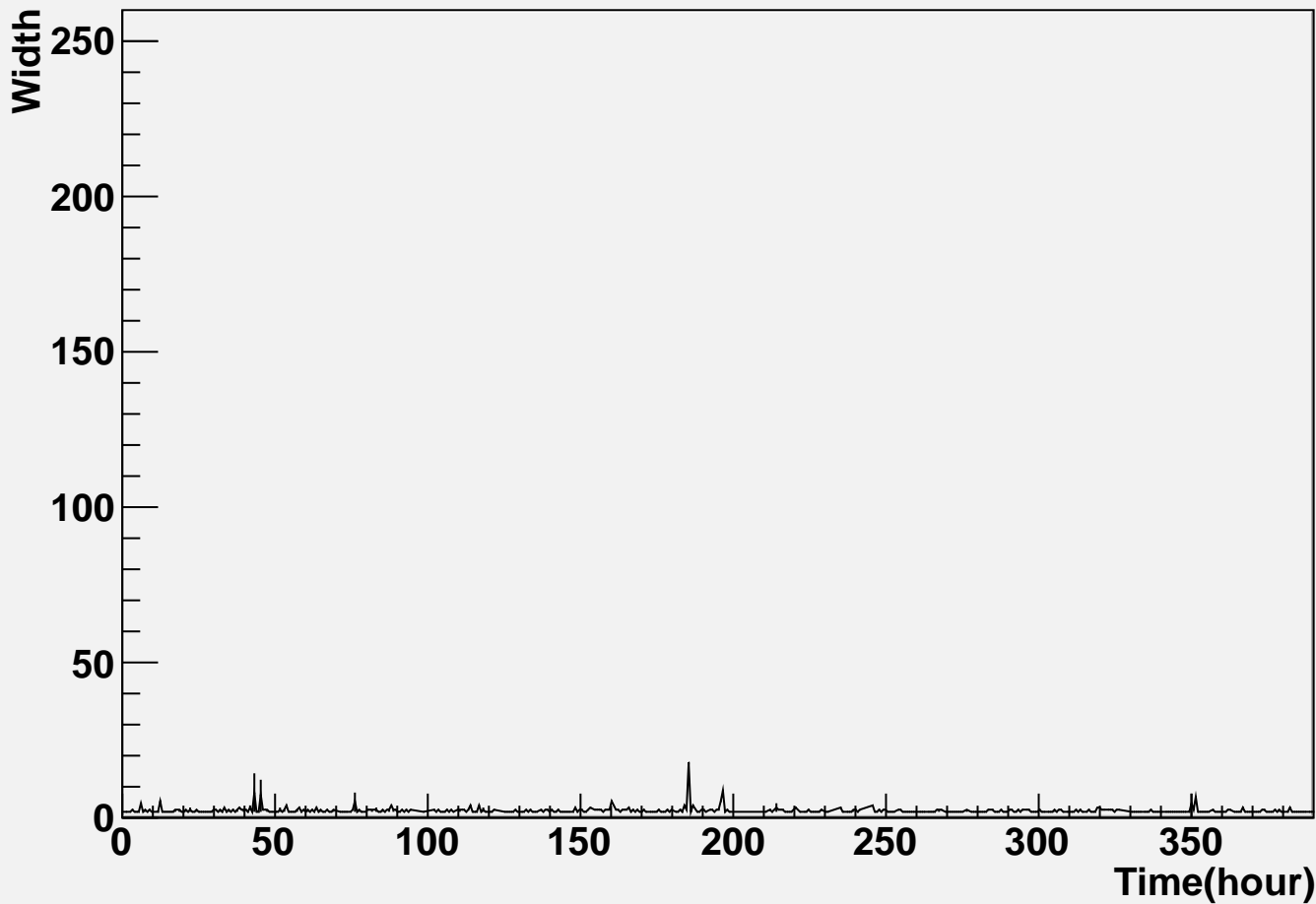
width-S2-SFEC01-Ch8



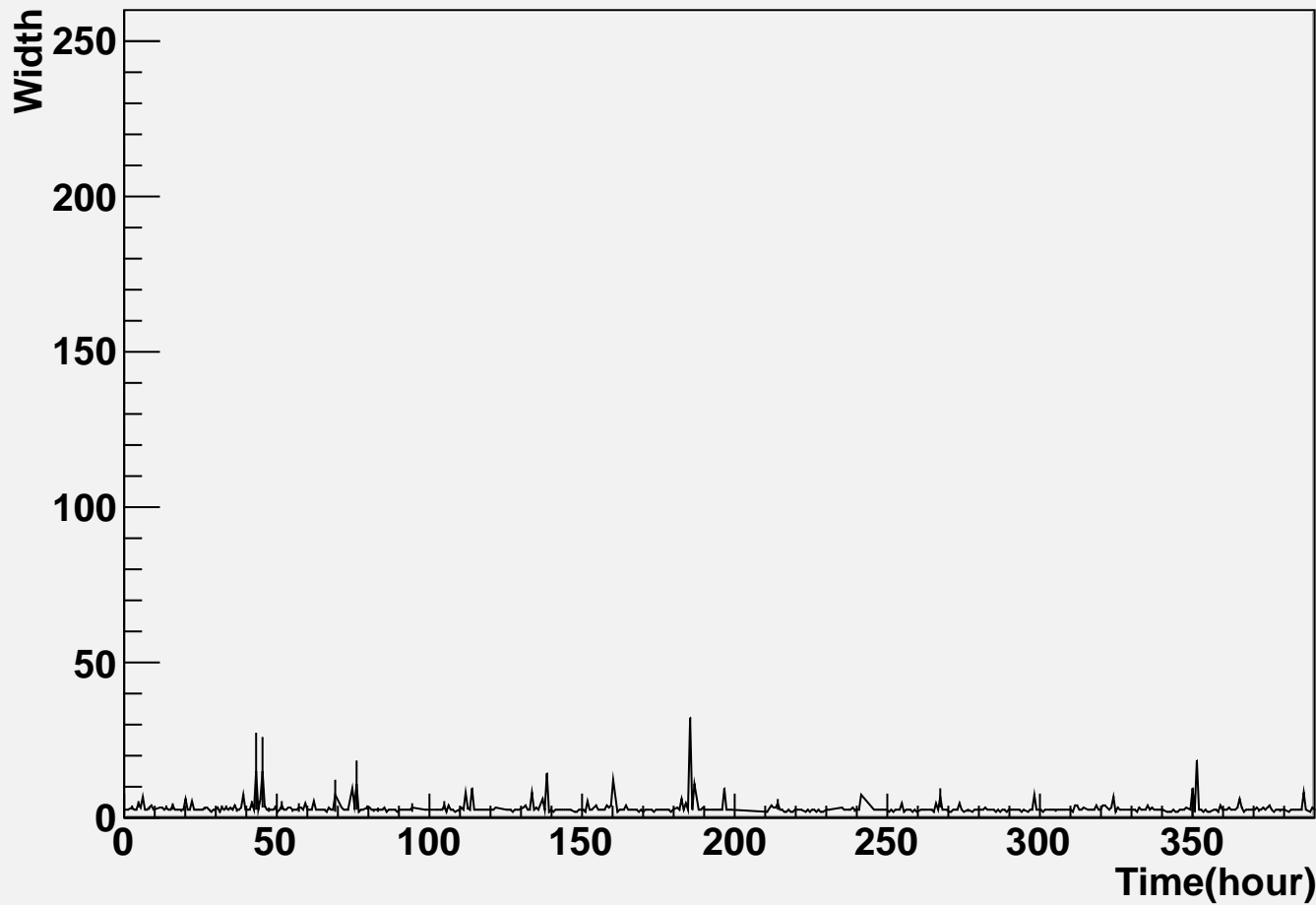
width-S2-SFEC01-Ch9



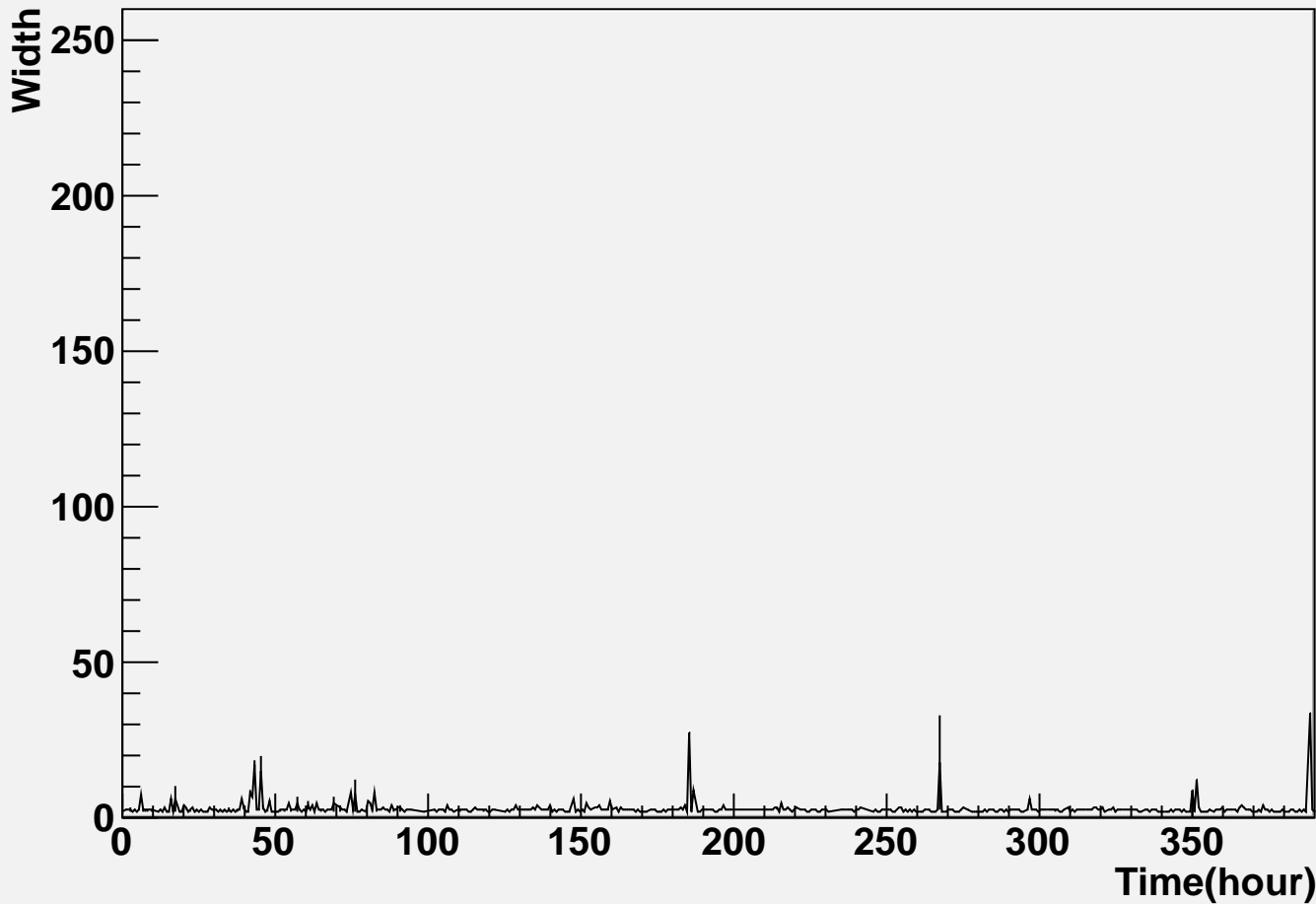
width-S2-SFEC10-Ch0



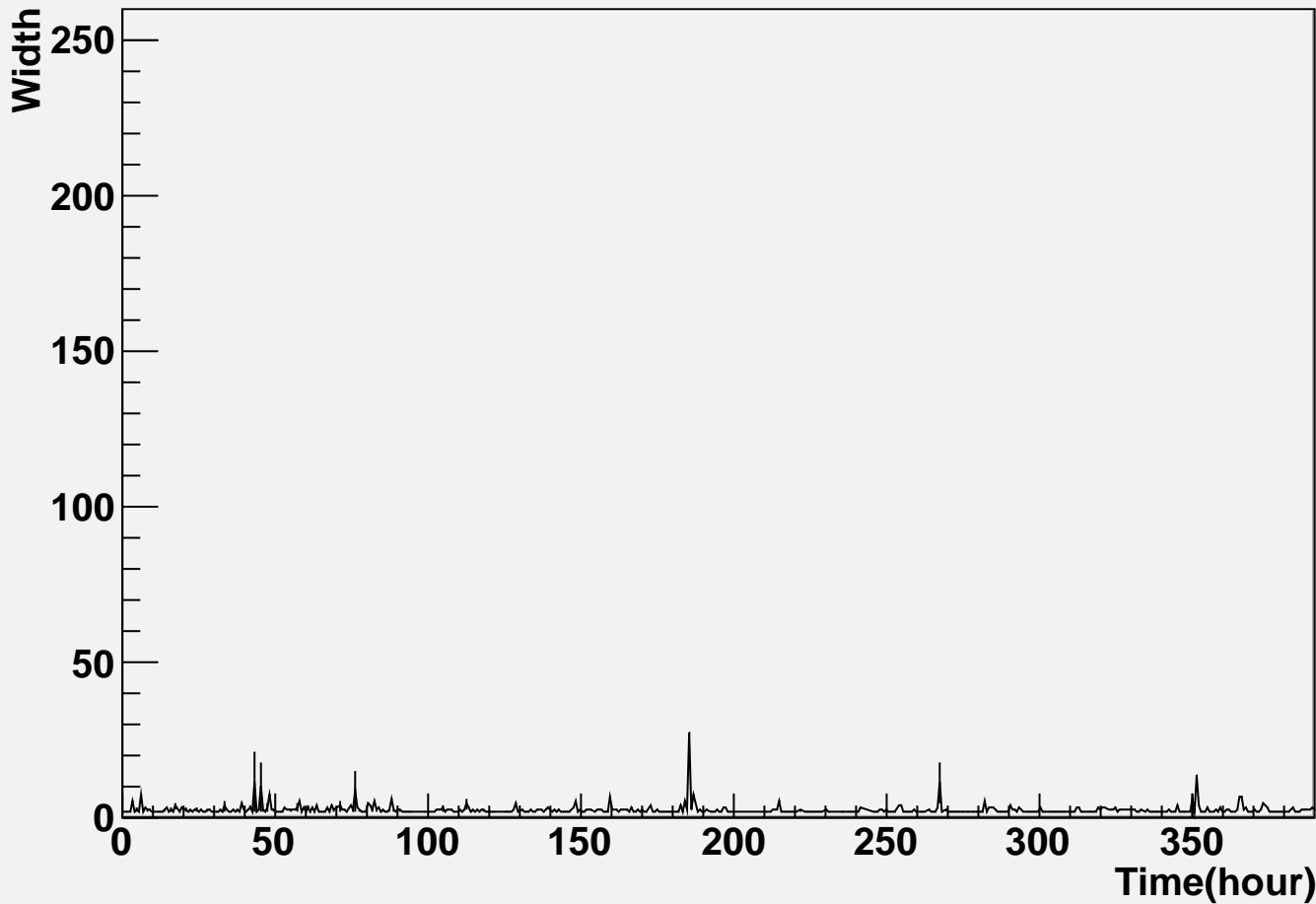
width-S2-SFEC10-Ch1



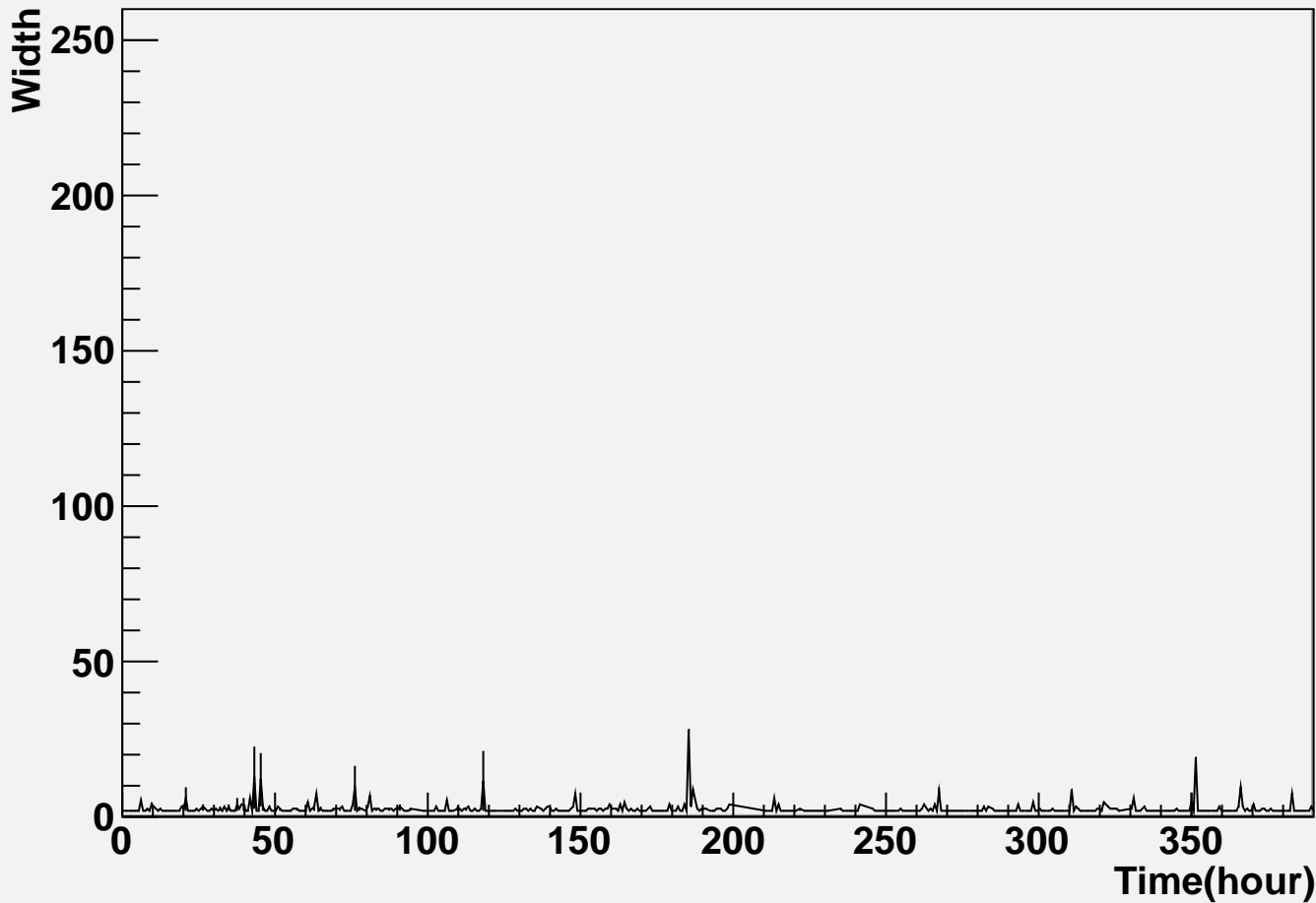
width-S2-SFEC10-Ch2



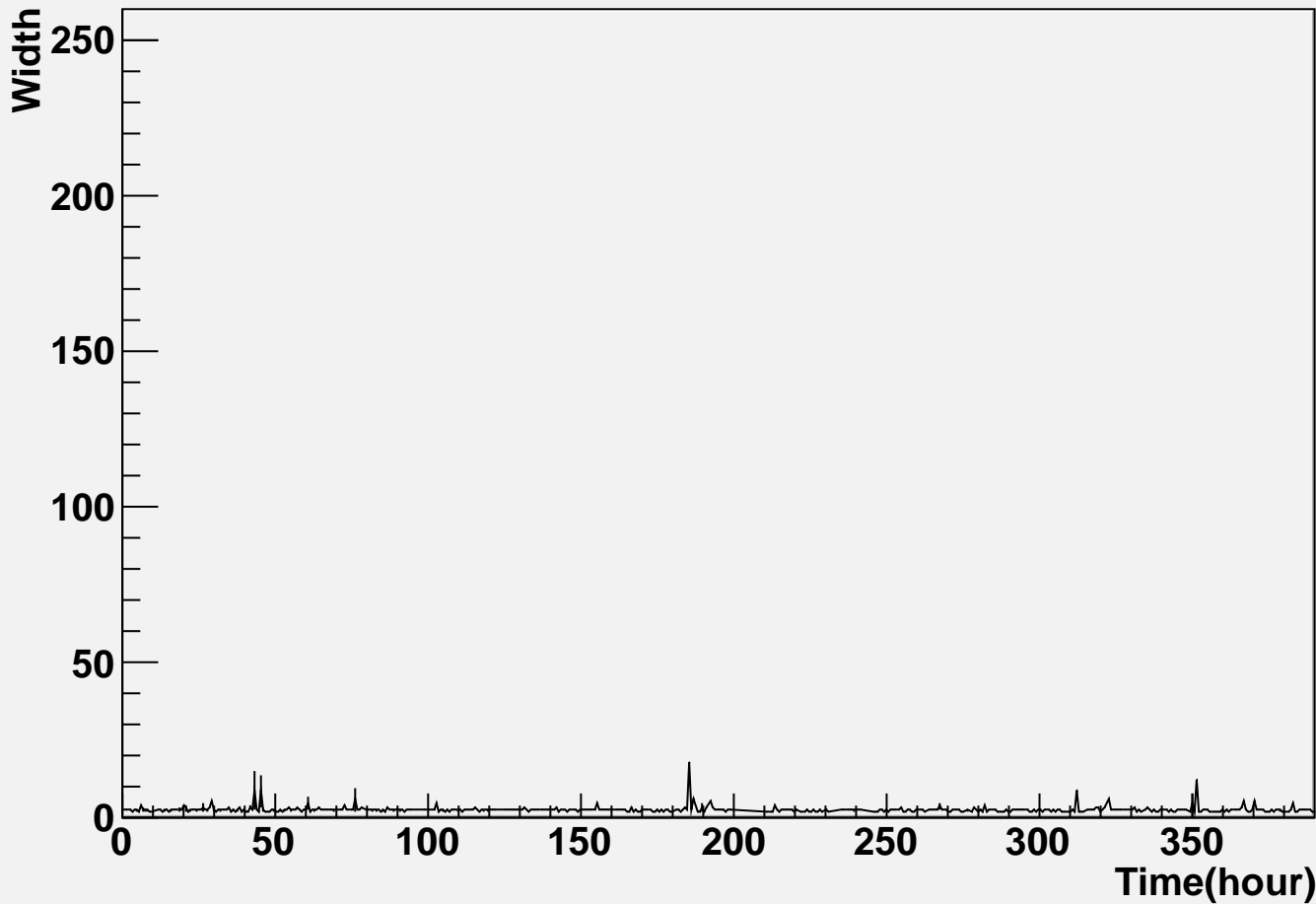
width-S2-SFEC10-Ch3



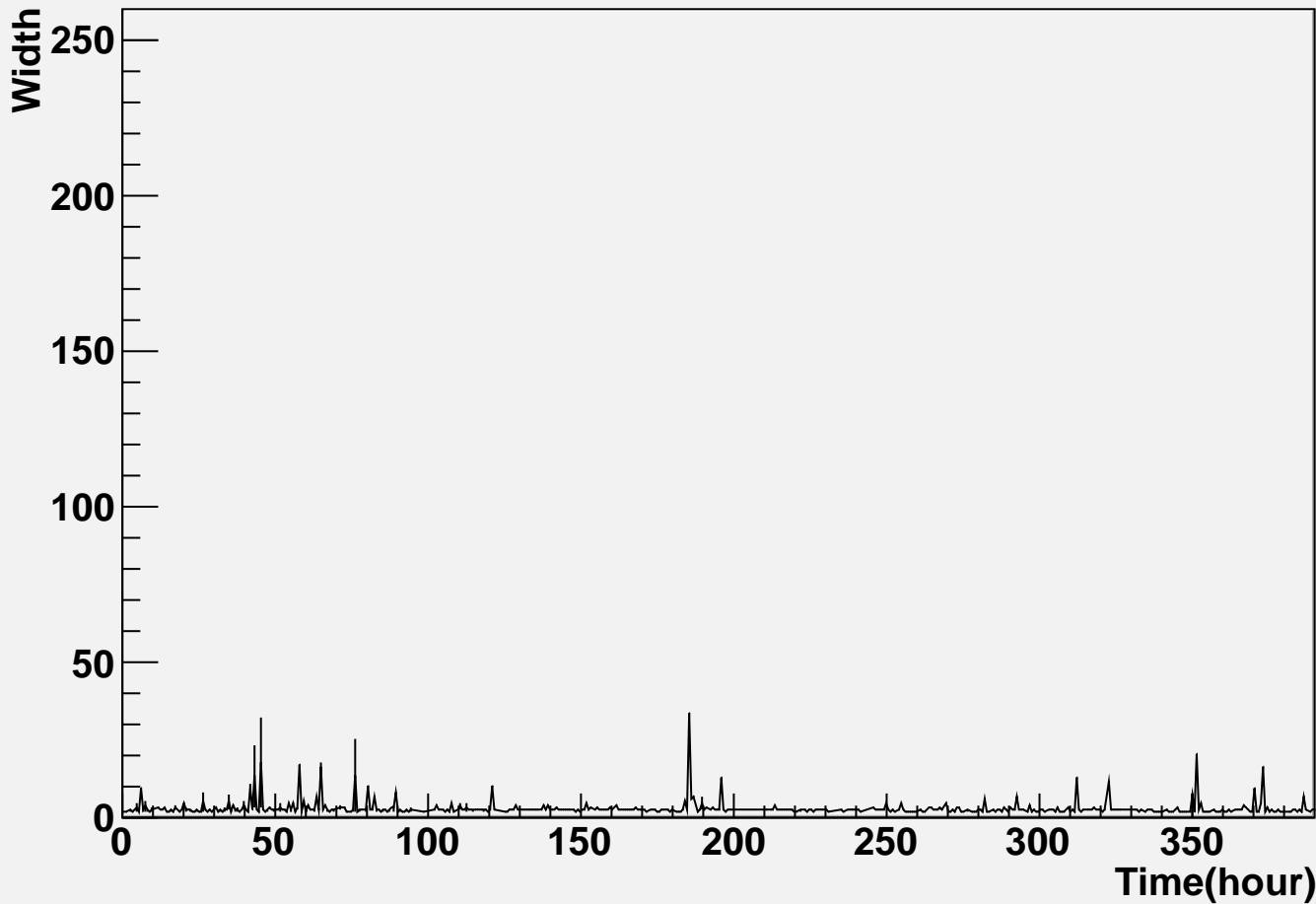
width-S2-SFEC10-Ch4



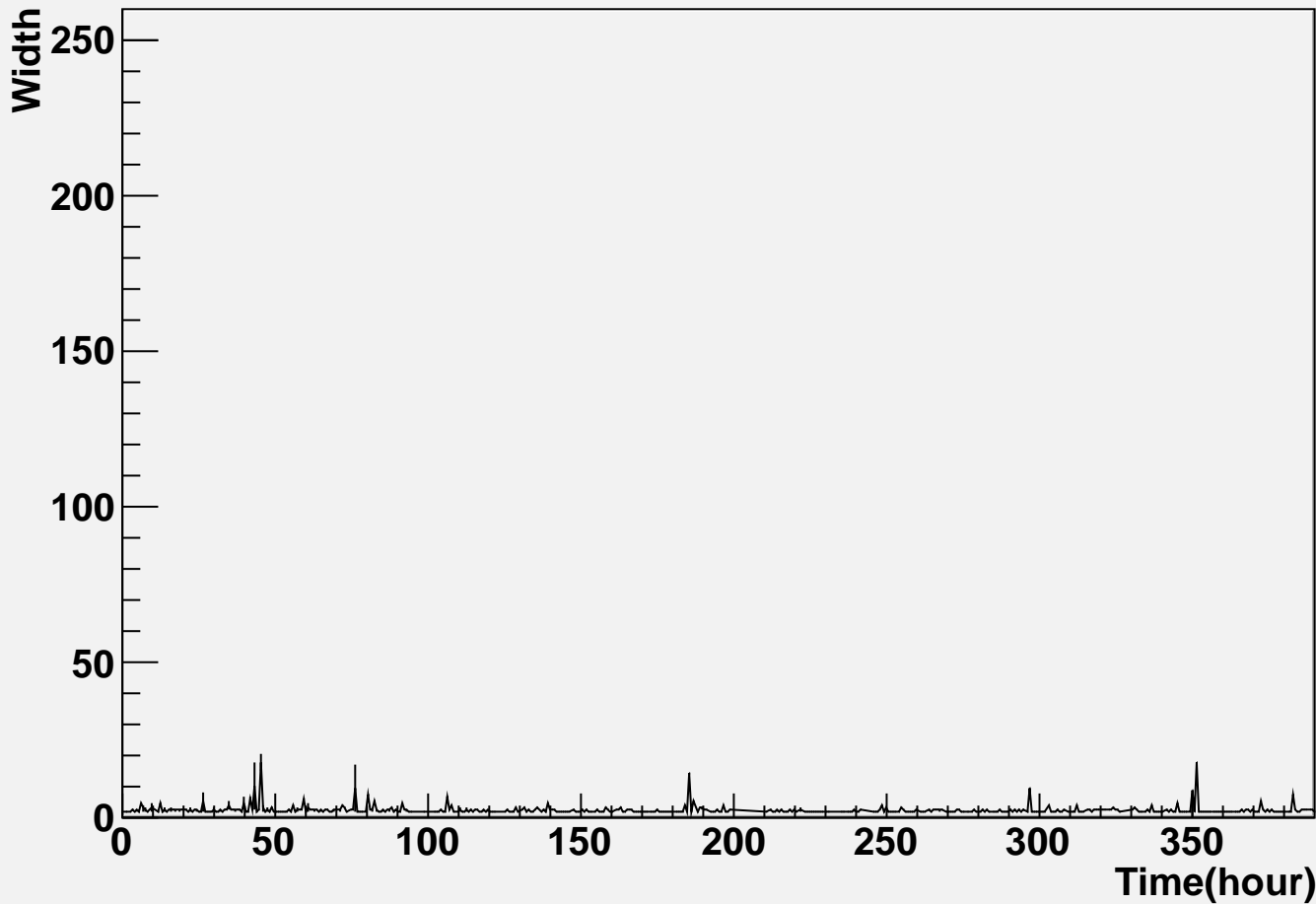
width-S2-SFEC10-Ch5



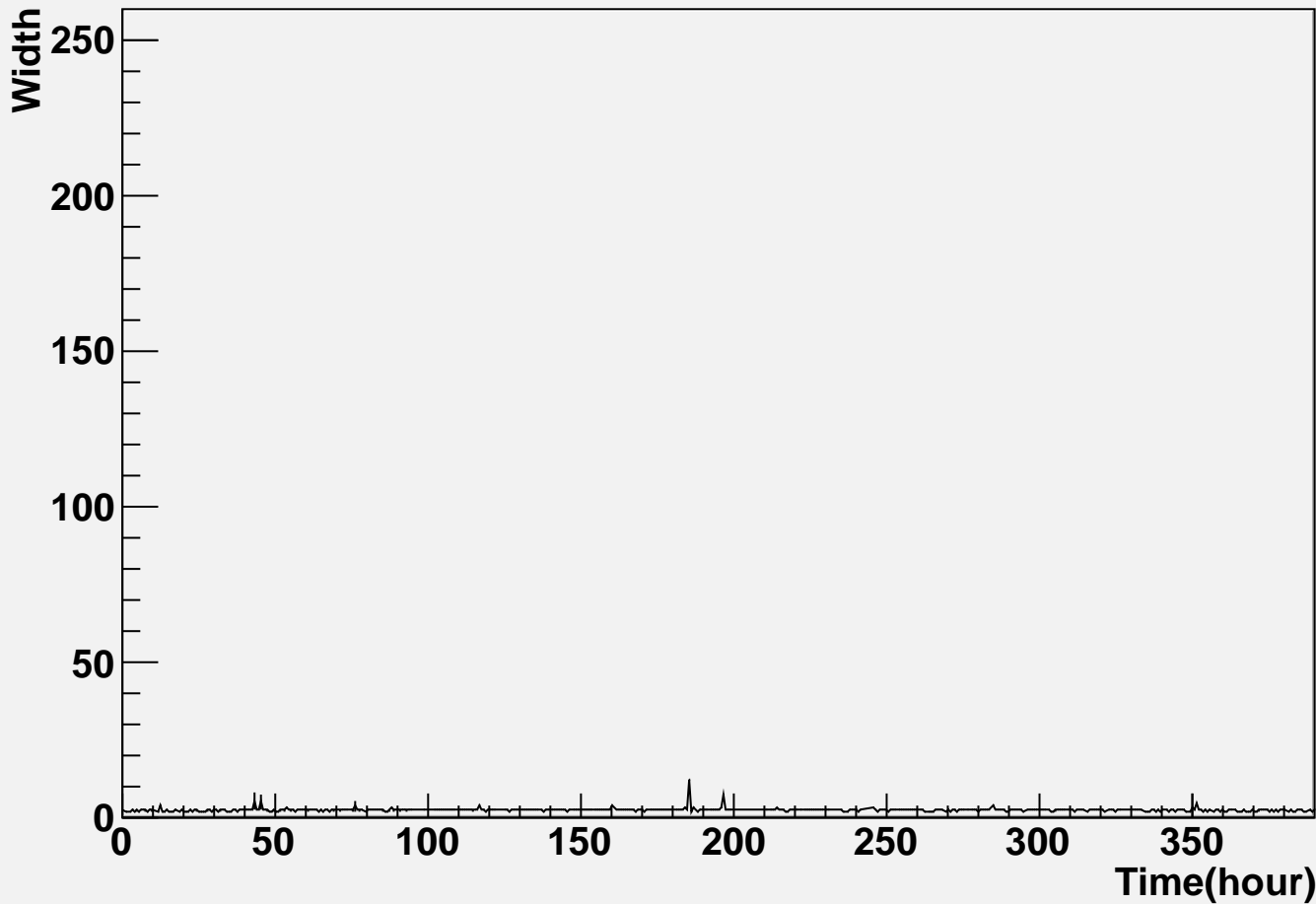
width-S2-SFEC10-Ch6



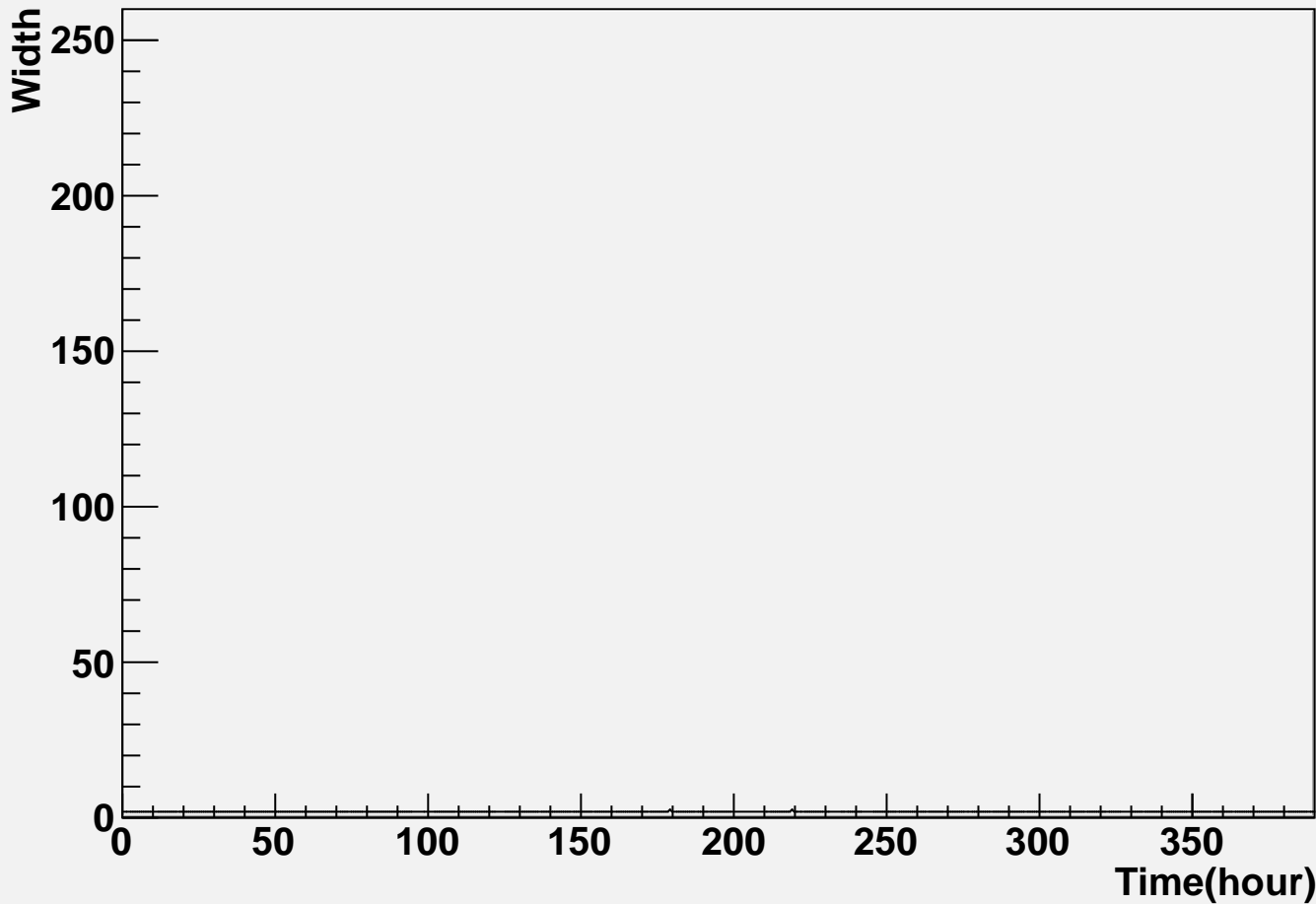
width-S2-SFEC10-Ch7



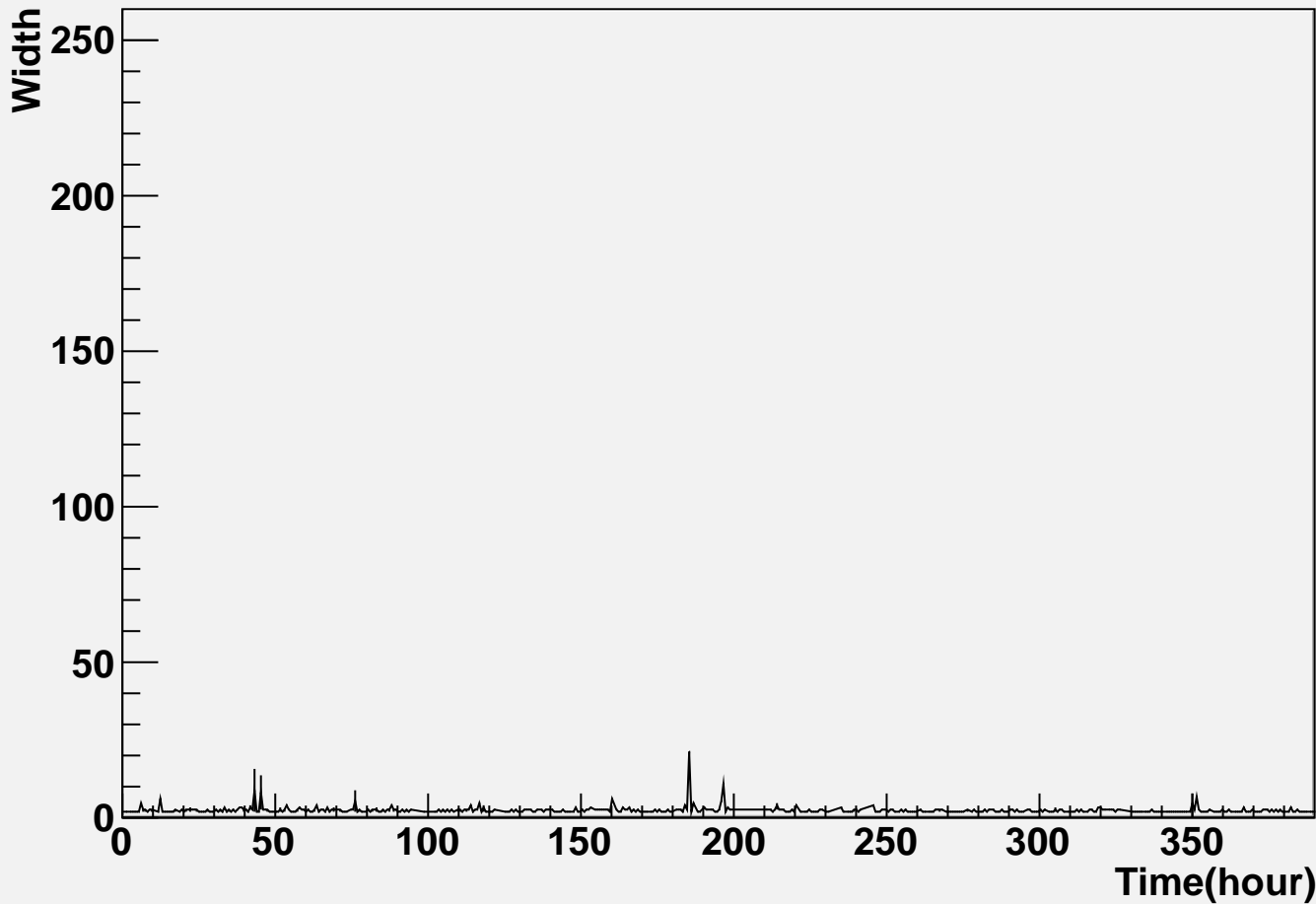
width-S2-SFEC10-Ch8



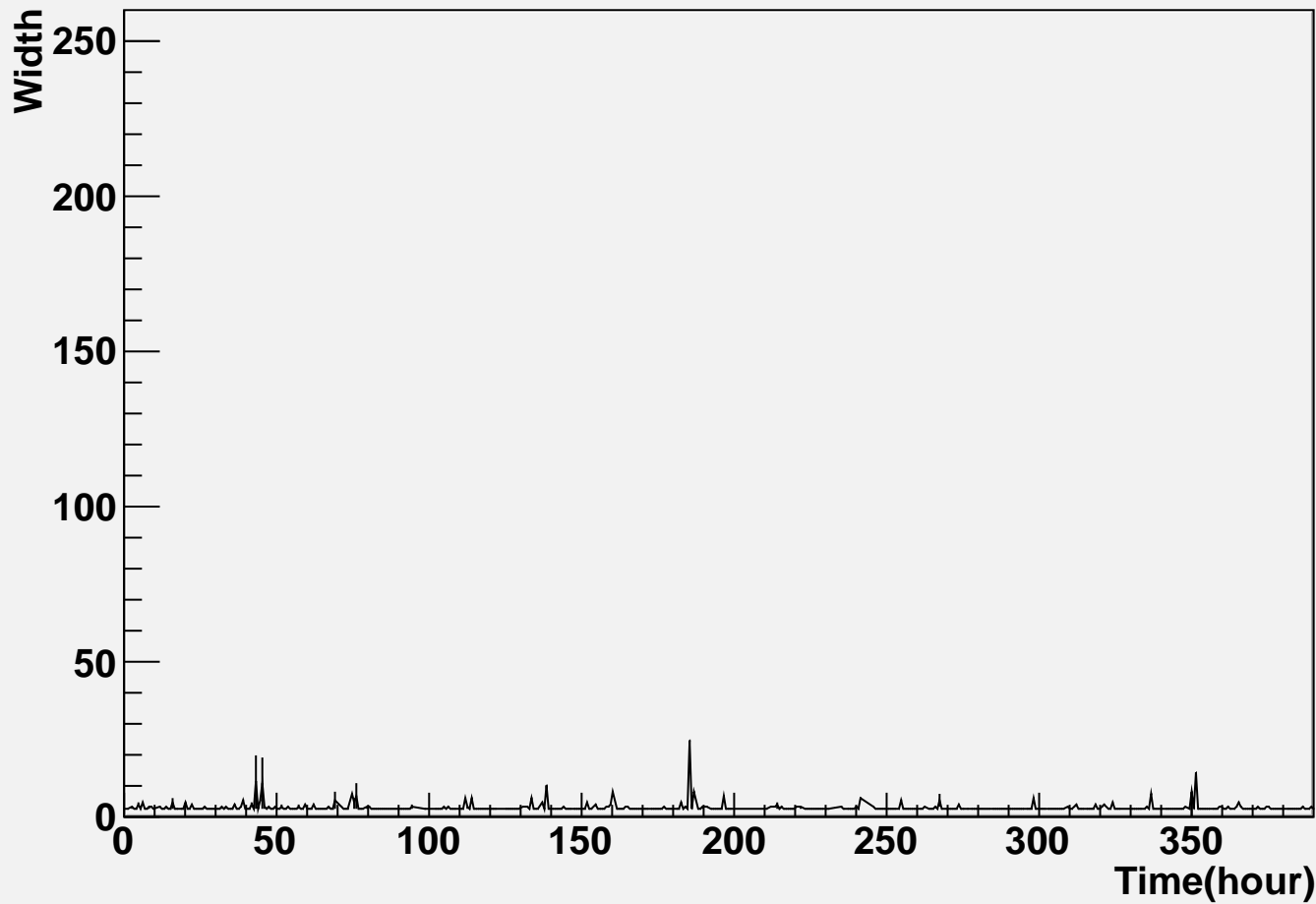
width-S2-SFEC10-Ch9



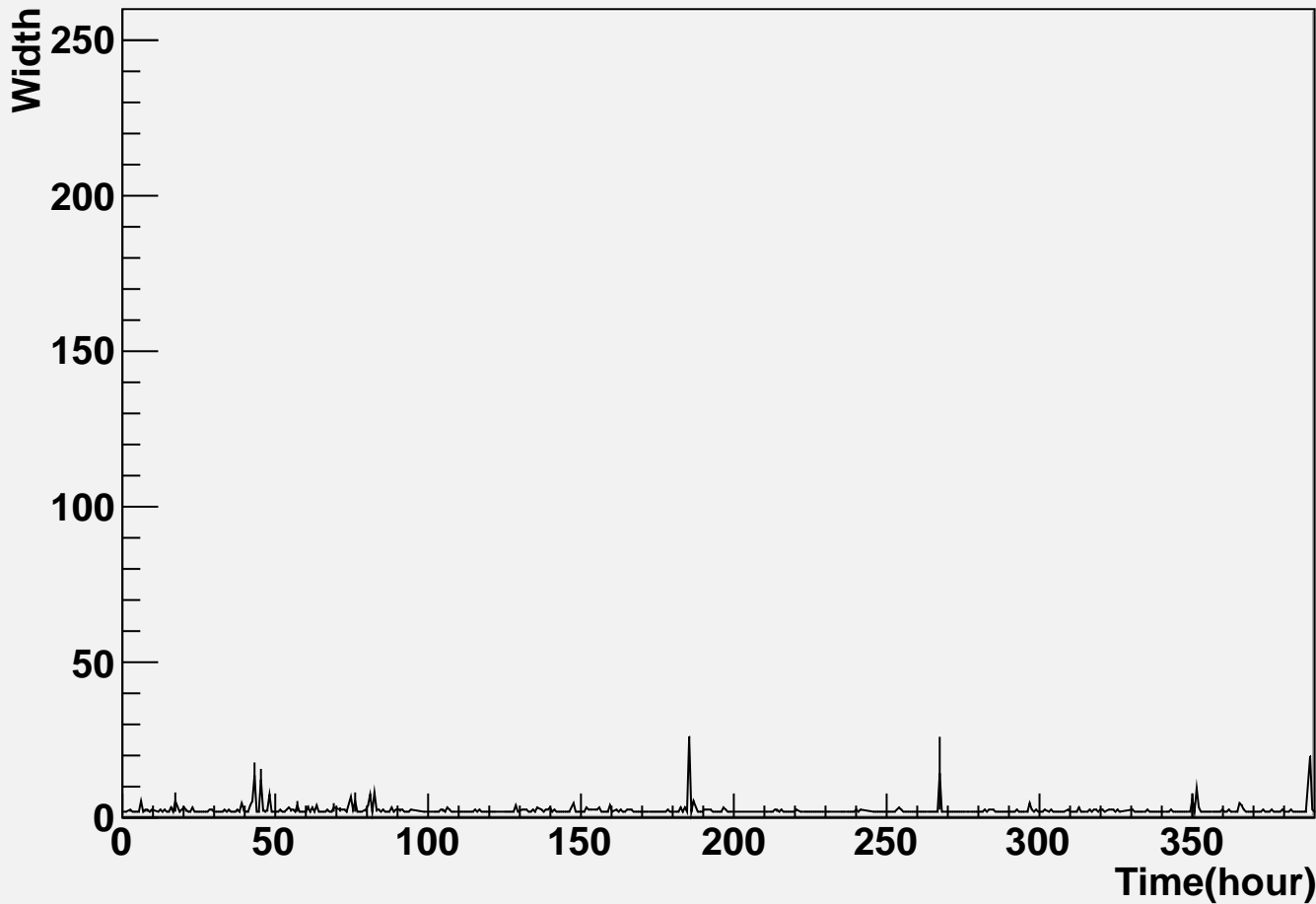
width-S2-SFEC11-Ch0



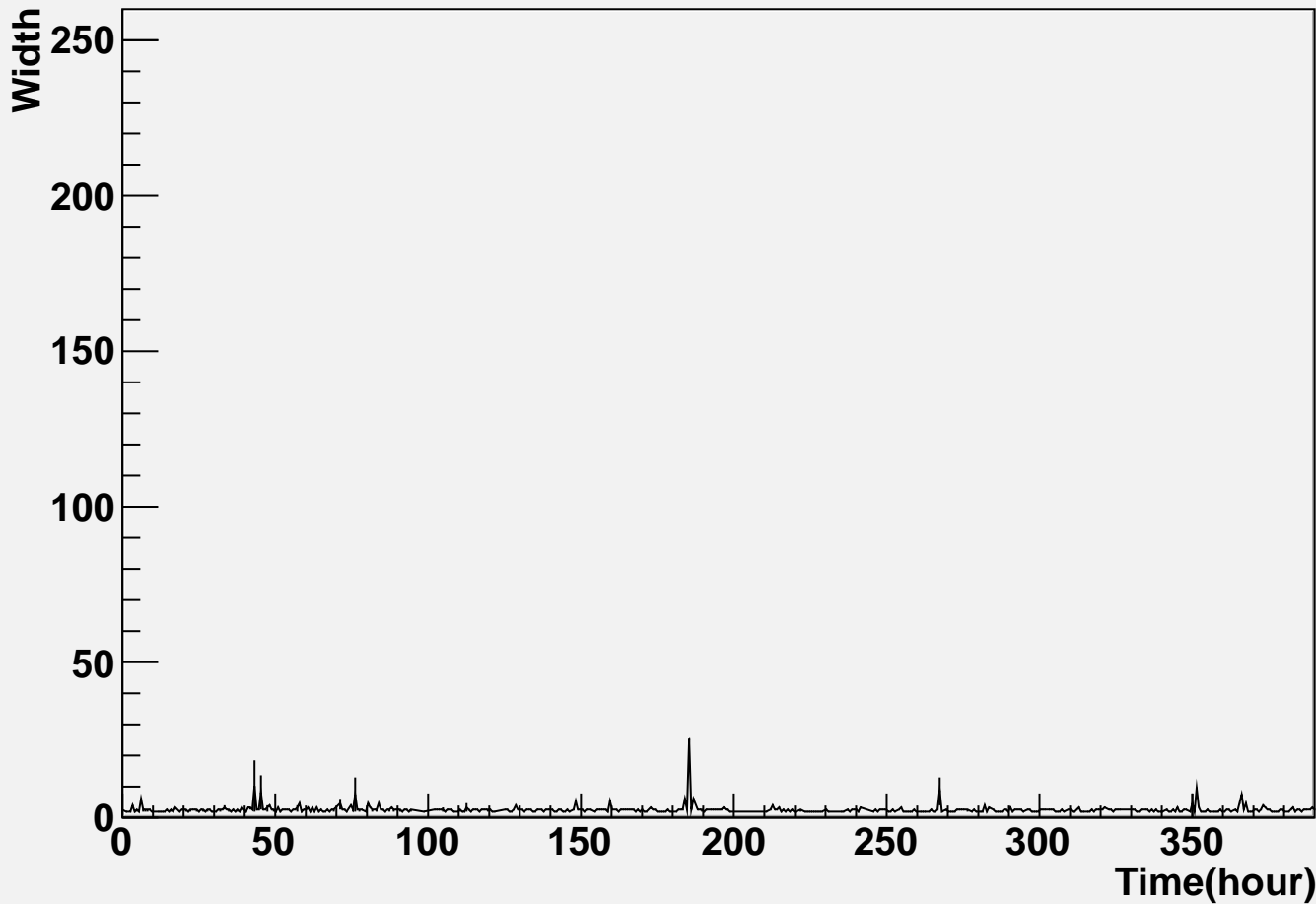
width-S2-SFEC11-Ch1



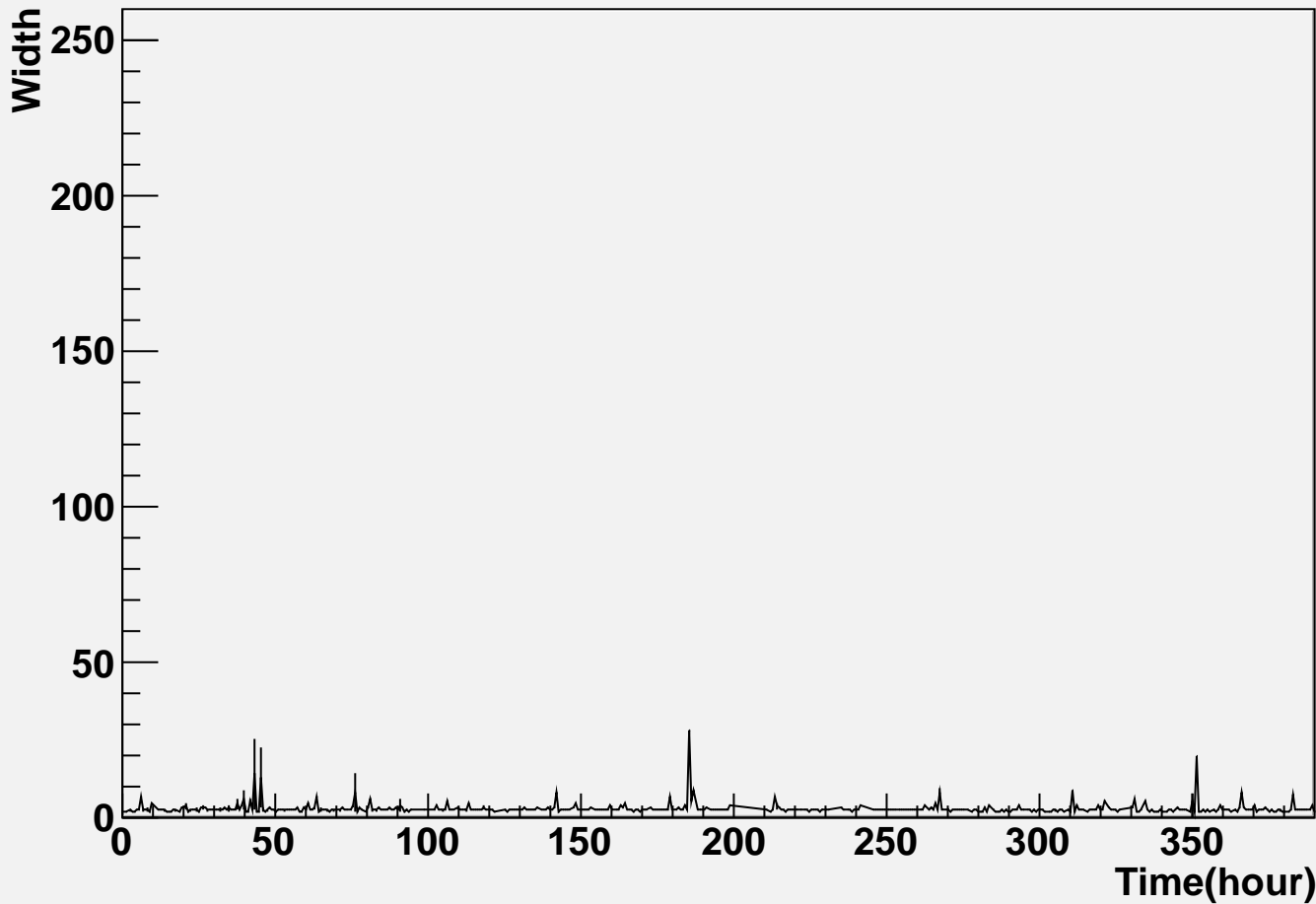
width-S2-SFEC11-Ch2



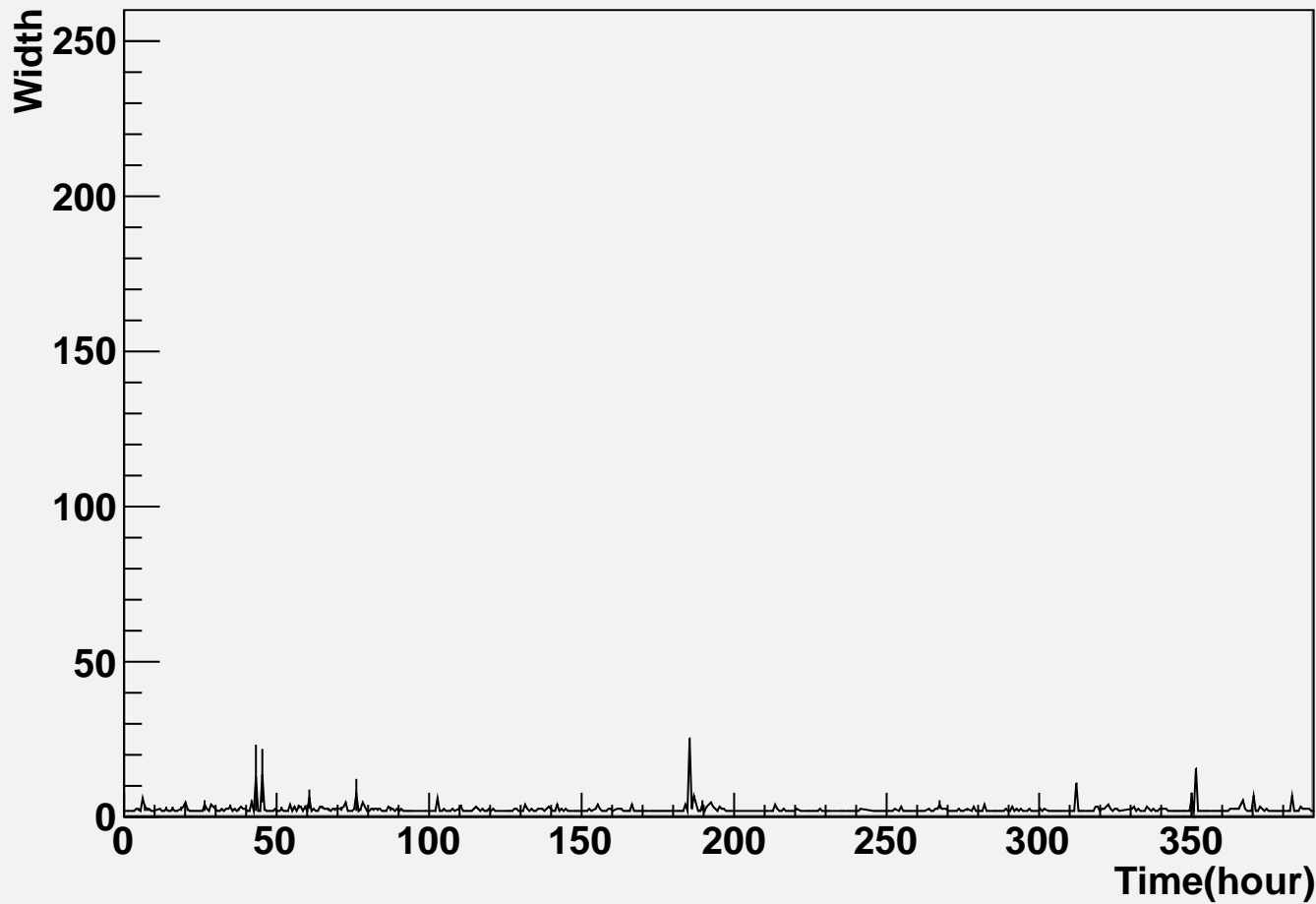
width-S2-SFEC11-Ch3



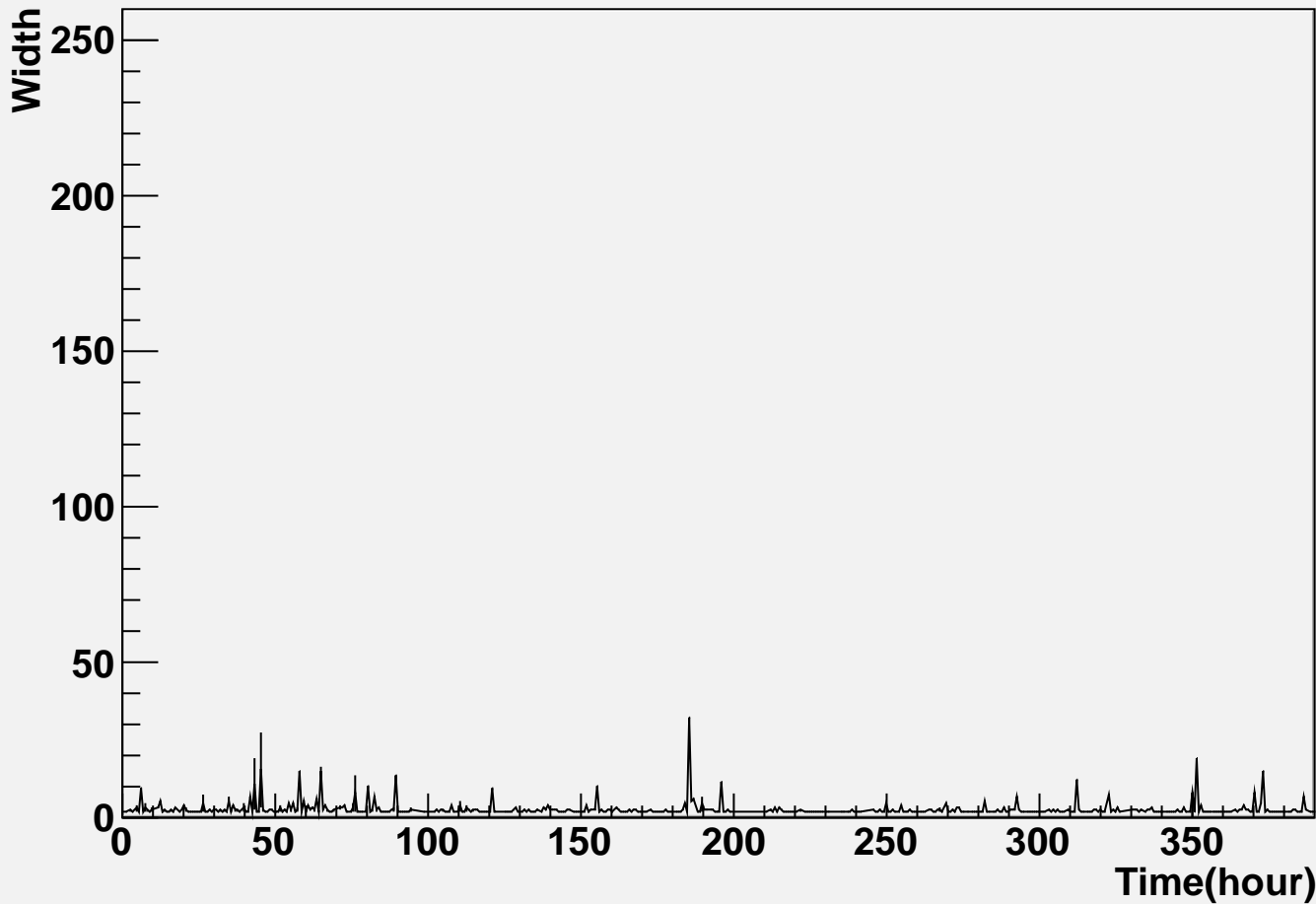
width-S2-SFEC11-Ch4



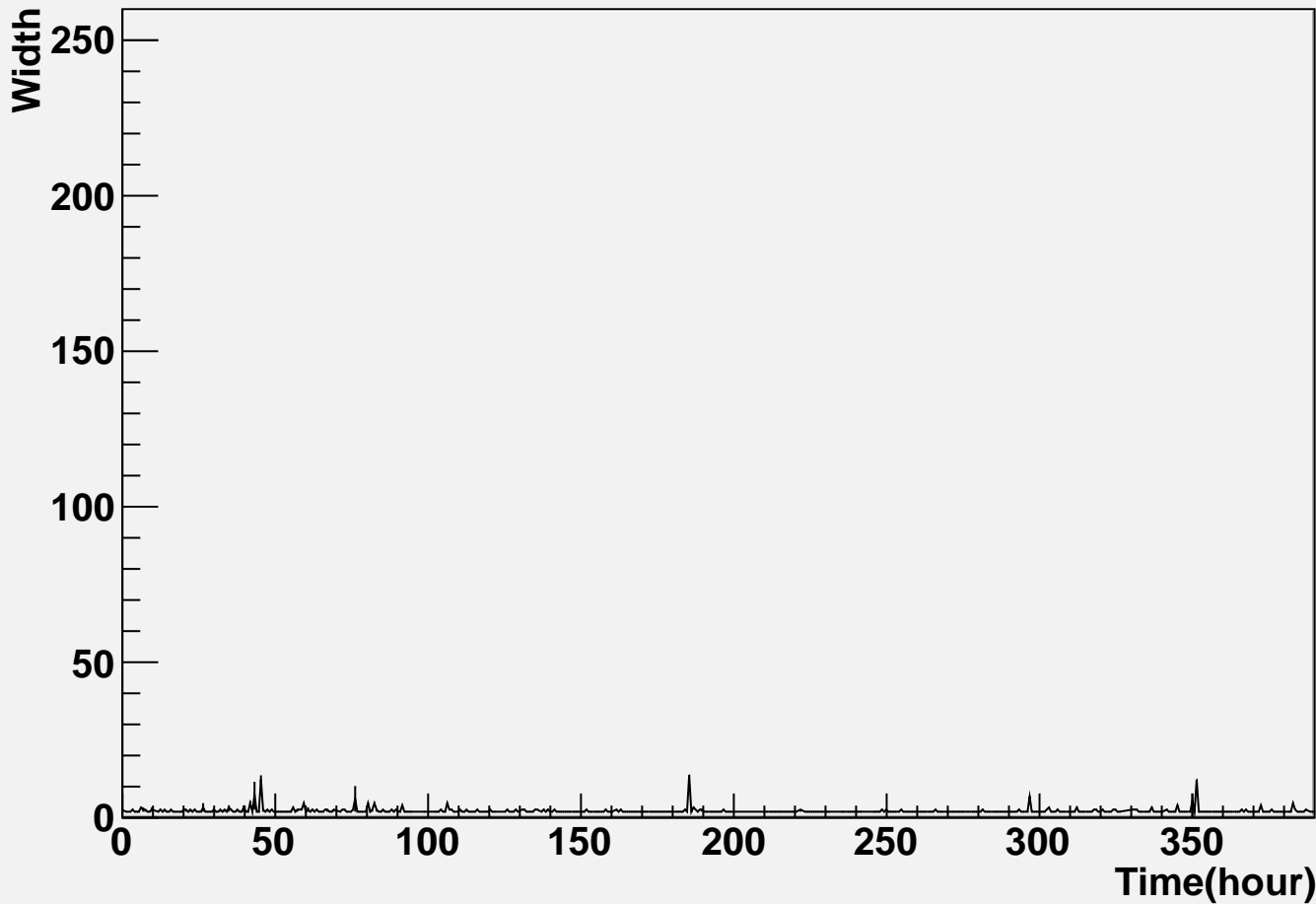
width-S2-SFEC11-Ch5



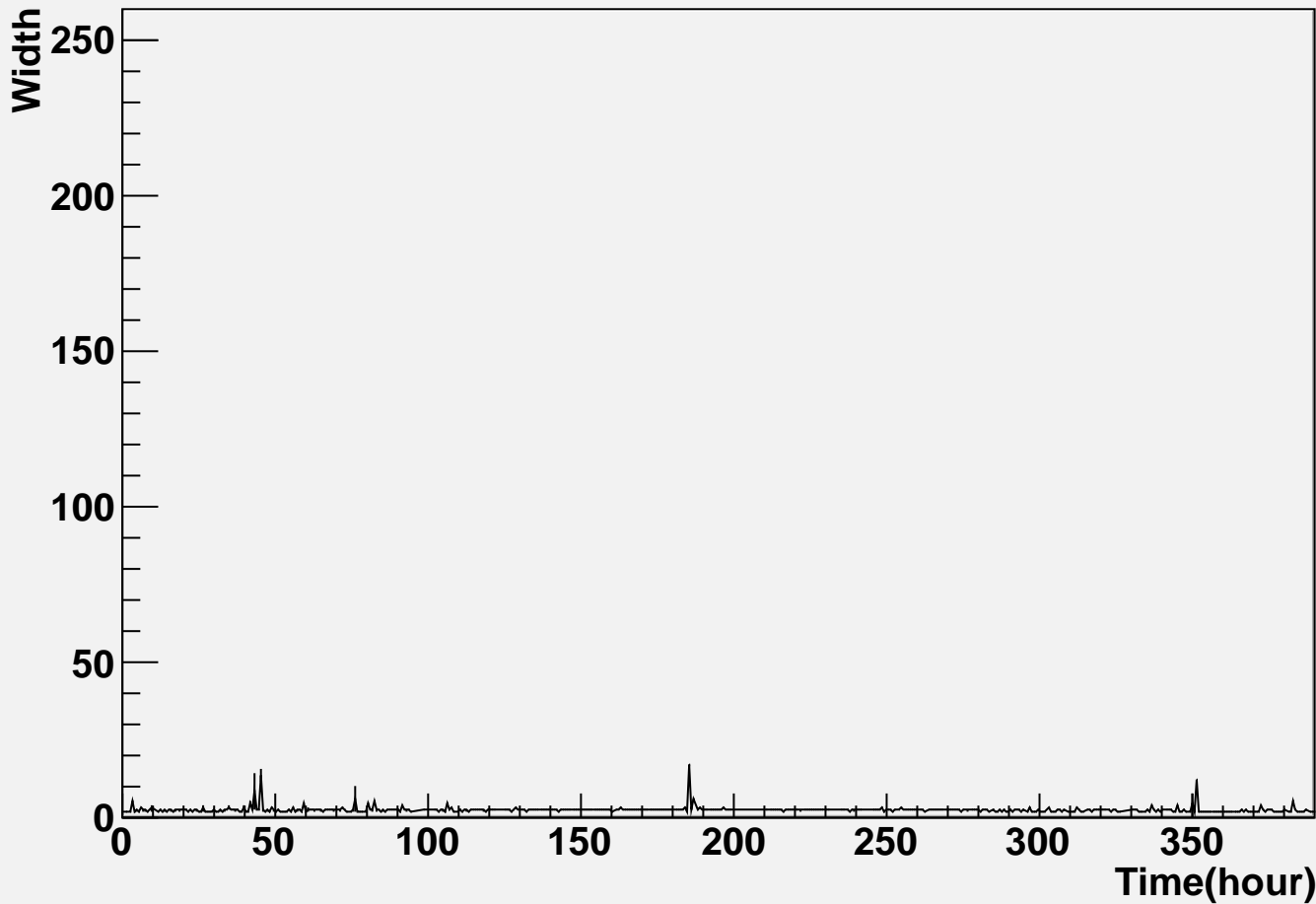
width-S2-SFEC11-Ch6



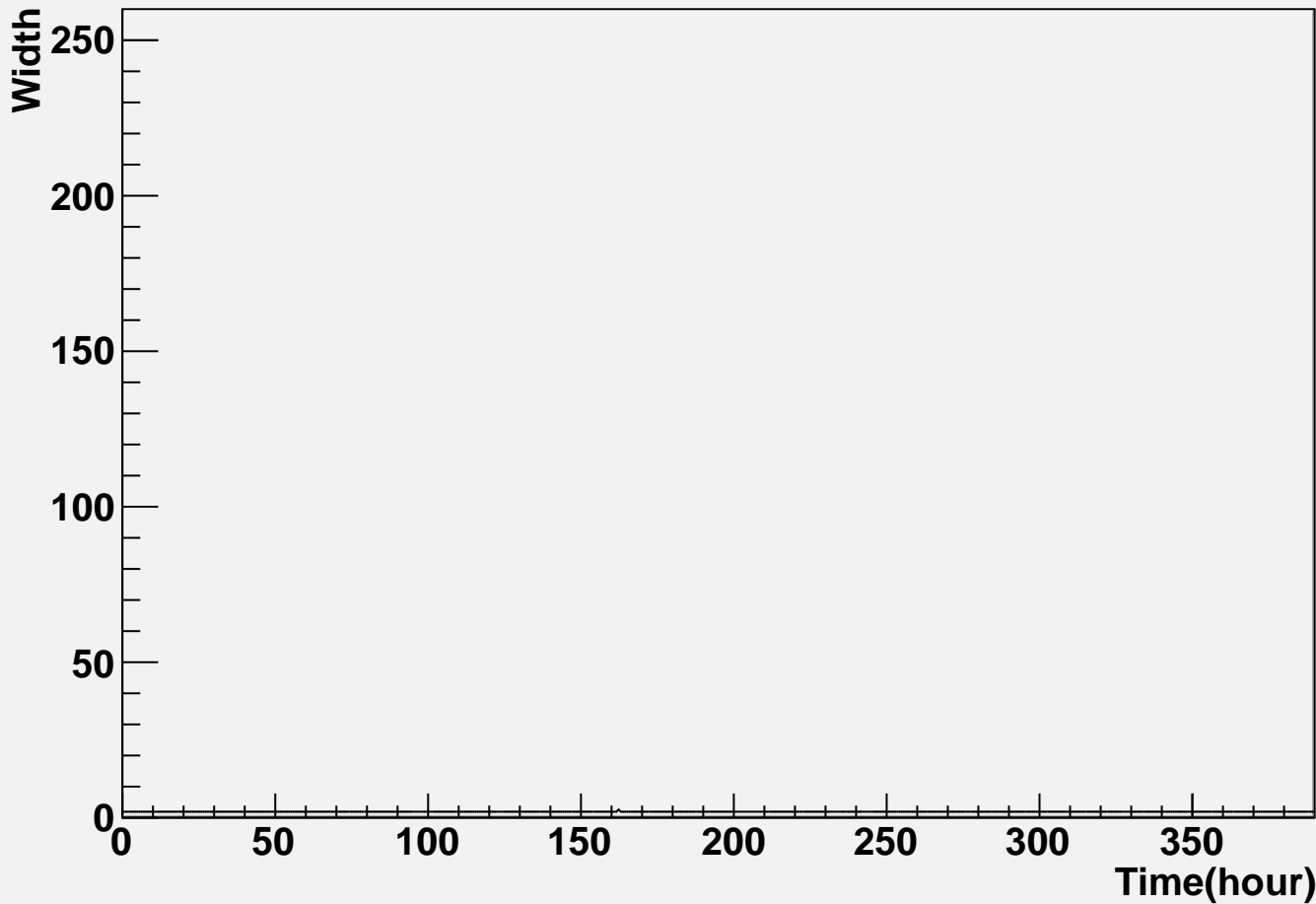
width-S2-SFEC11-Ch7



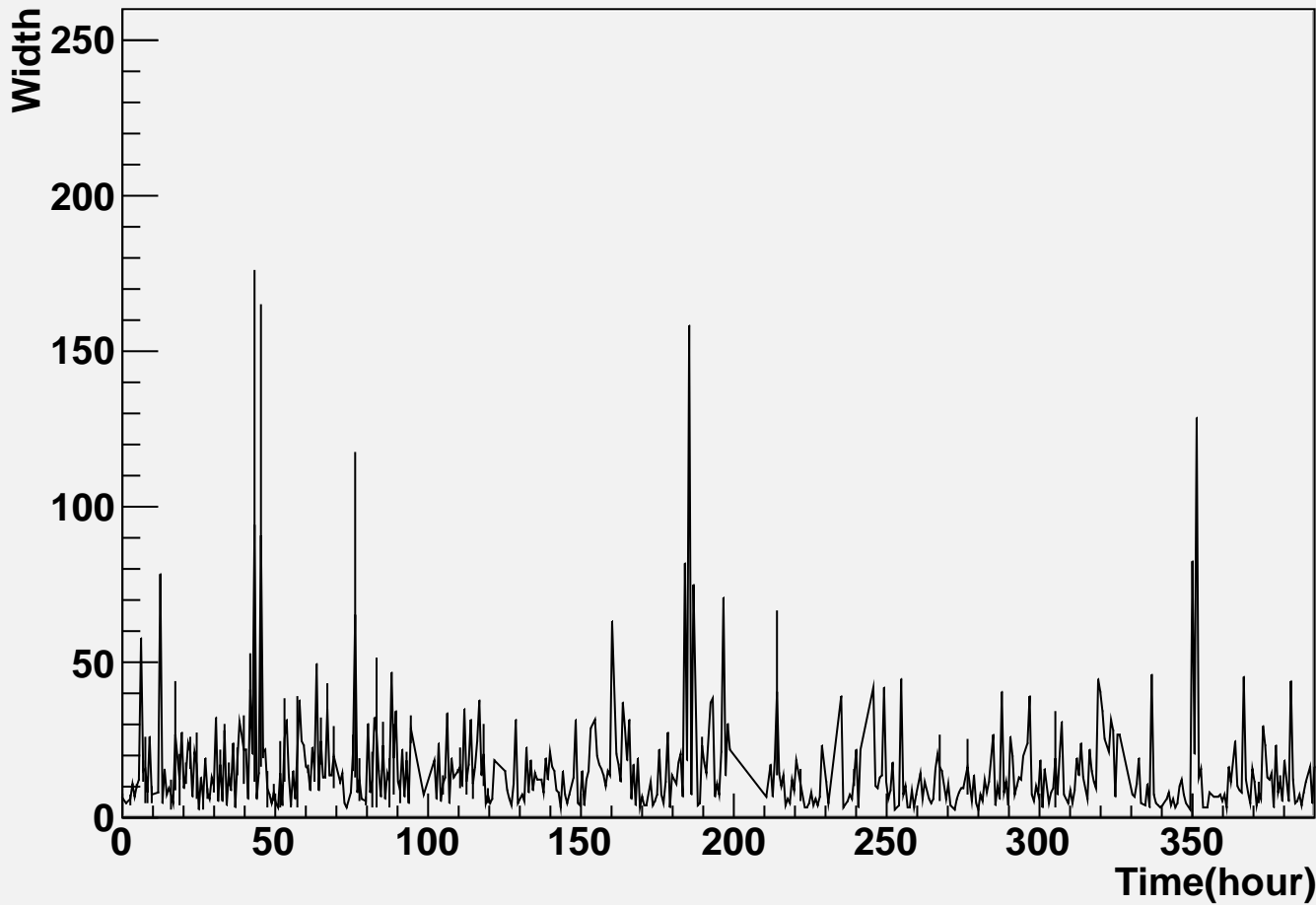
width-S2-SFEC11-Ch8



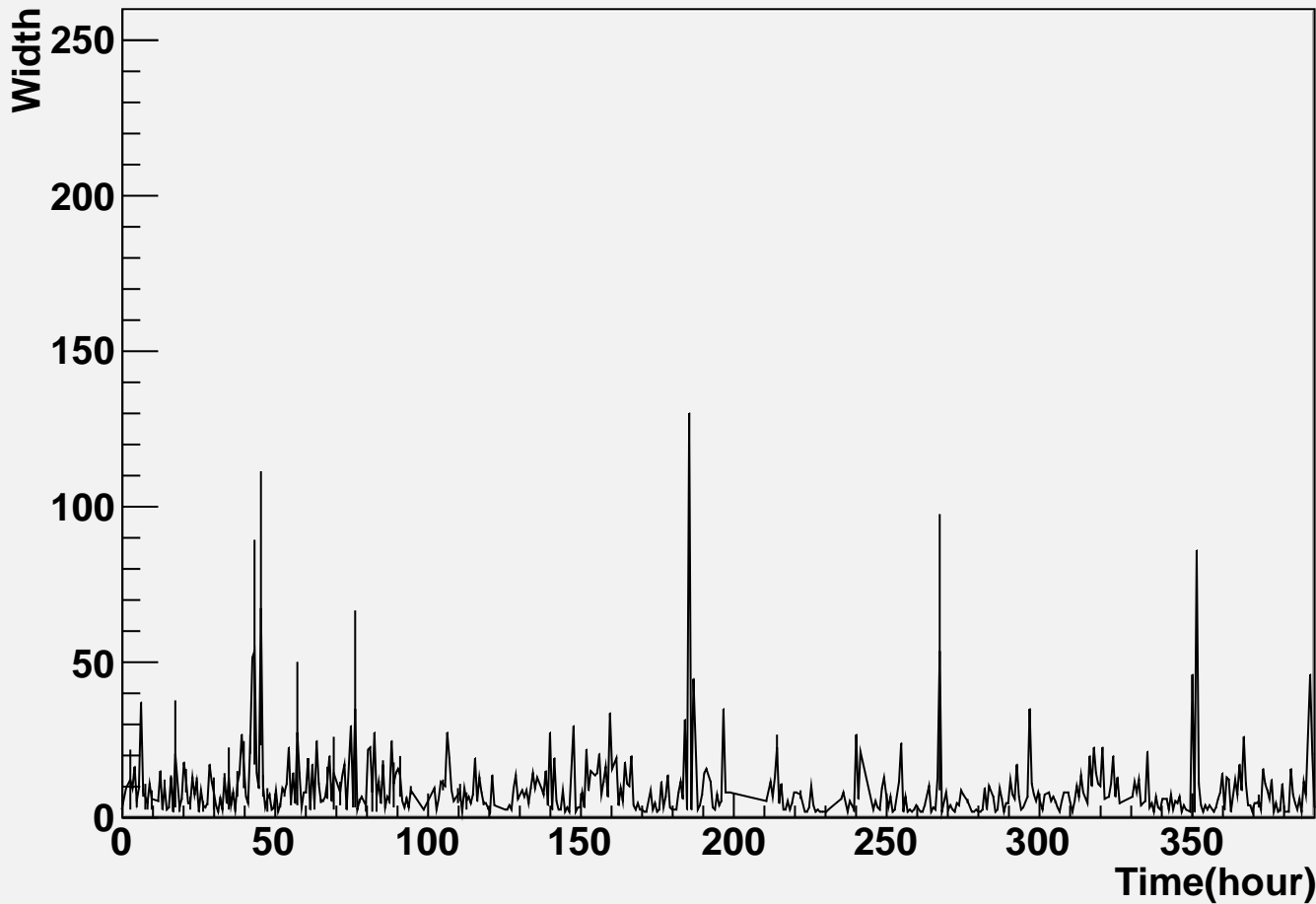
width-S2-SFEC11-Ch9



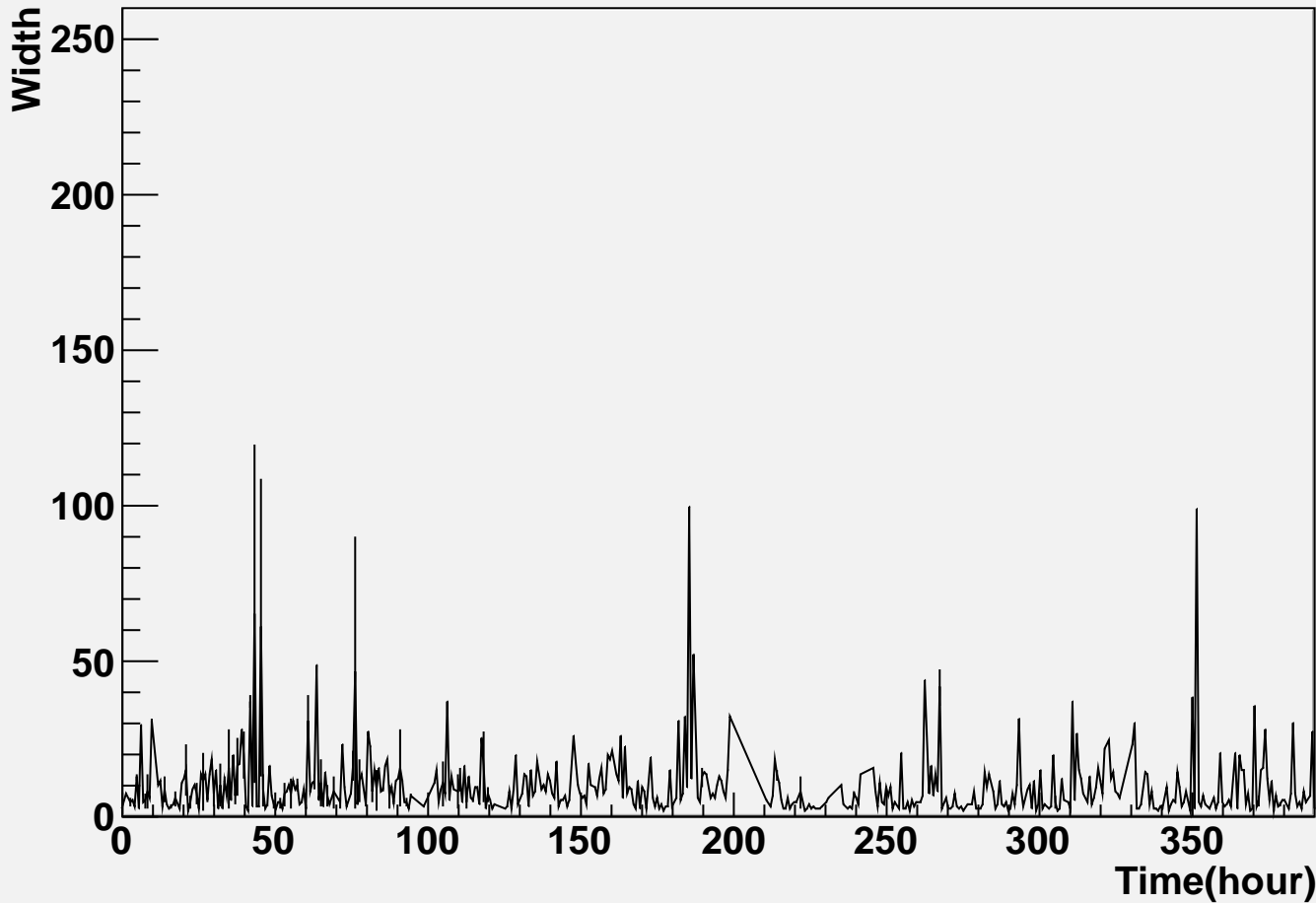
width-S3-SFET2A-Ch0



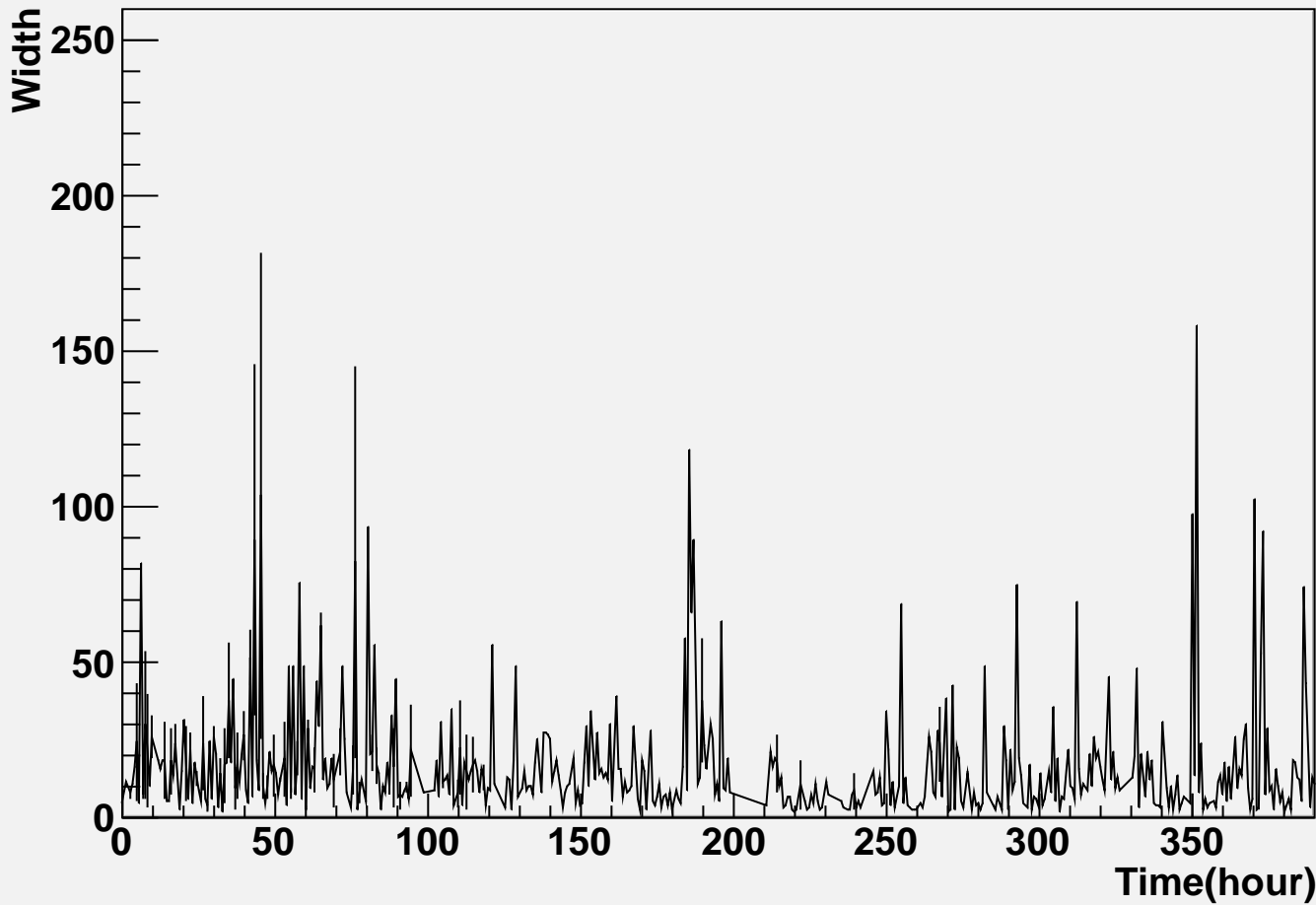
width-S3-SFET2A-Ch1



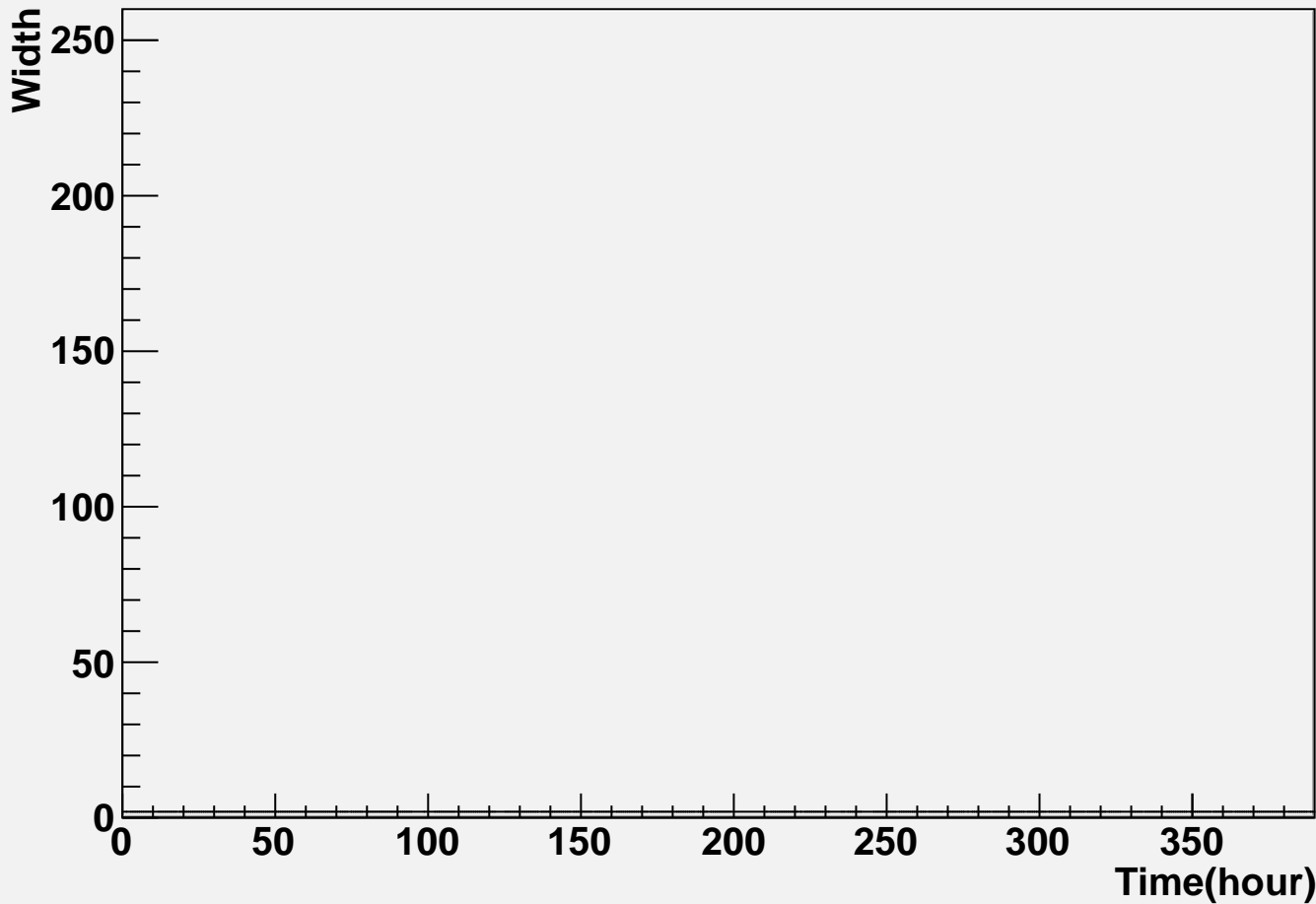
width-S3-SFET2A-Ch2



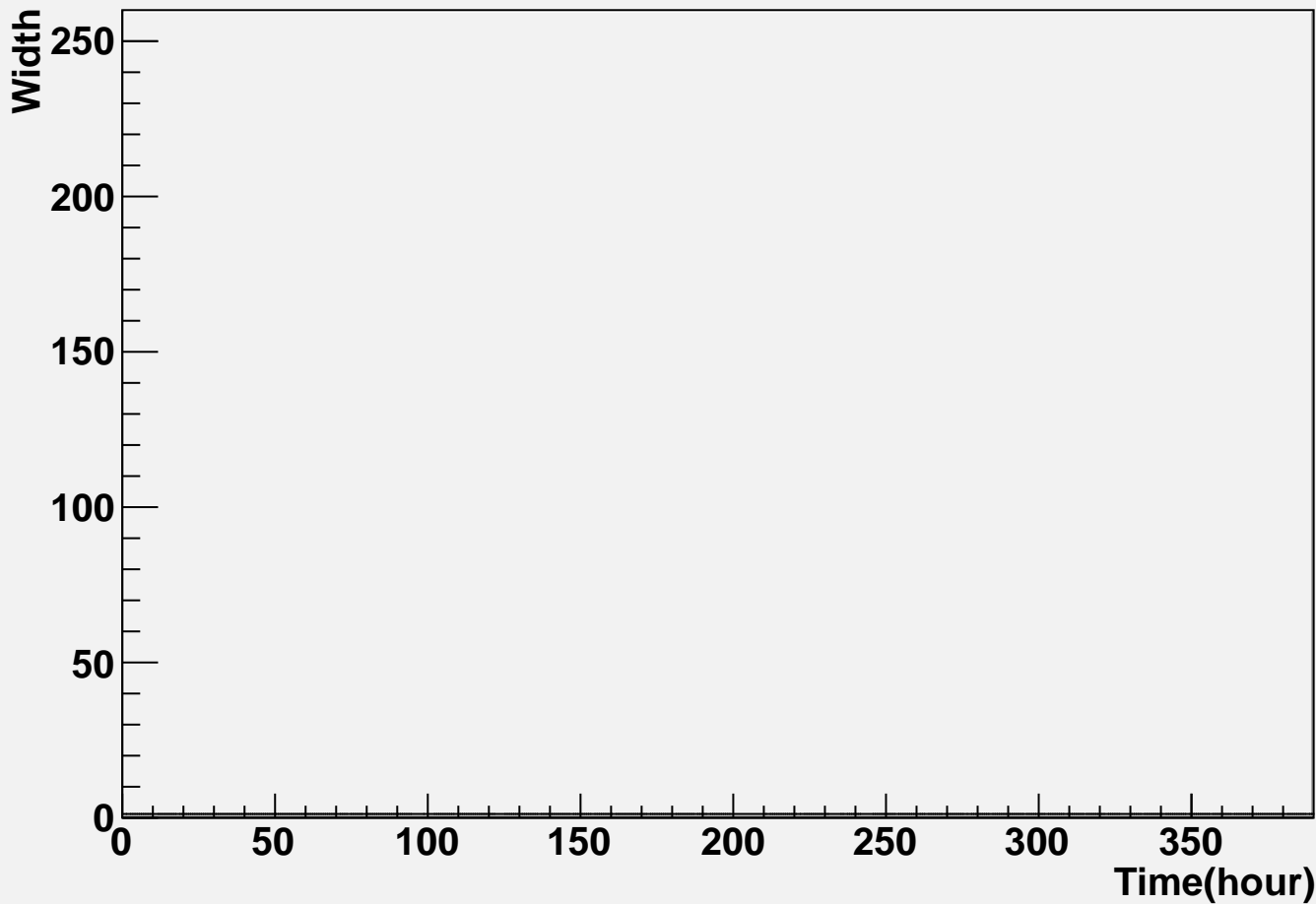
width-S3-SFET2A-Ch3



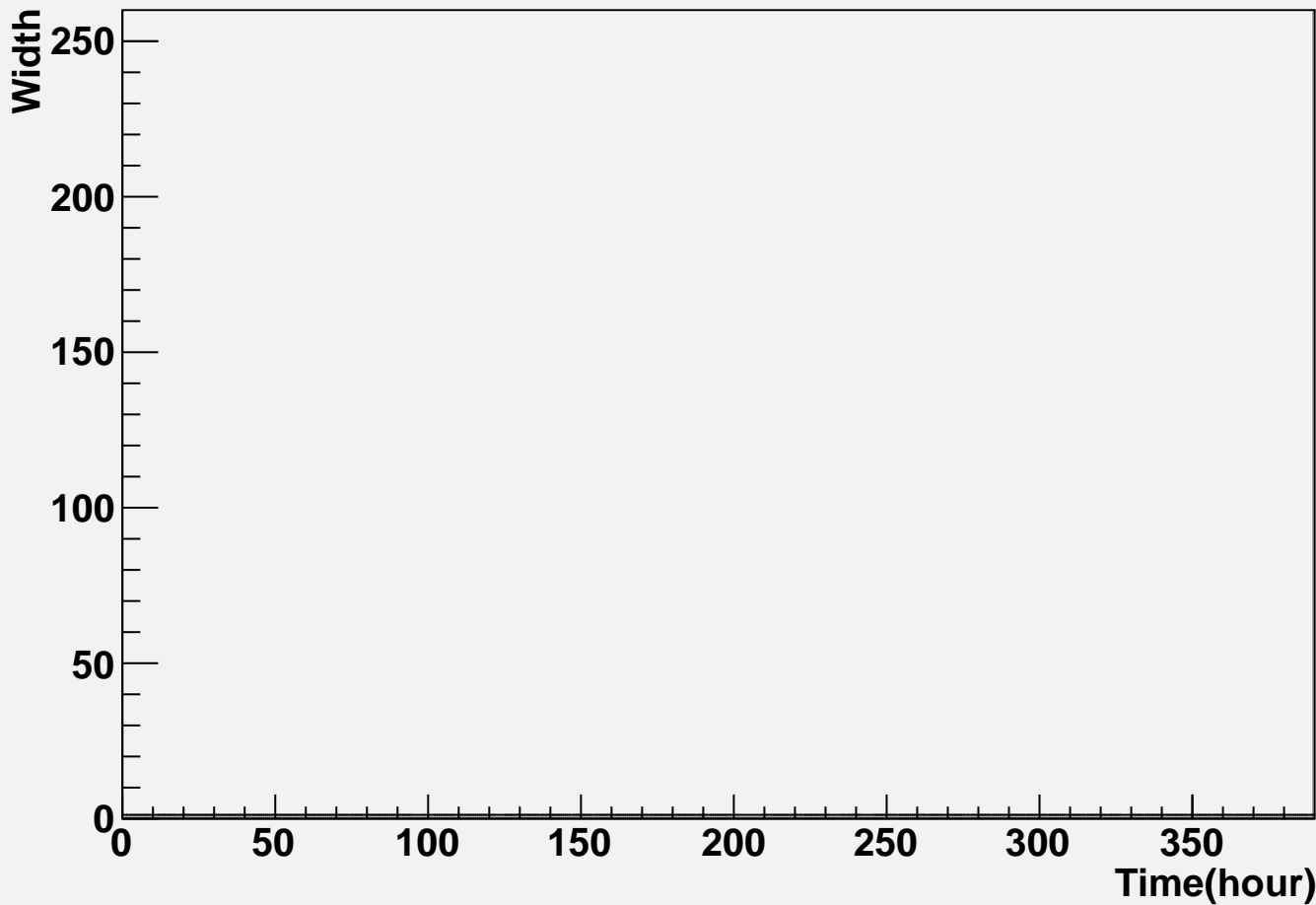
width-S3-SFET2A-Ch4



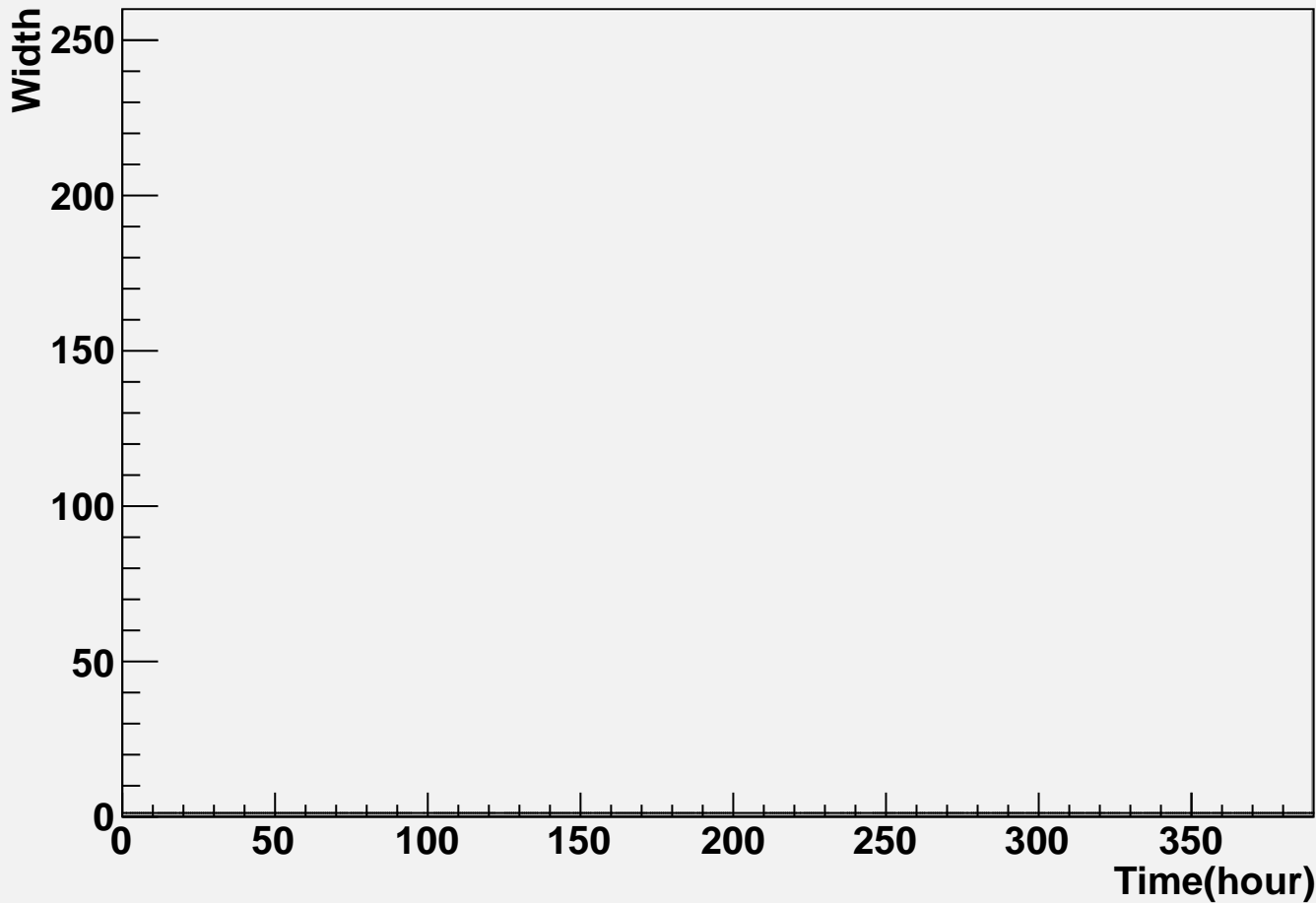
width-S3-SFET2A-Ch5



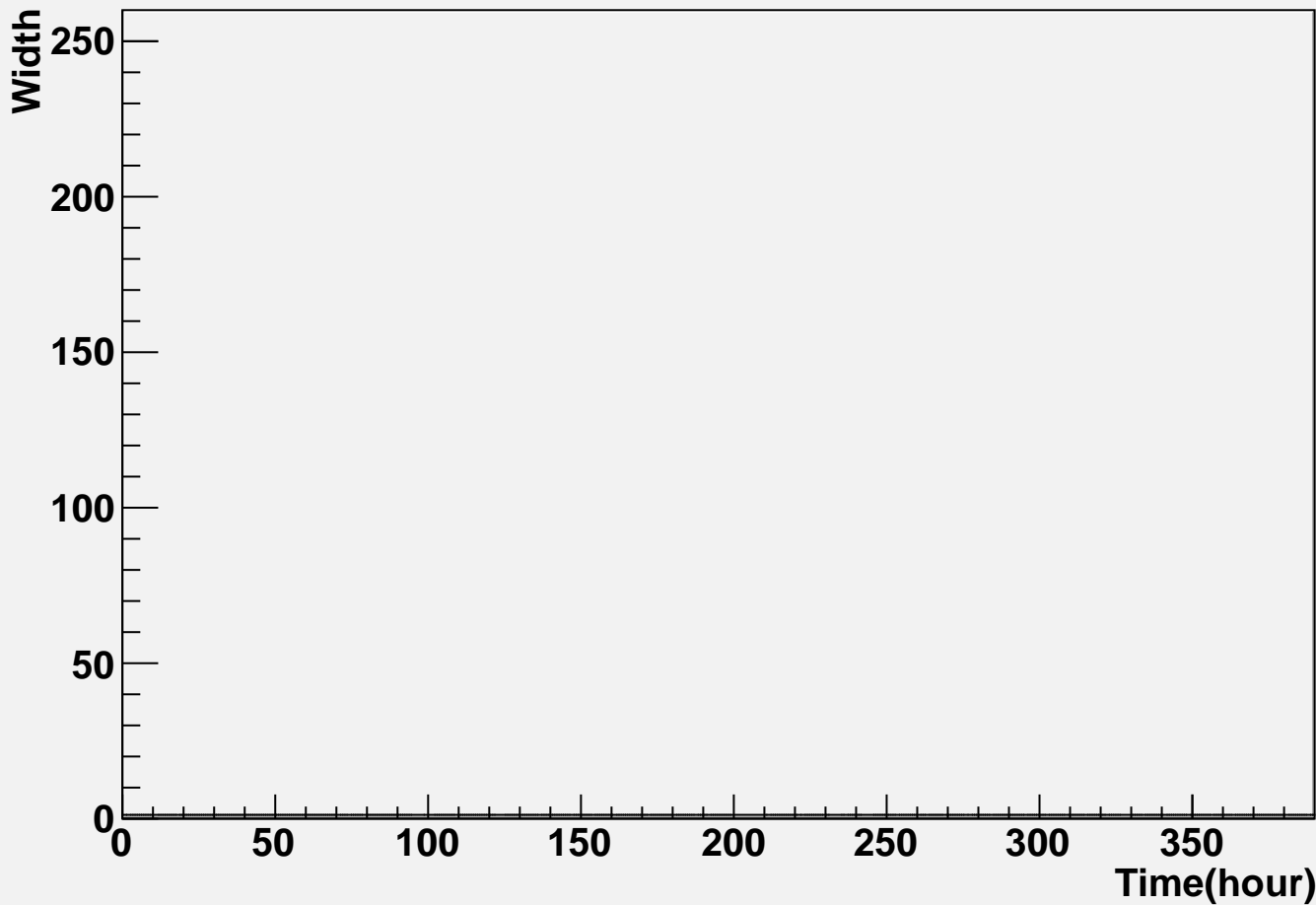
width-S3-SFET2A-Ch6



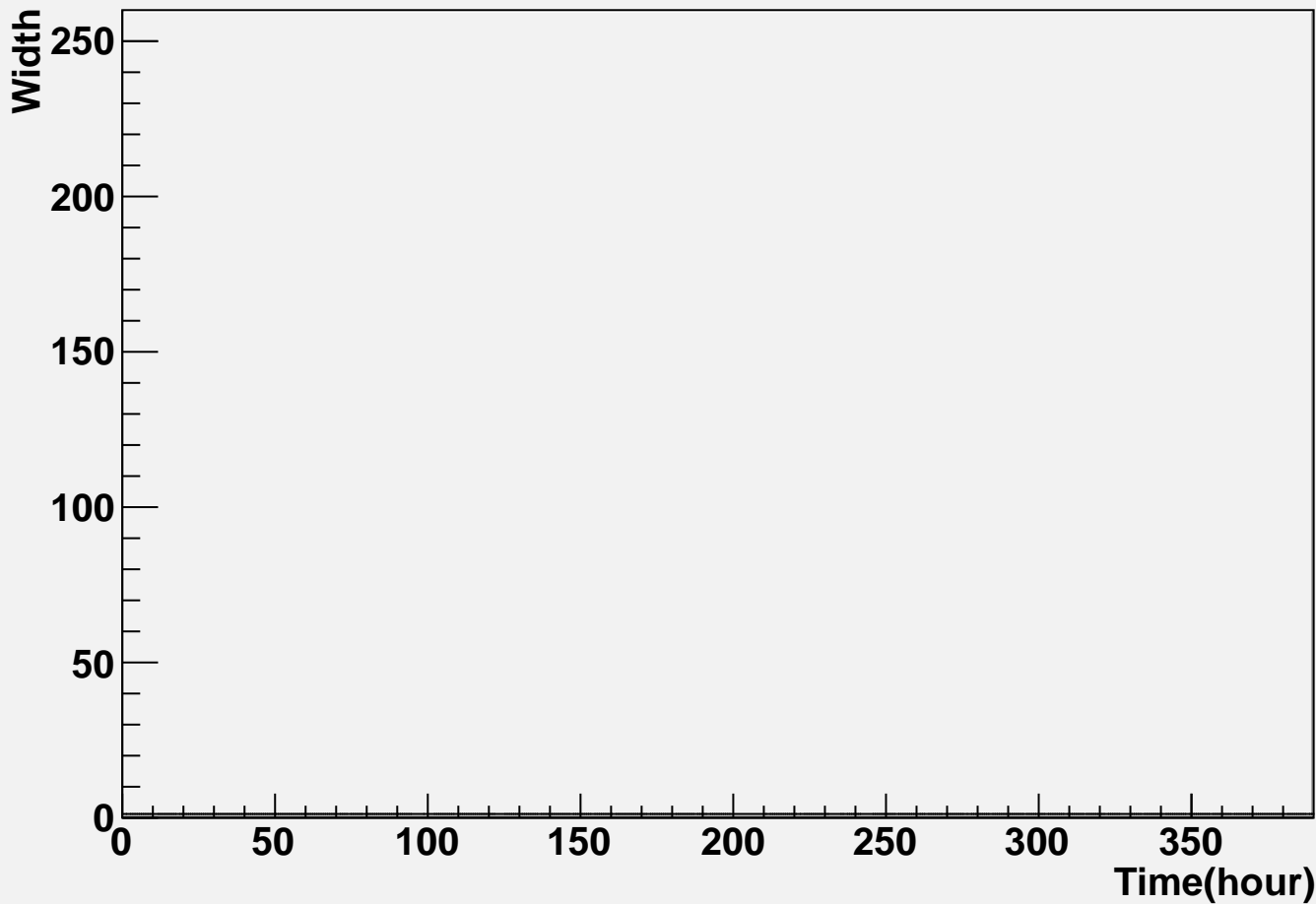
width-S3-SFET2A-Ch7



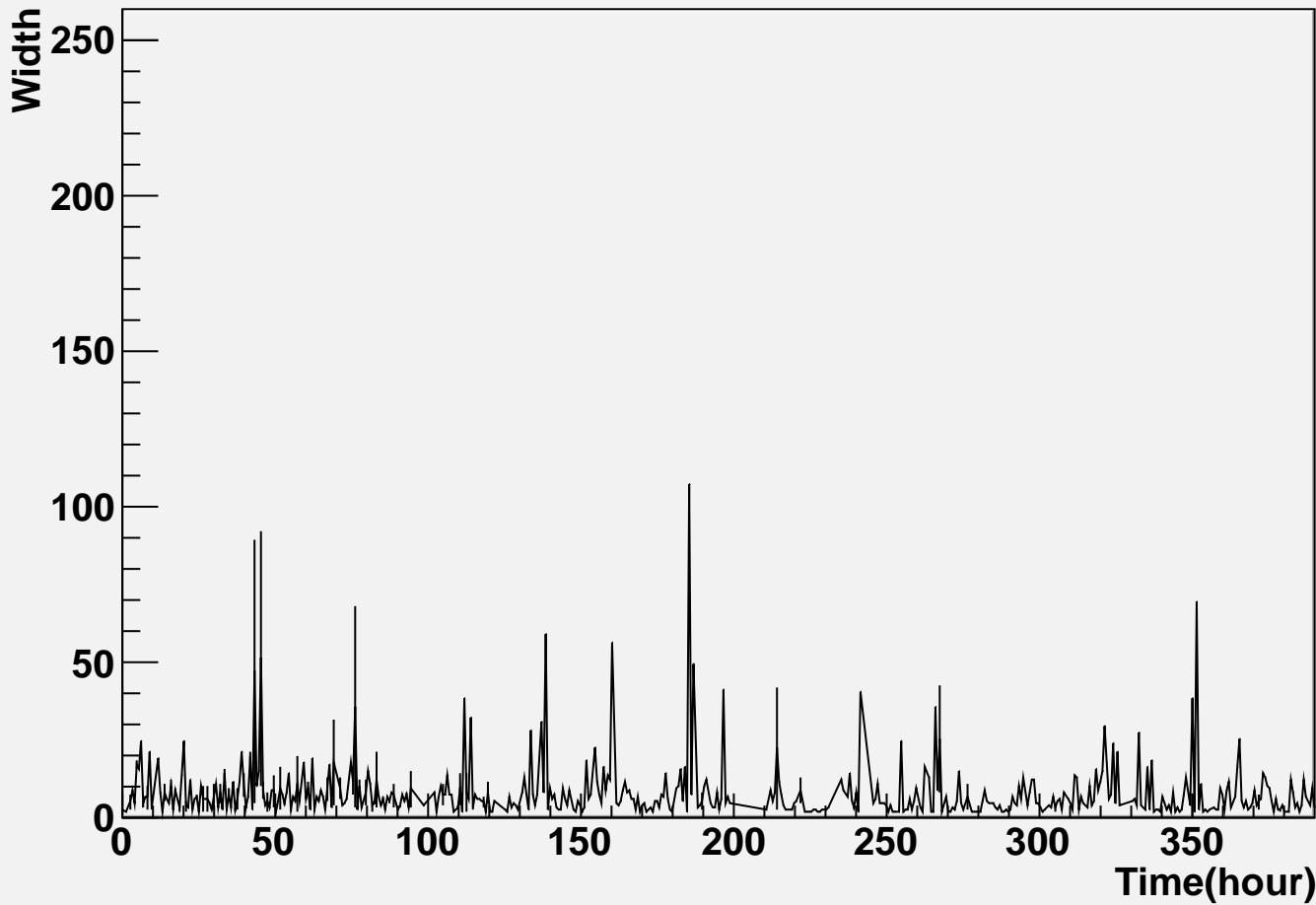
width-S3-SFET2A-Ch8



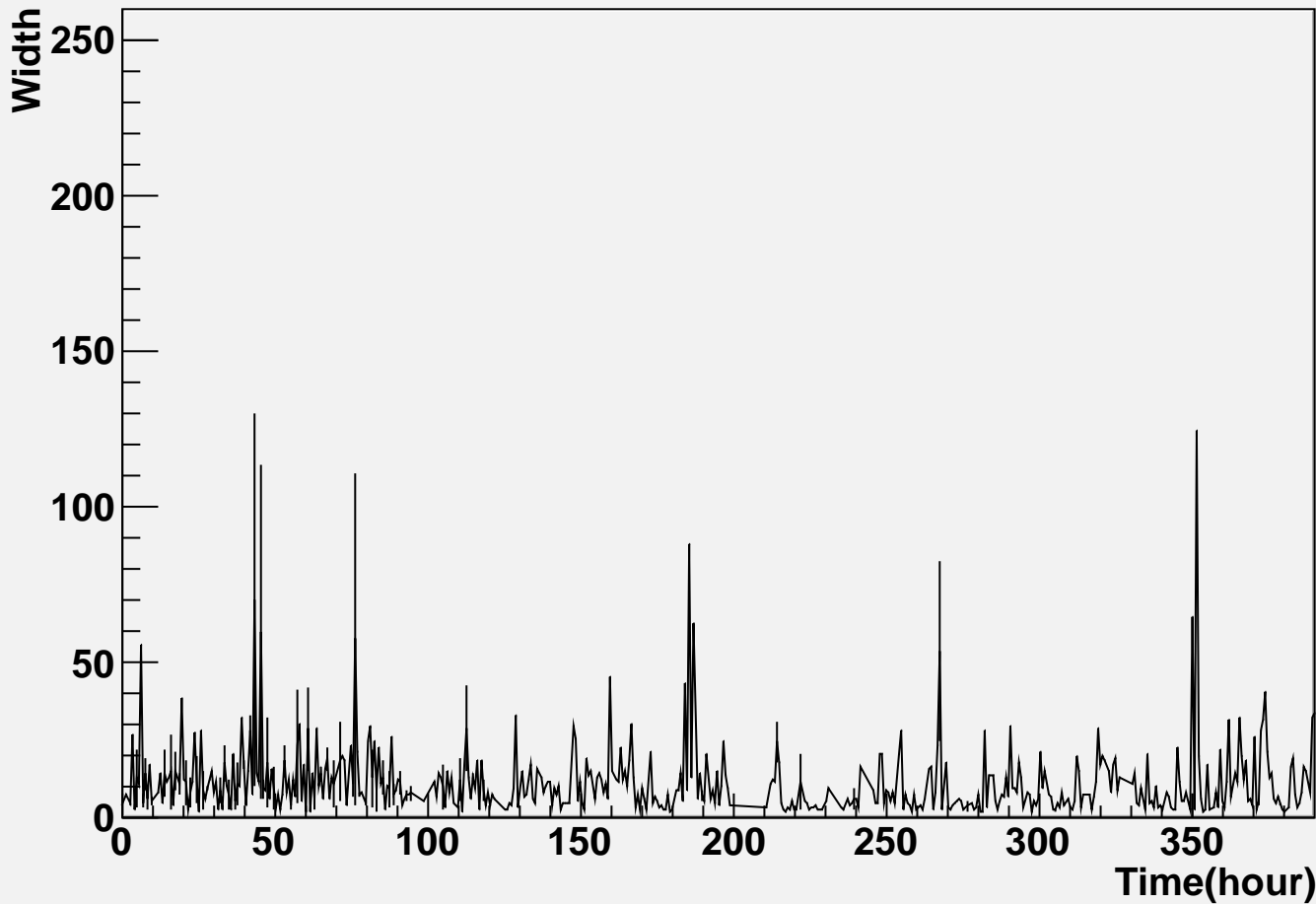
width-S3-SFET2A-Ch9



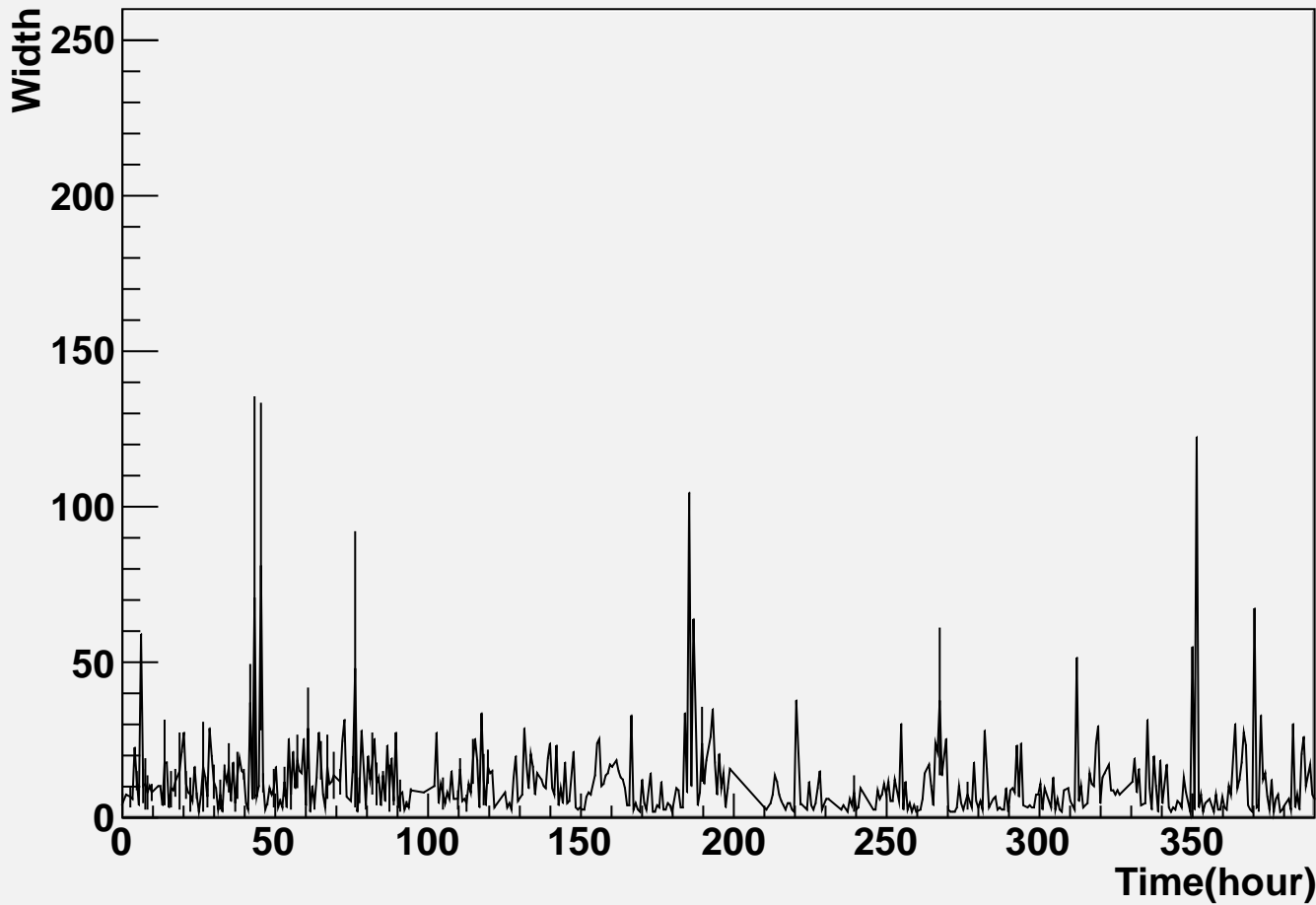
width-S3-SFET2B-Ch0



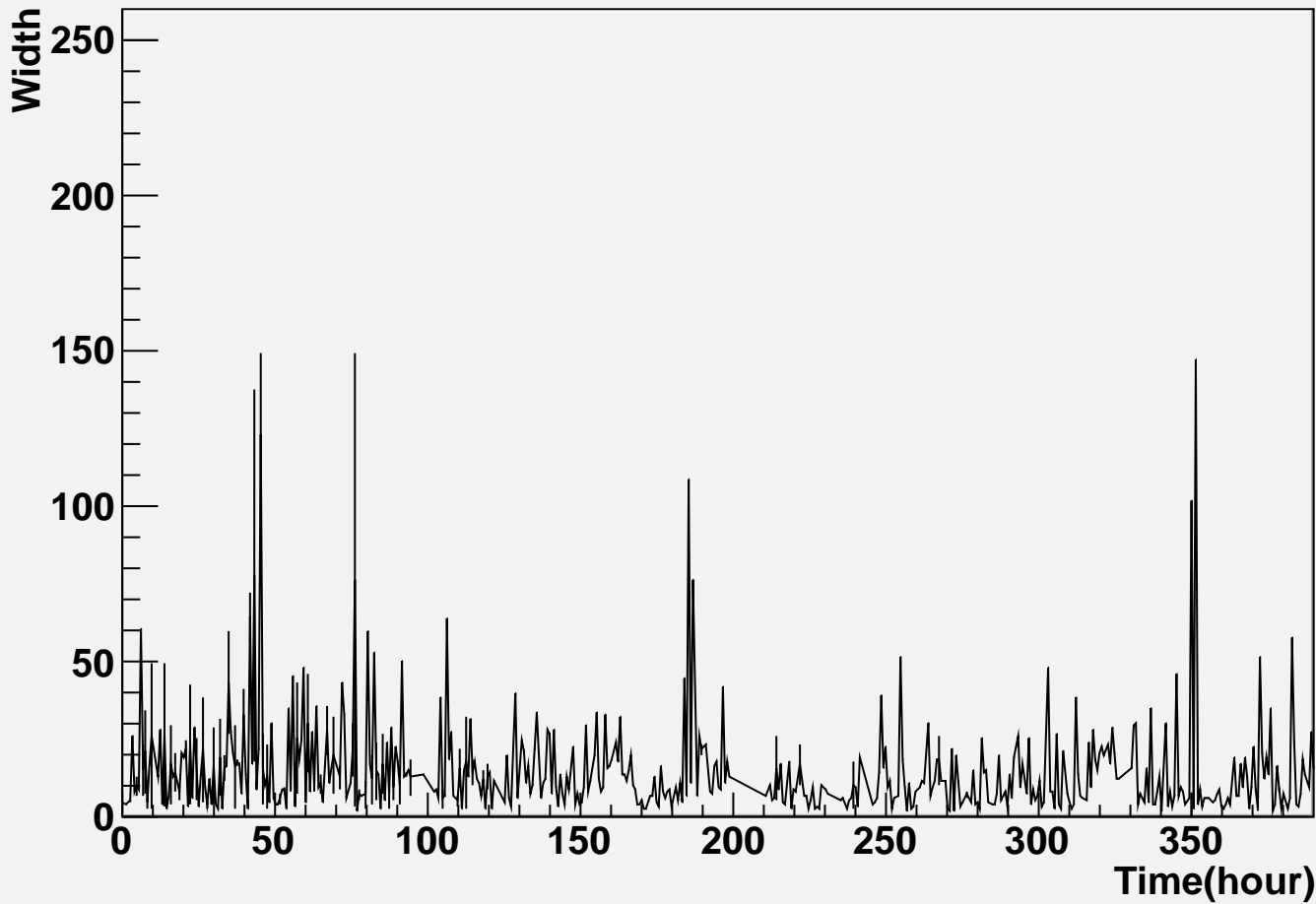
width-S3-SFET2B-Ch1



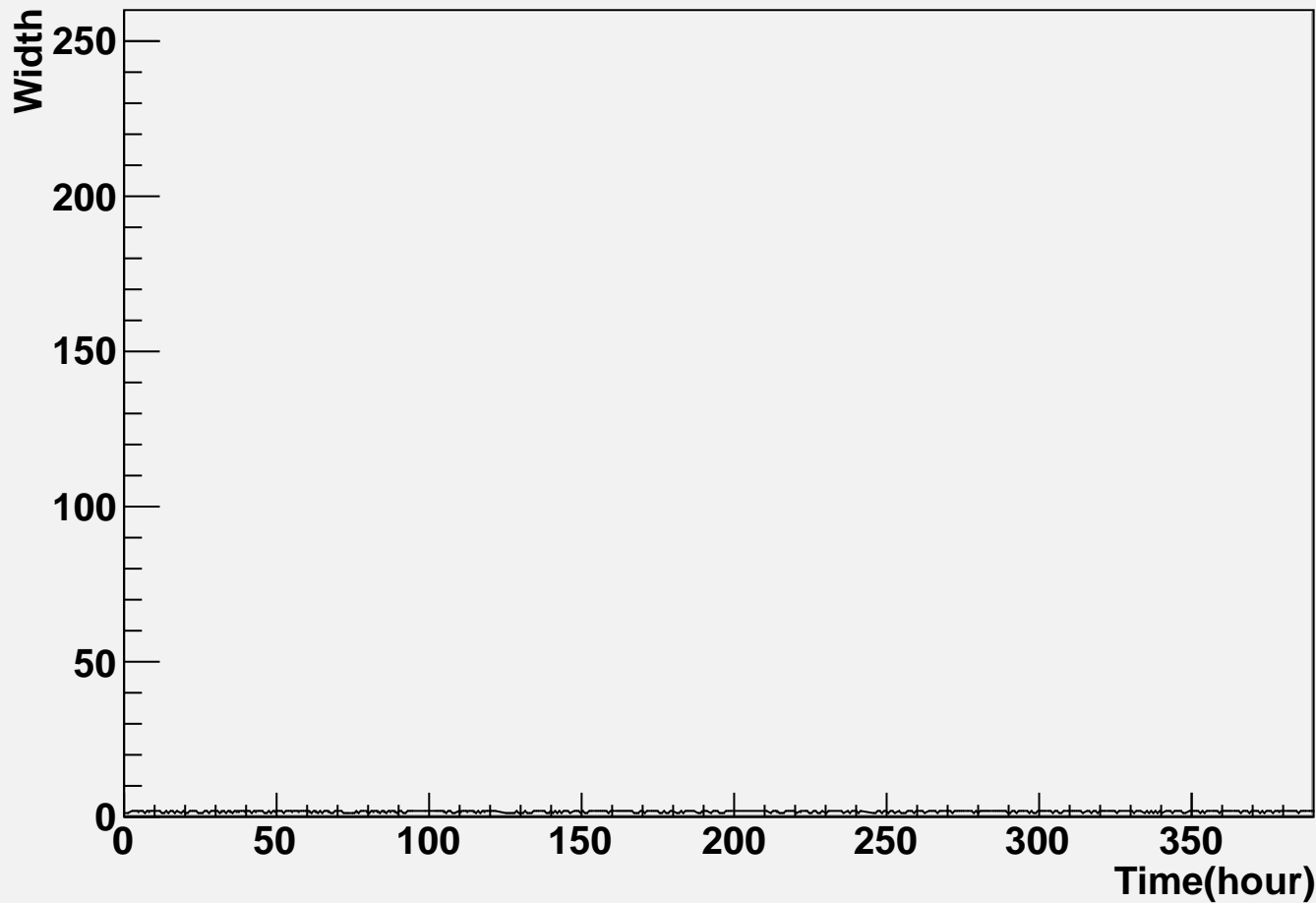
width-S3-SFET2B-Ch2



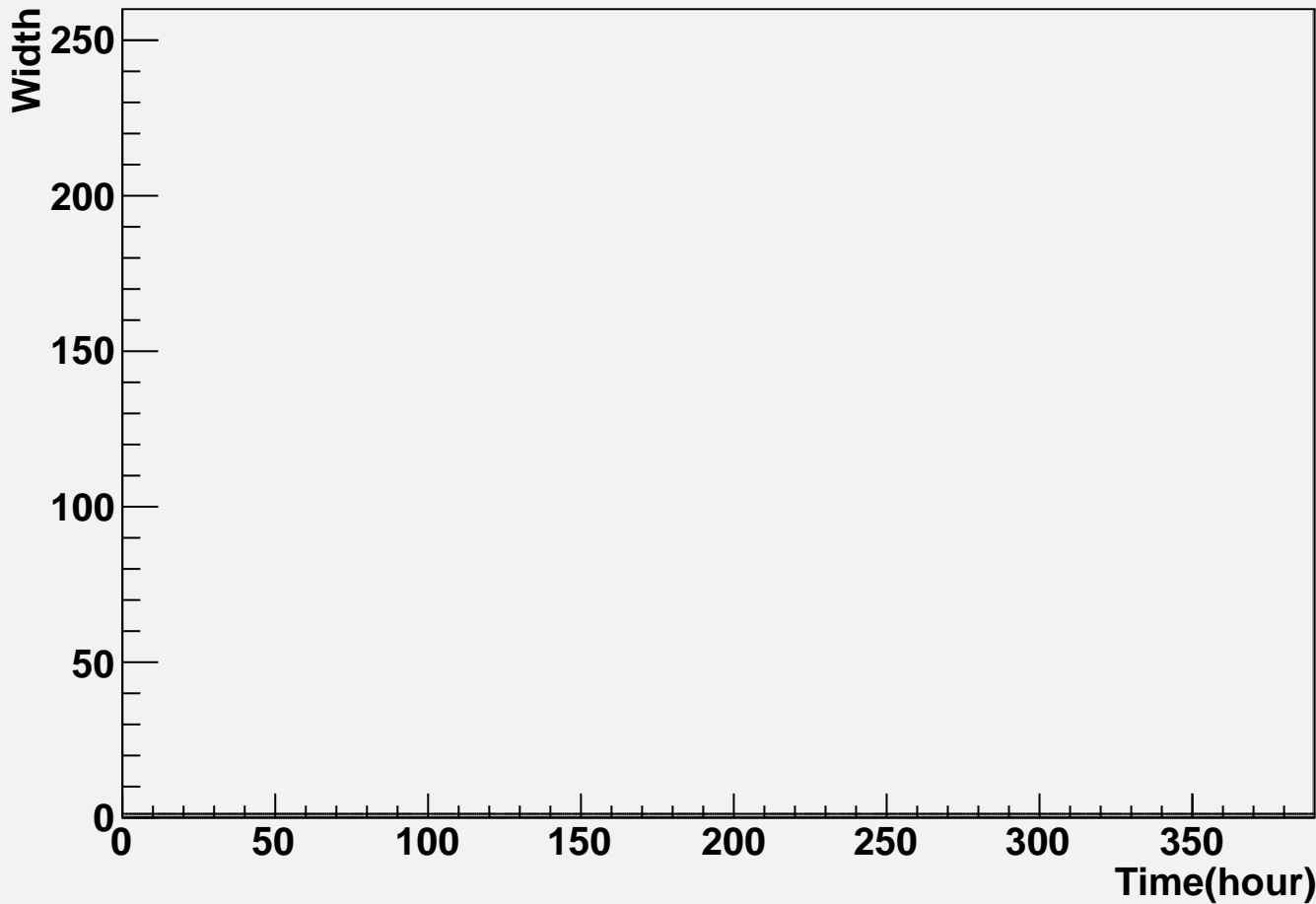
width-S3-SFET2B-Ch3



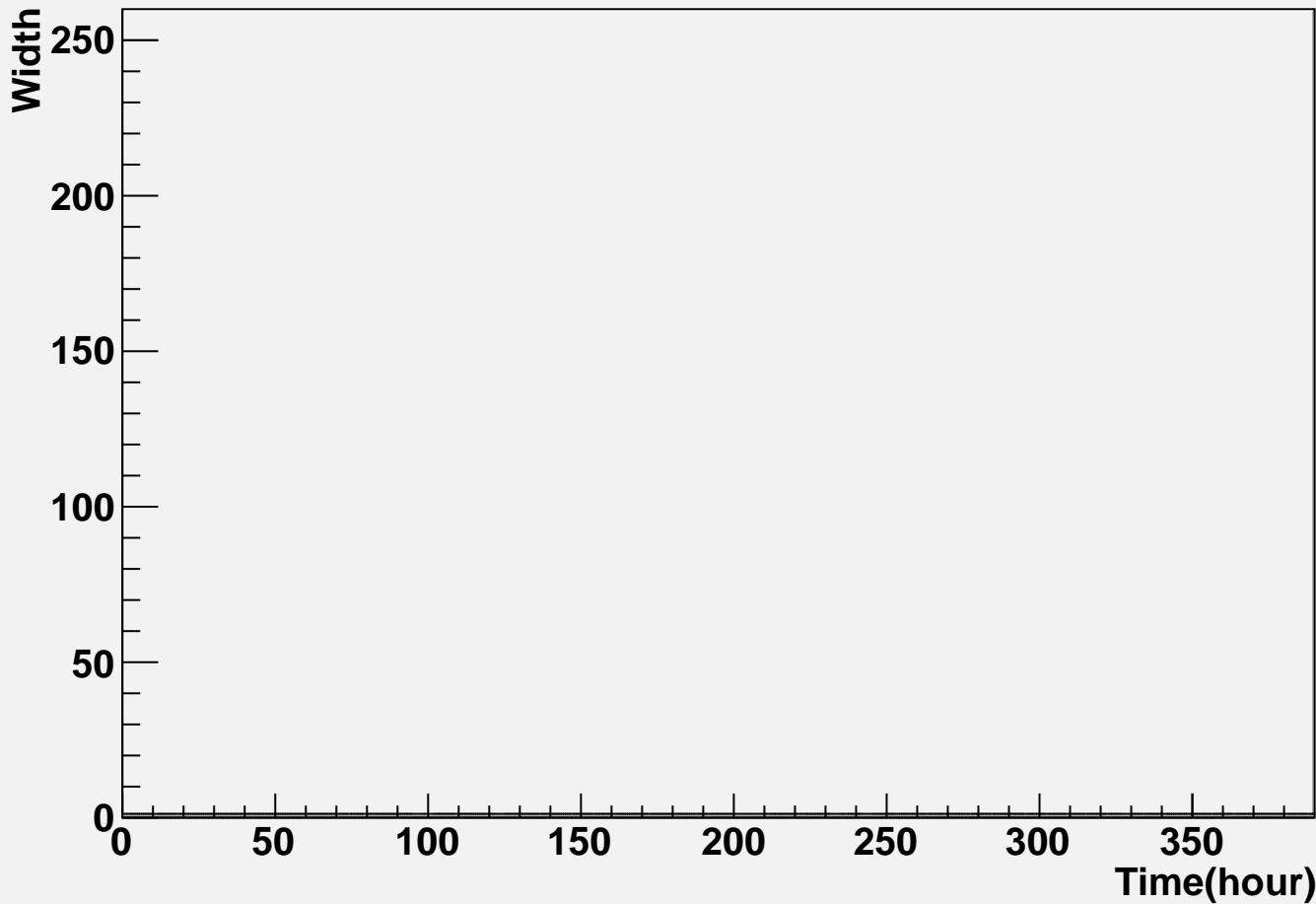
width-S3-SFET2B-Ch4



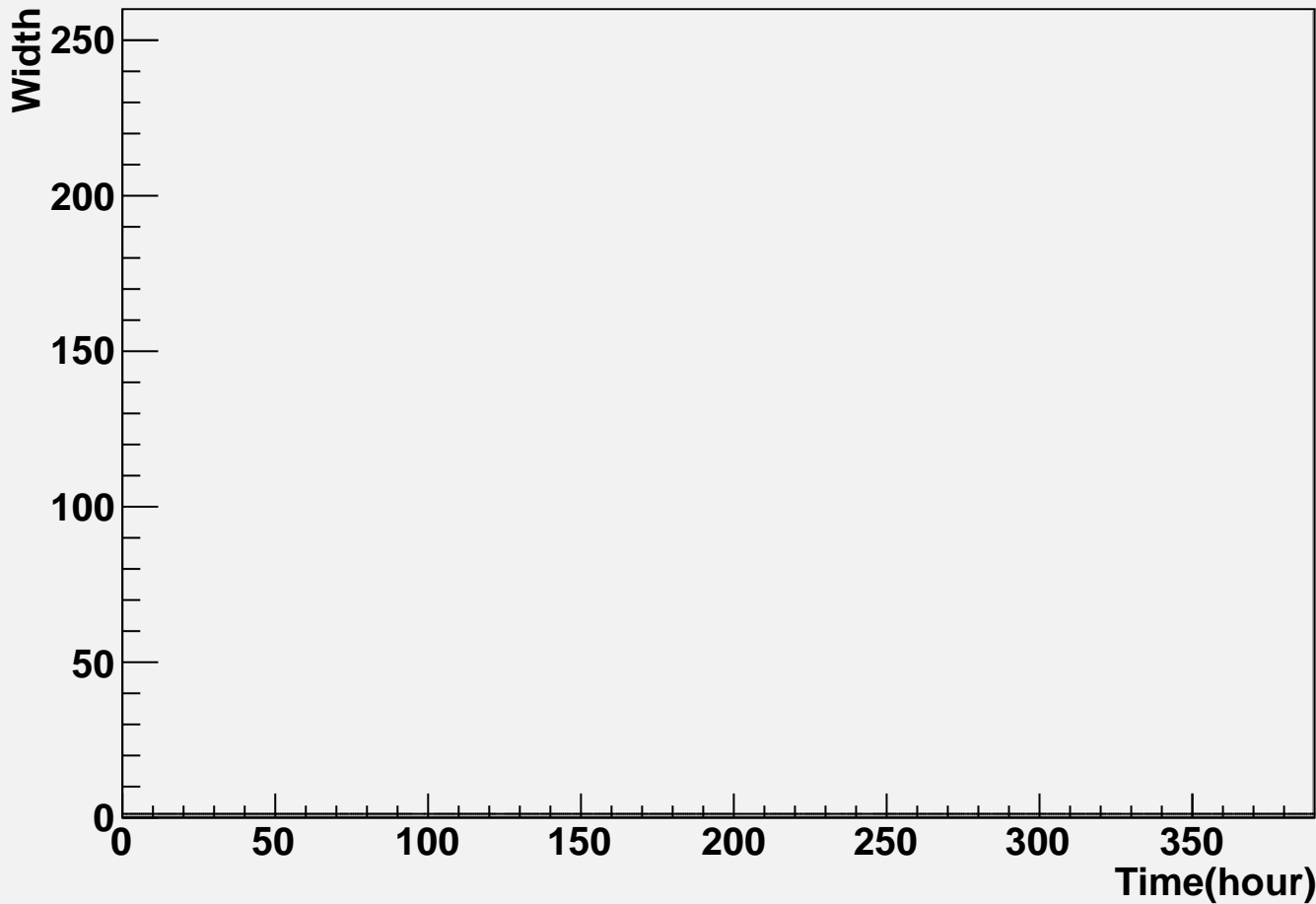
width-S3-SFET2B-Ch5



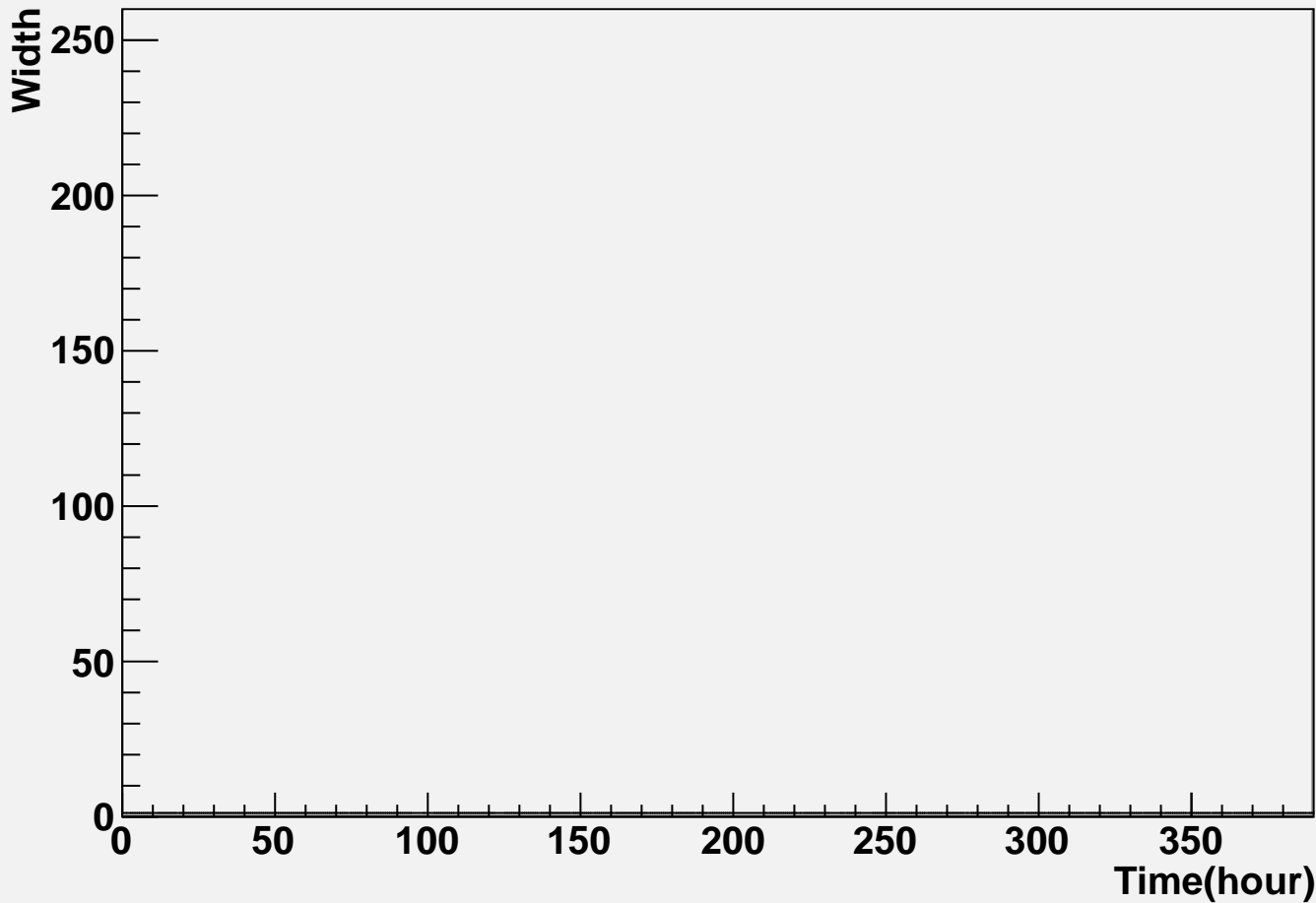
width-S3-SFET2B-Ch6



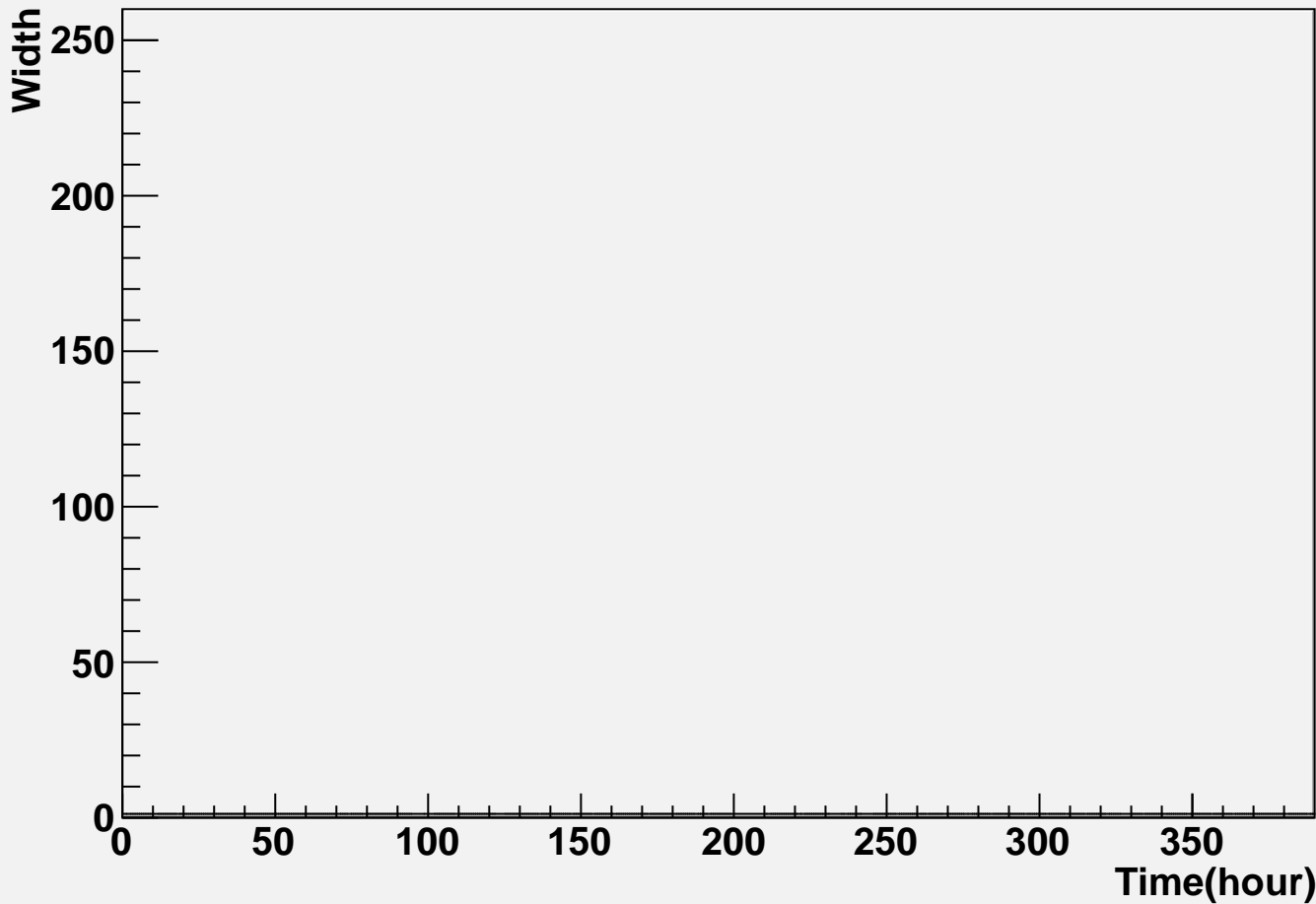
width-S3-SFET2B-Ch7



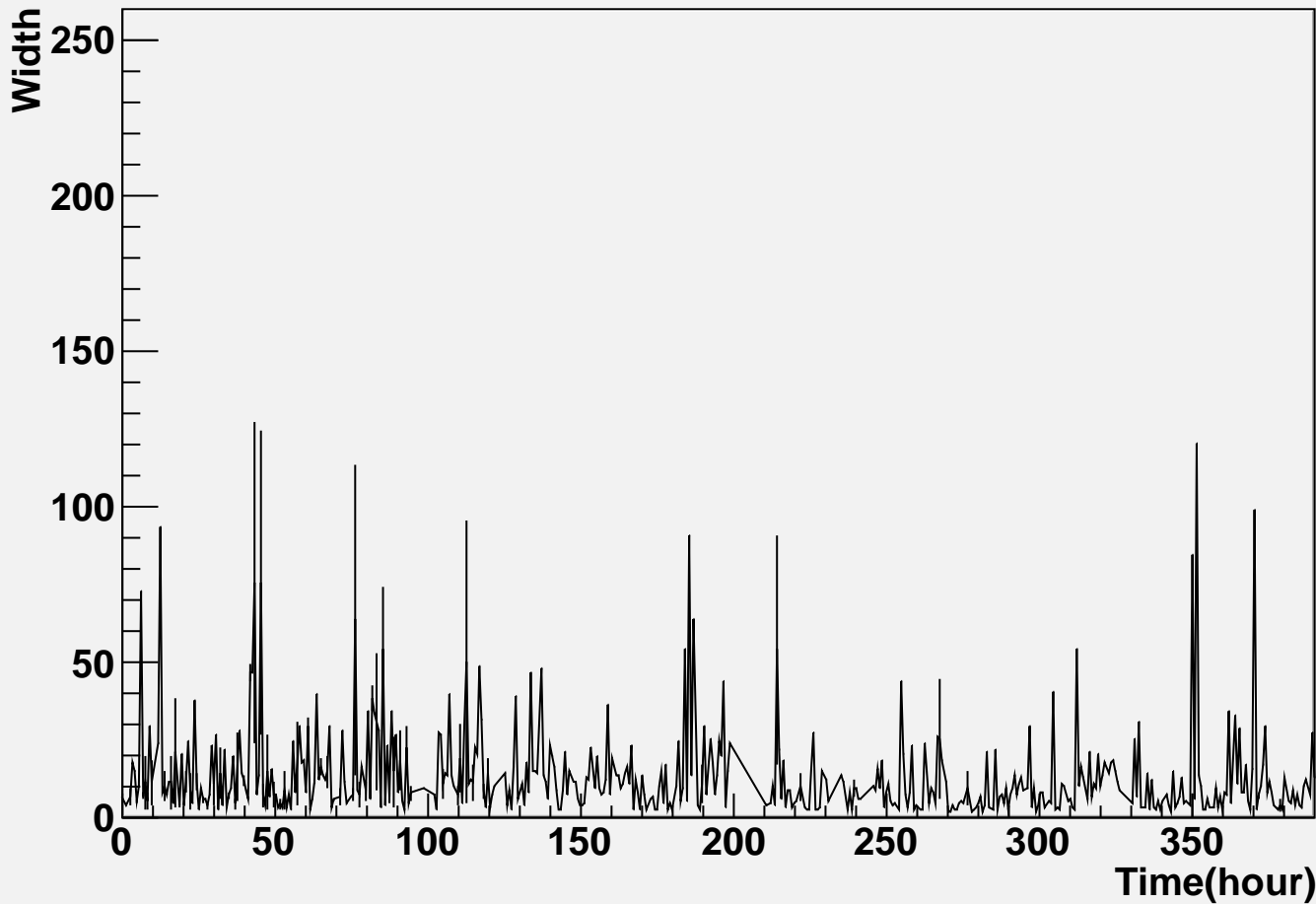
width-S3-SFET2B-Ch8



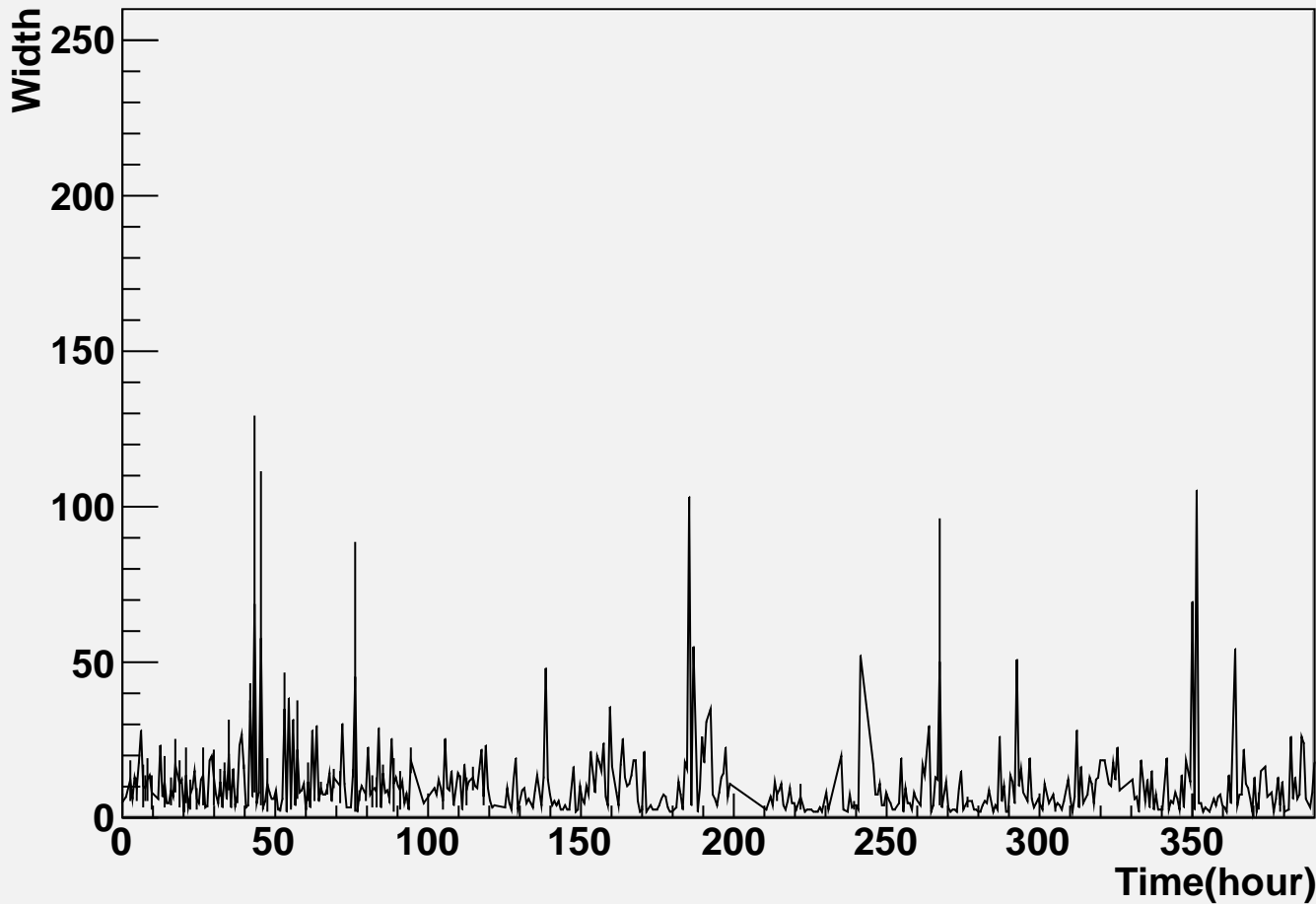
width-S3-SFET2B-Ch9



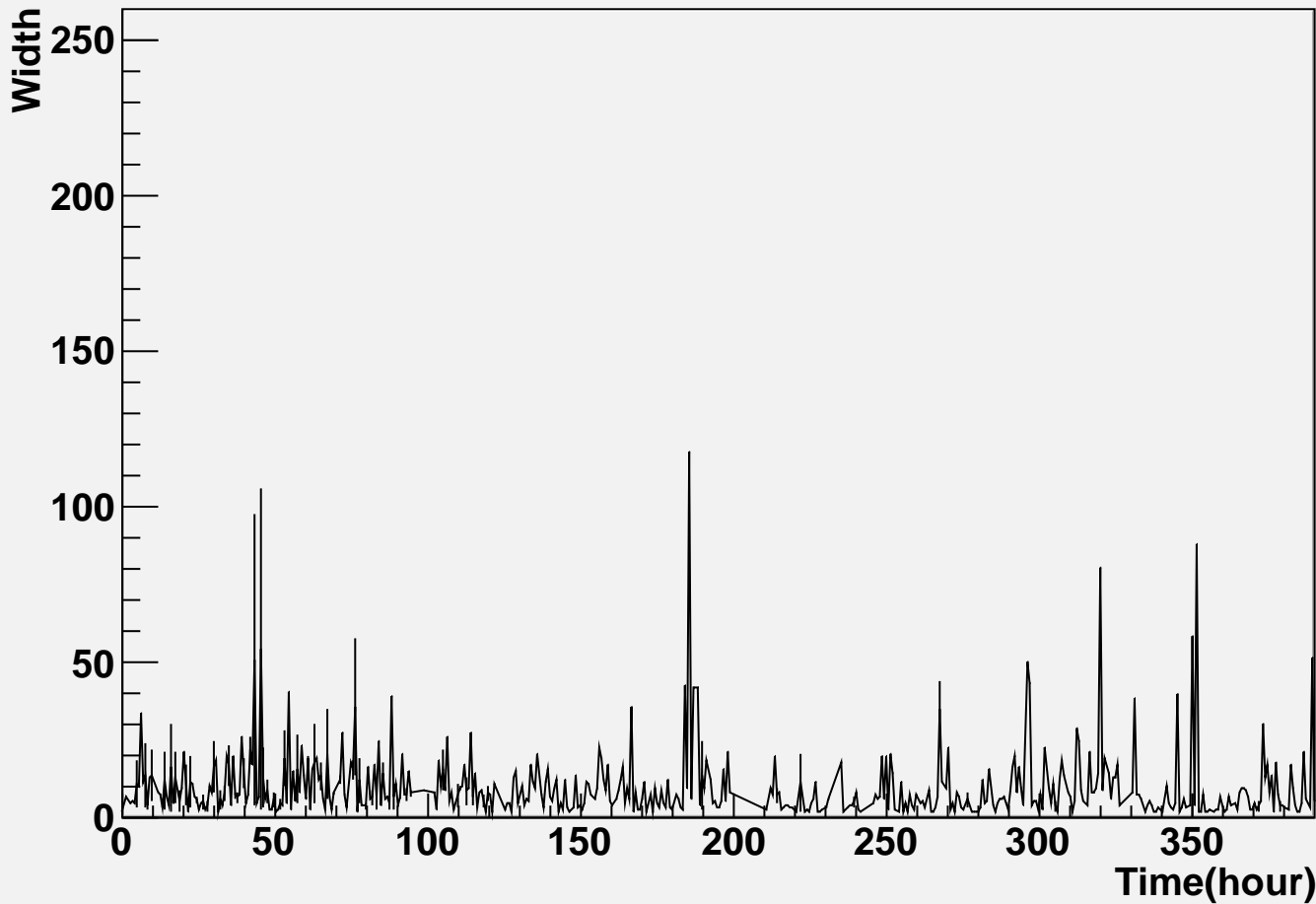
width-S3-SFET2C-Ch0



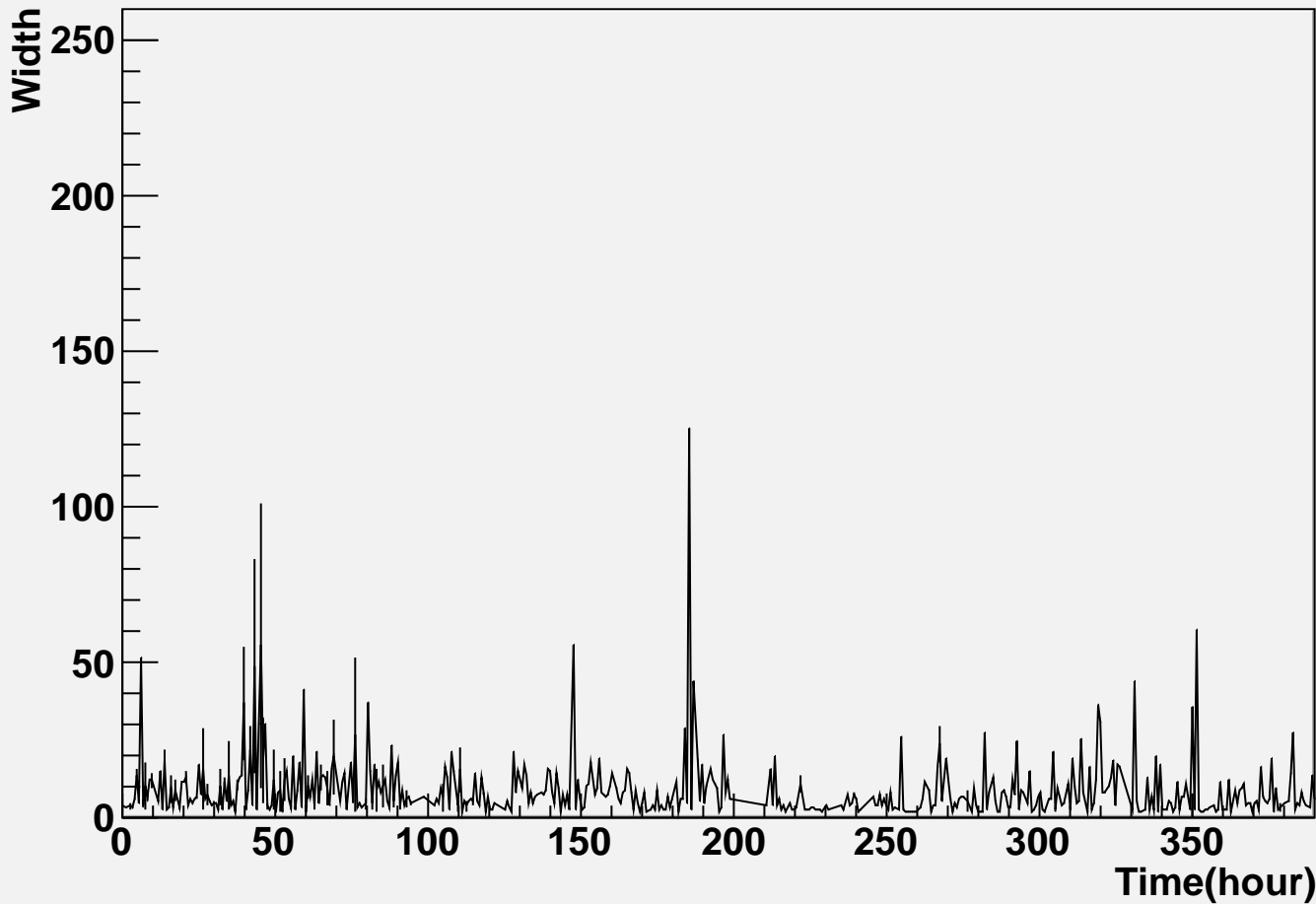
width-S3-SFET2C-Ch1



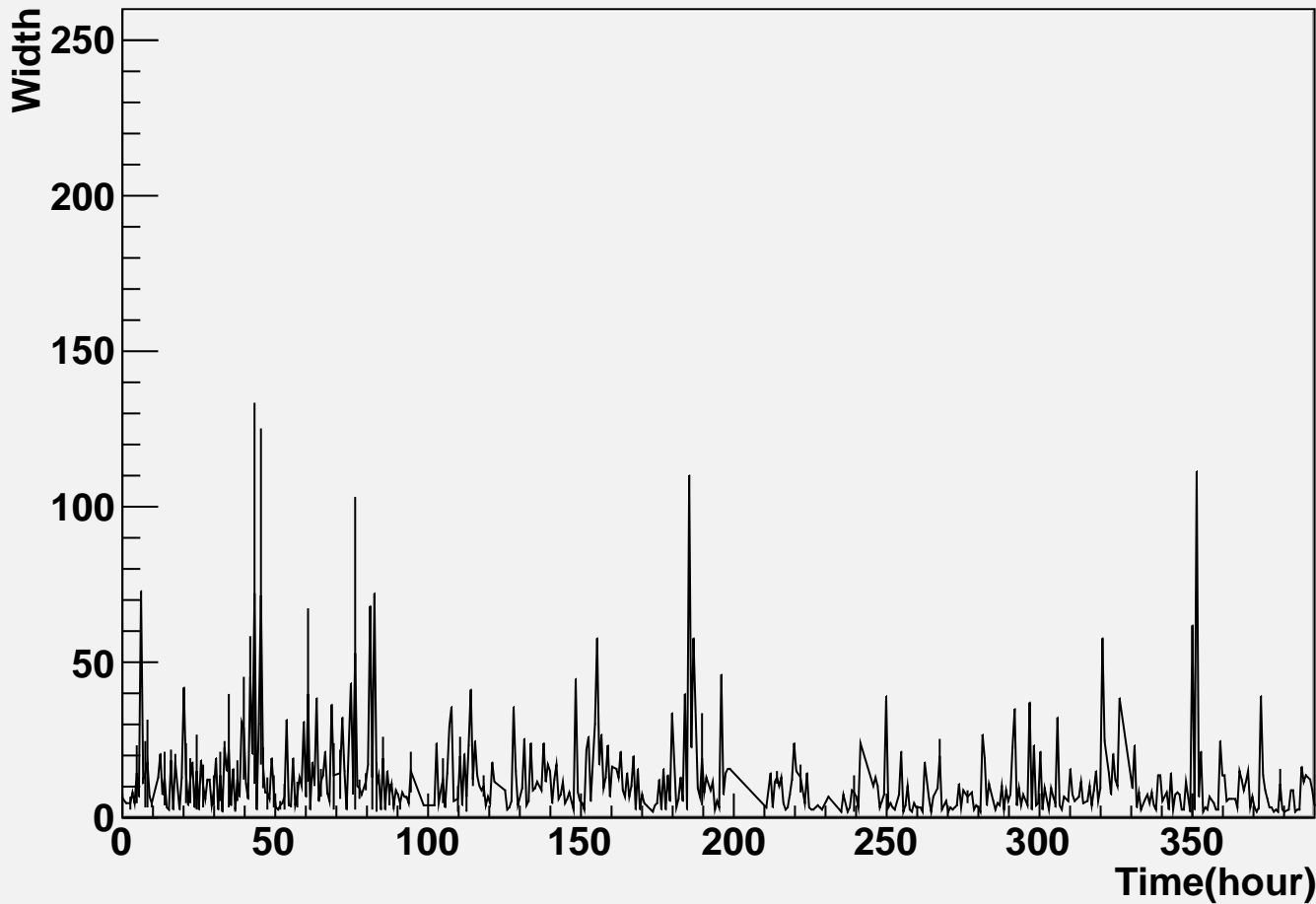
width-S3-SFET2C-Ch2



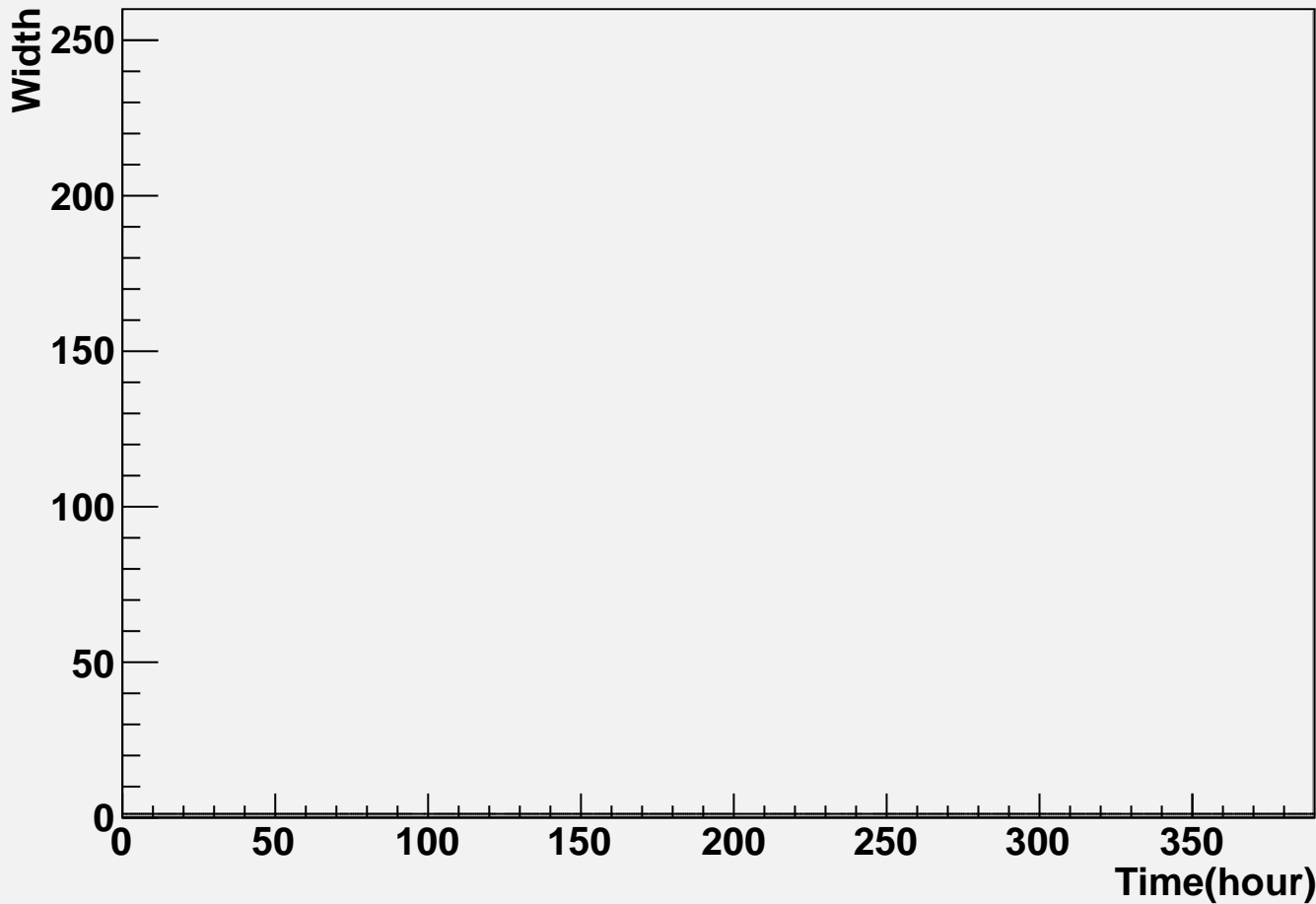
width-S3-SFET2C-Ch3



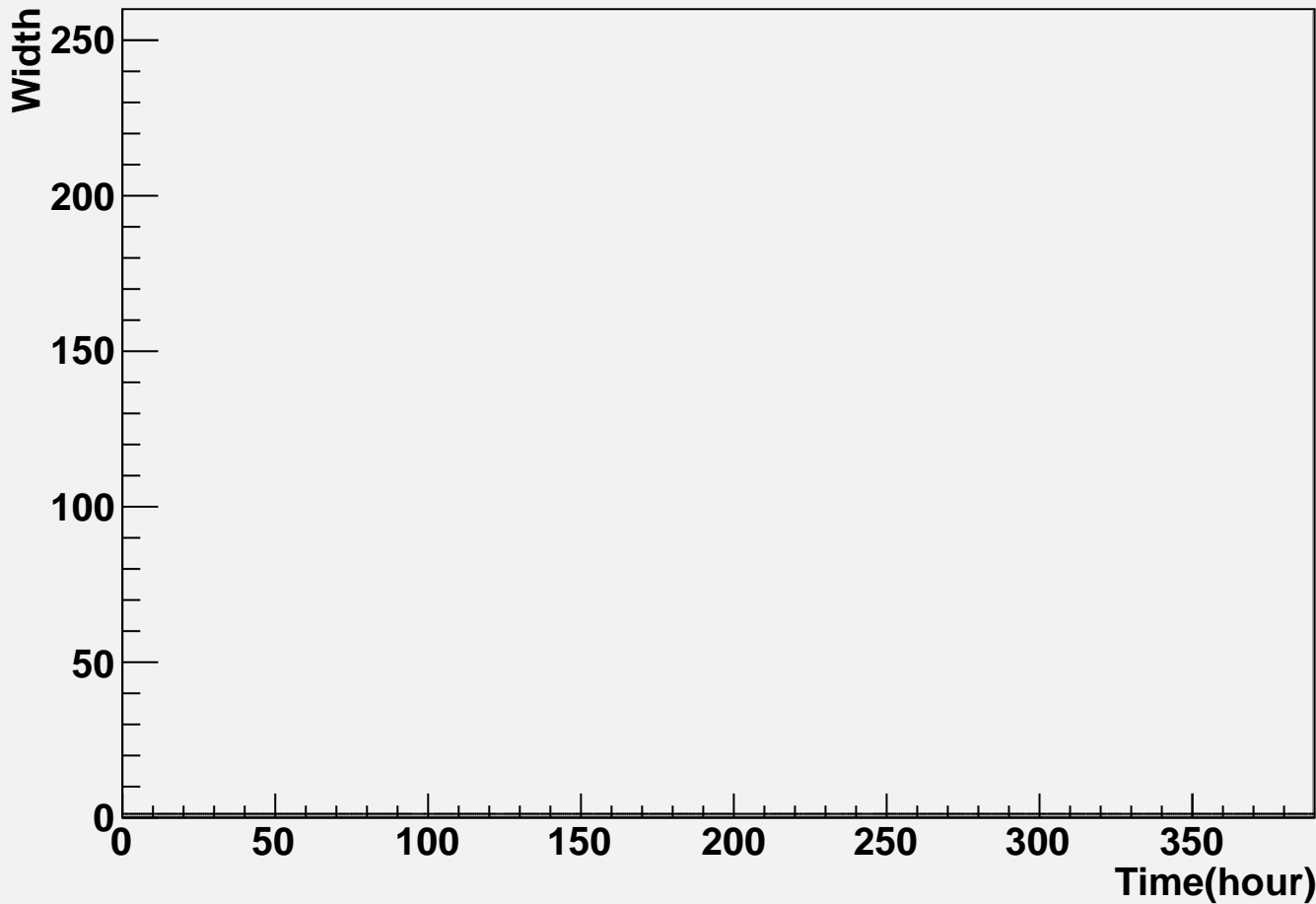
width-S3-SFET2C-Ch4



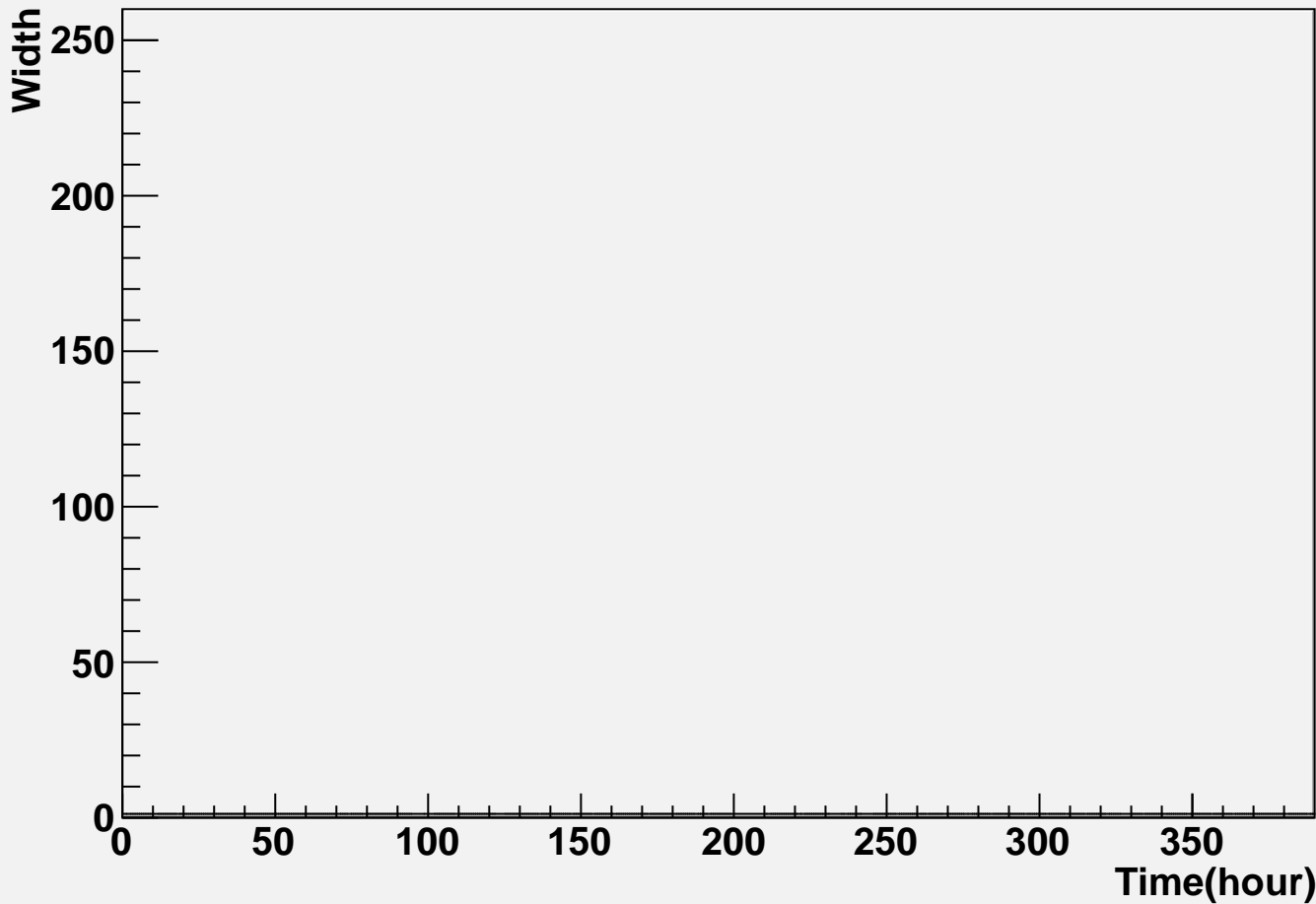
width-S3-SFET2C-Ch5



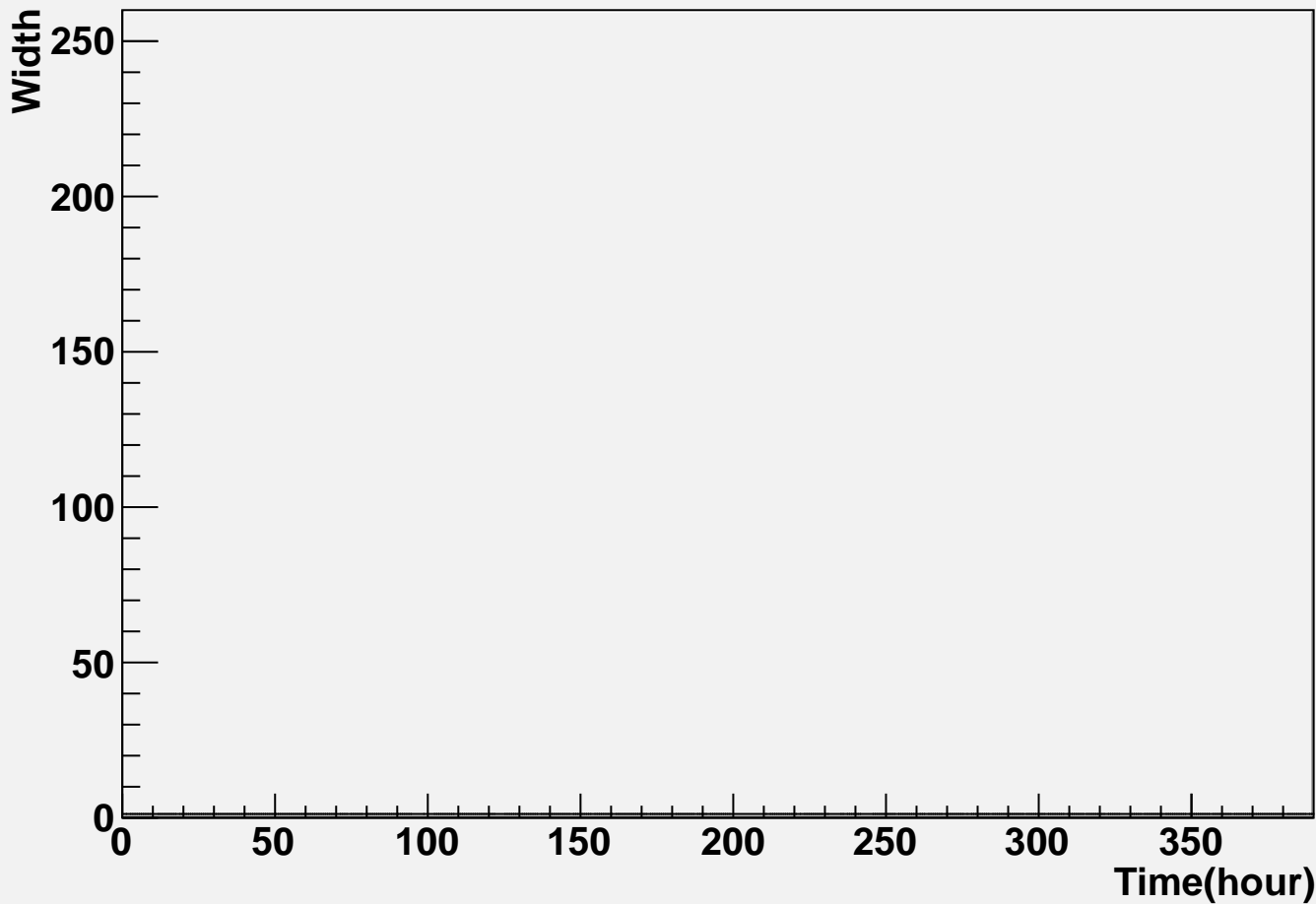
width-S3-SFET2C-Ch6



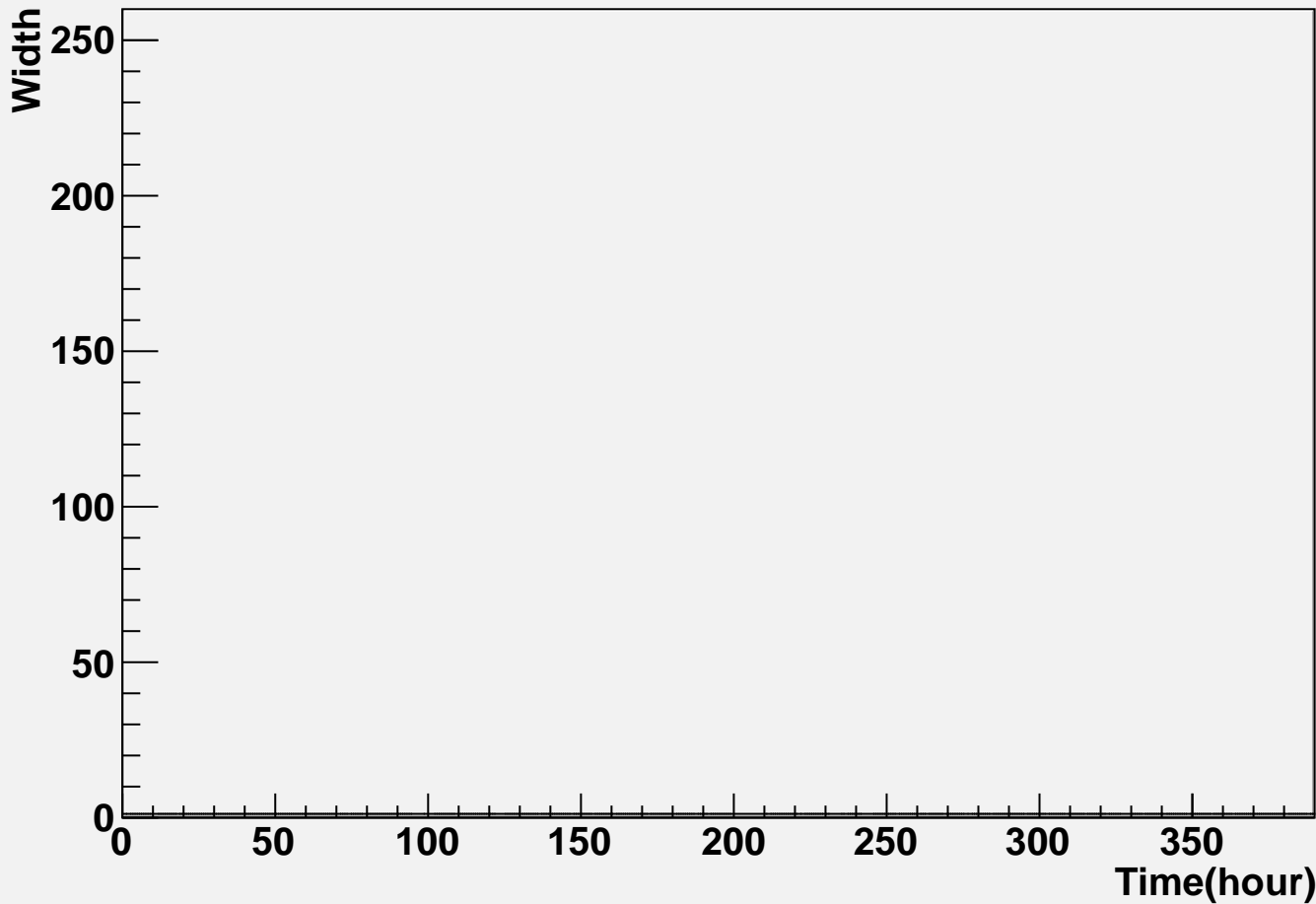
width-S3-SFET2C-Ch7



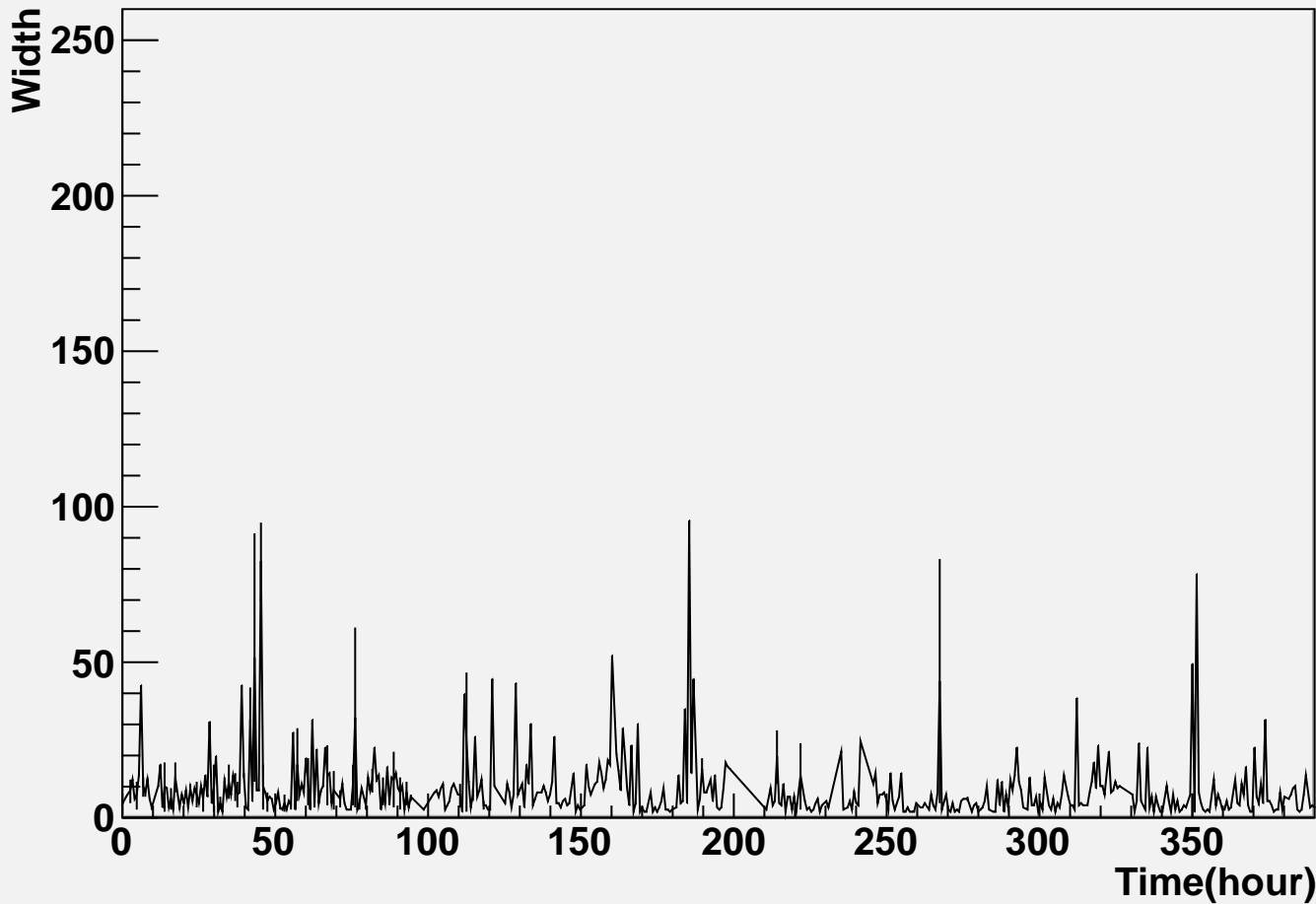
width-S3-SFET2C-Ch8



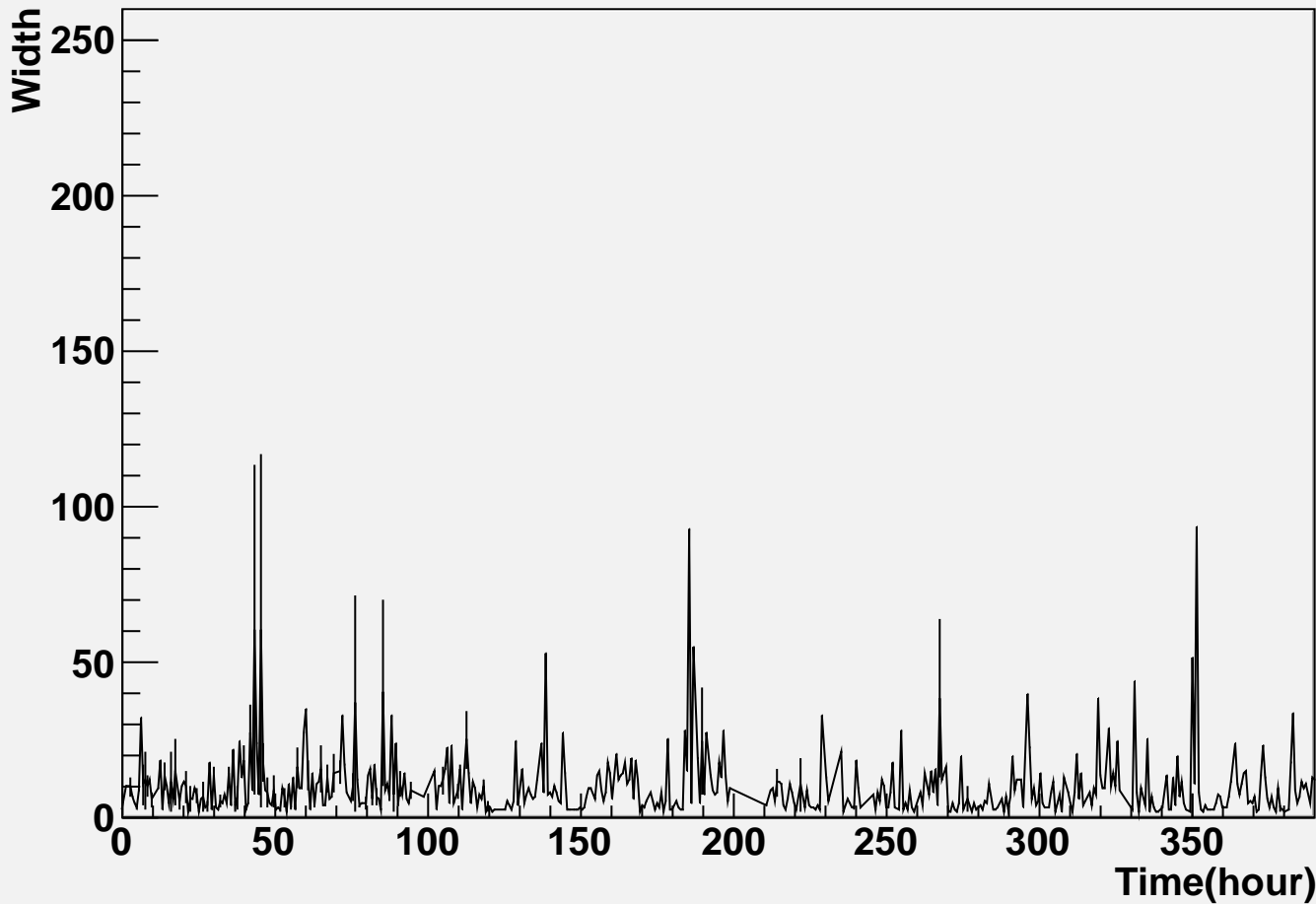
width-S3-SFET2C-Ch9



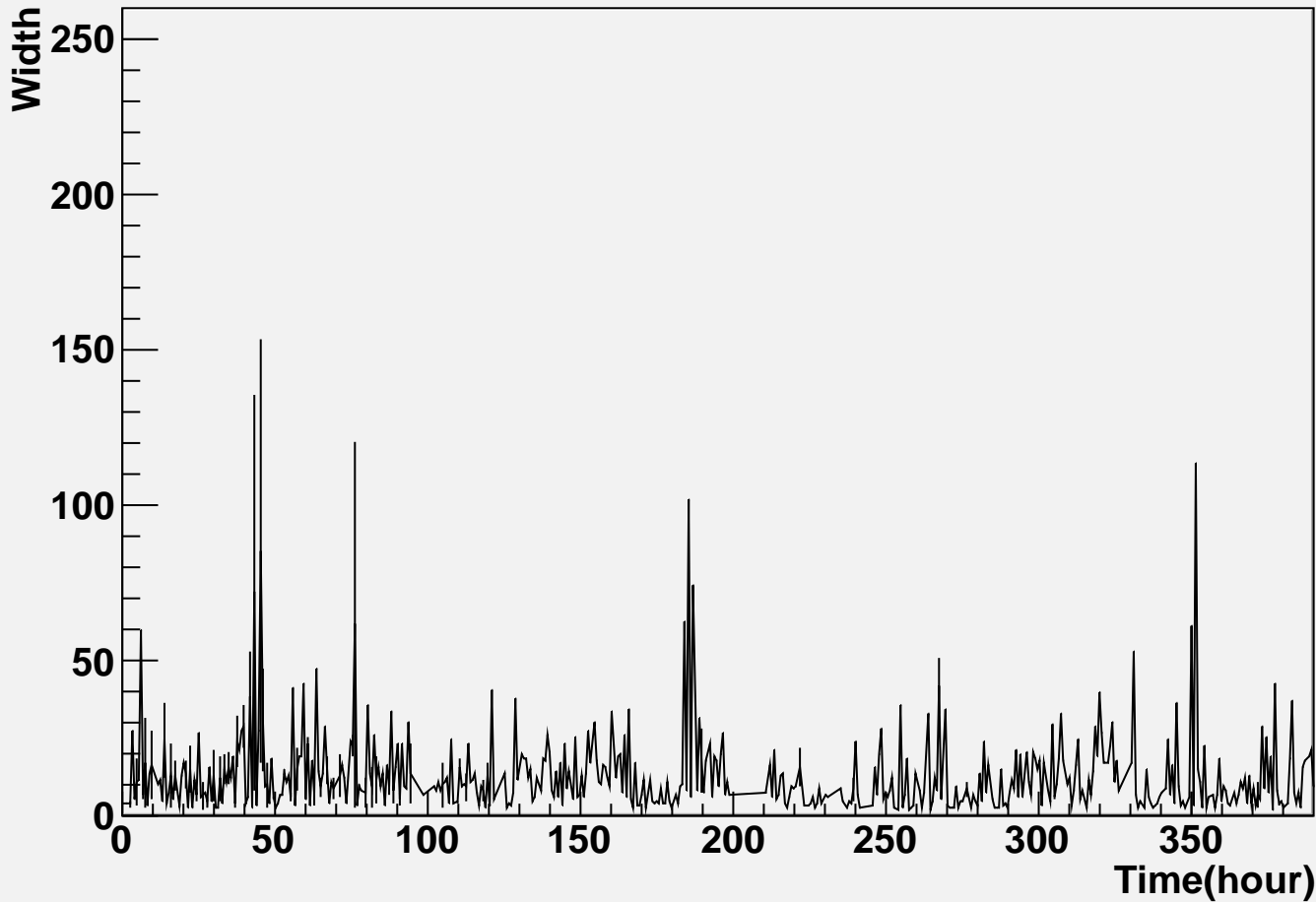
width-S3-SFET2D-Ch0



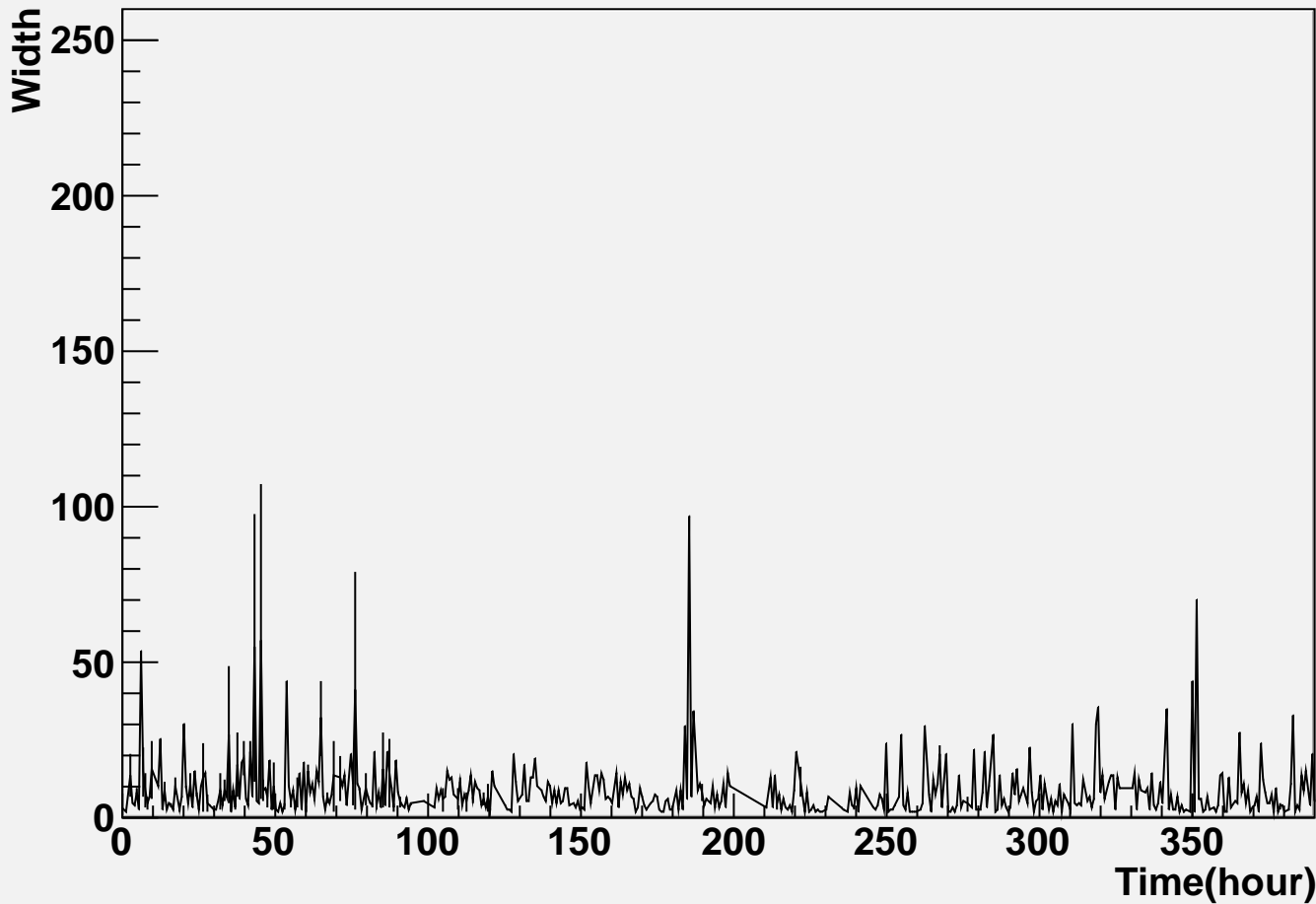
width-S3-SFET2D-Ch1



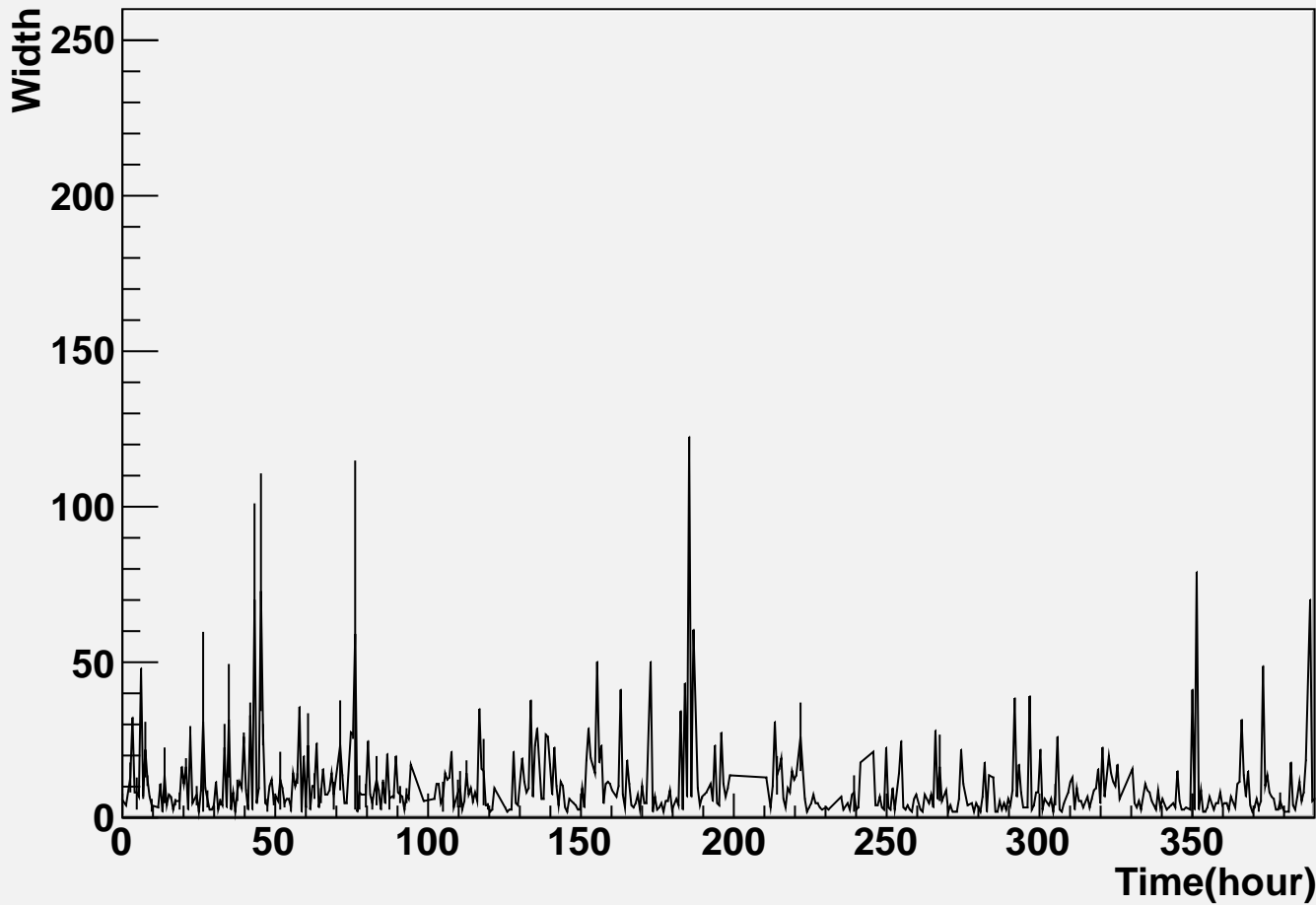
width-S3-SFET2D-Ch2



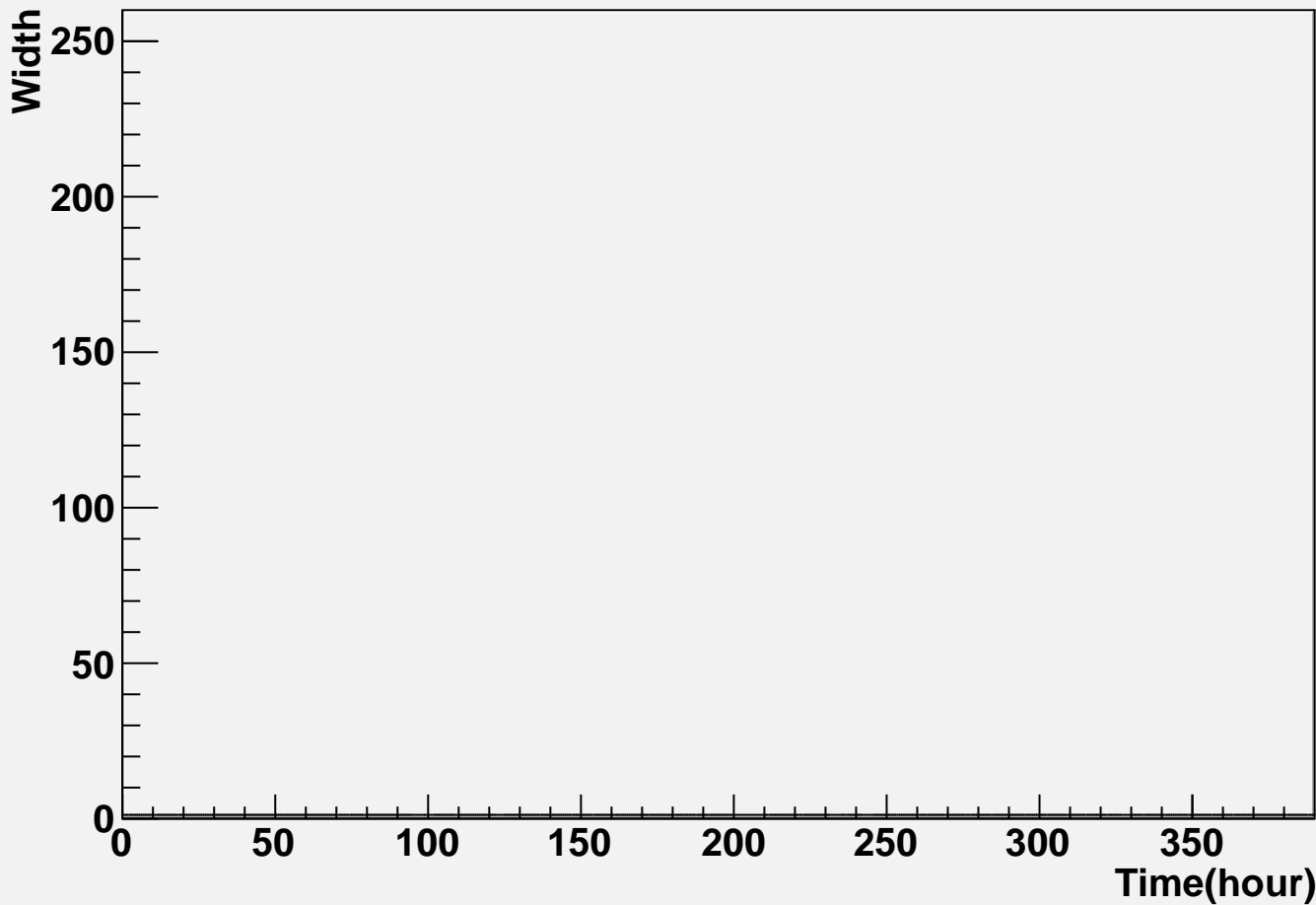
width-S3-SFET2D-Ch3



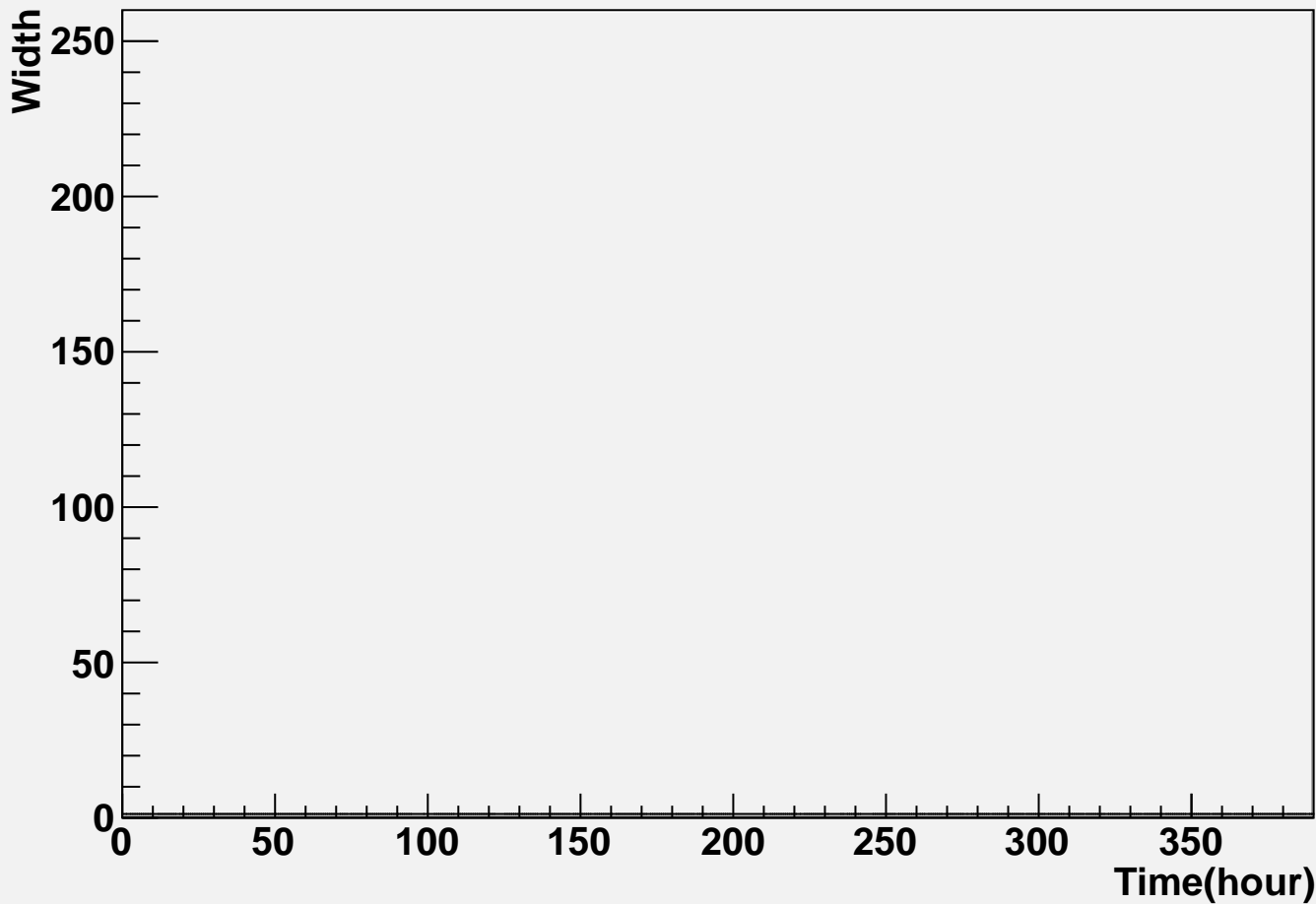
width-S3-SFET2D-Ch4



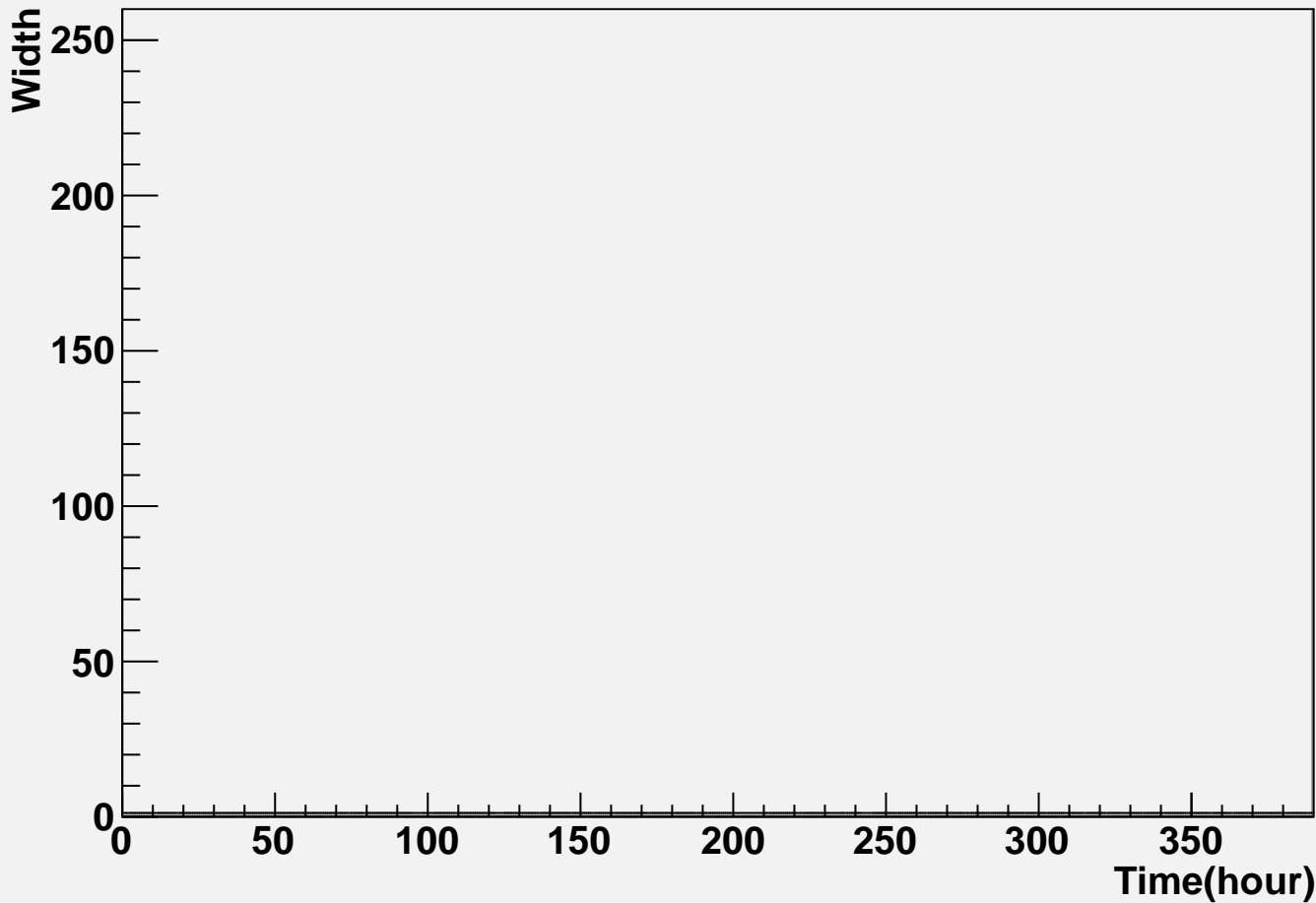
width-S3-SFET2D-Ch5



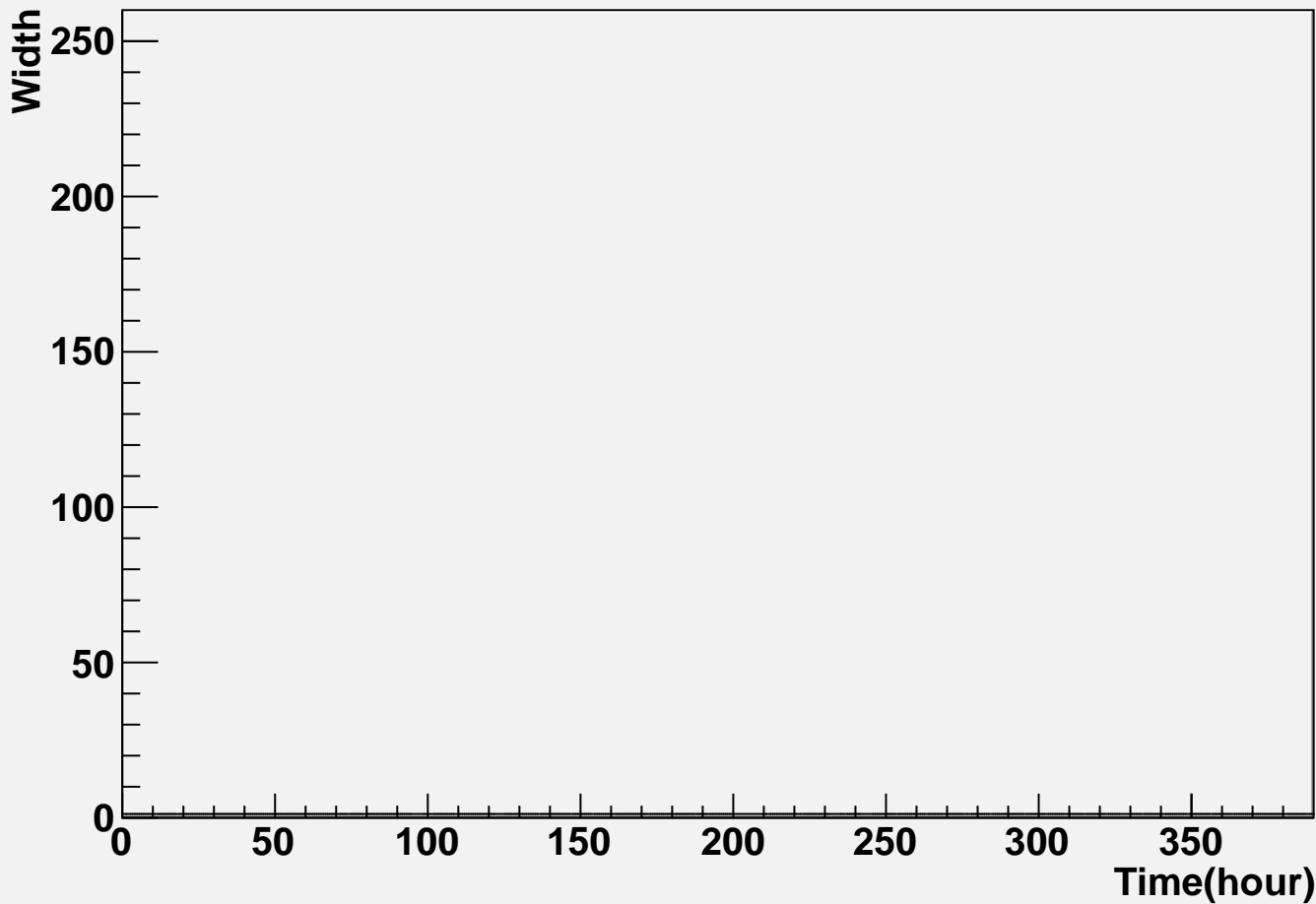
width-S3-SFET2D-Ch6



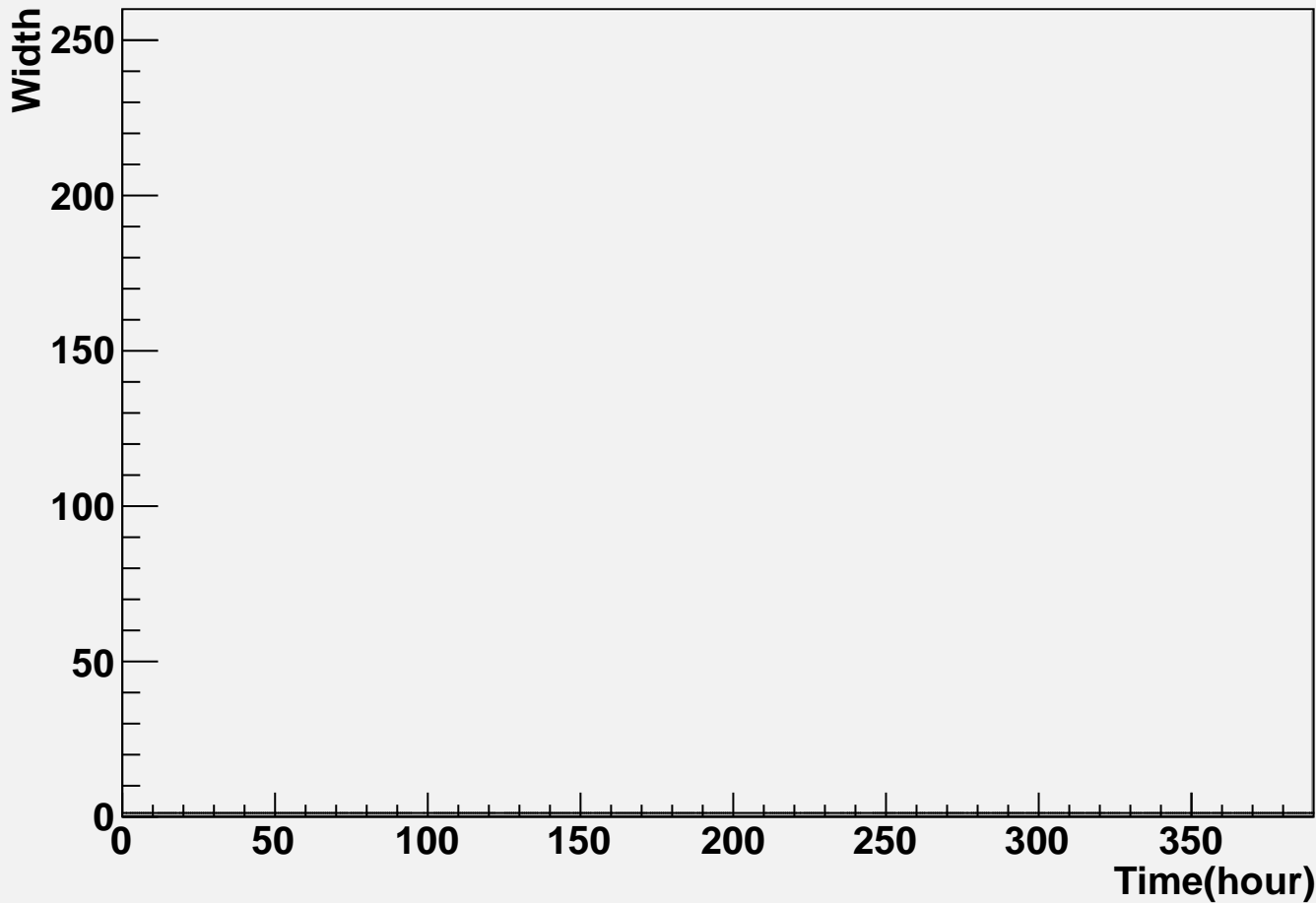
width-S3-SFET2D-Ch7



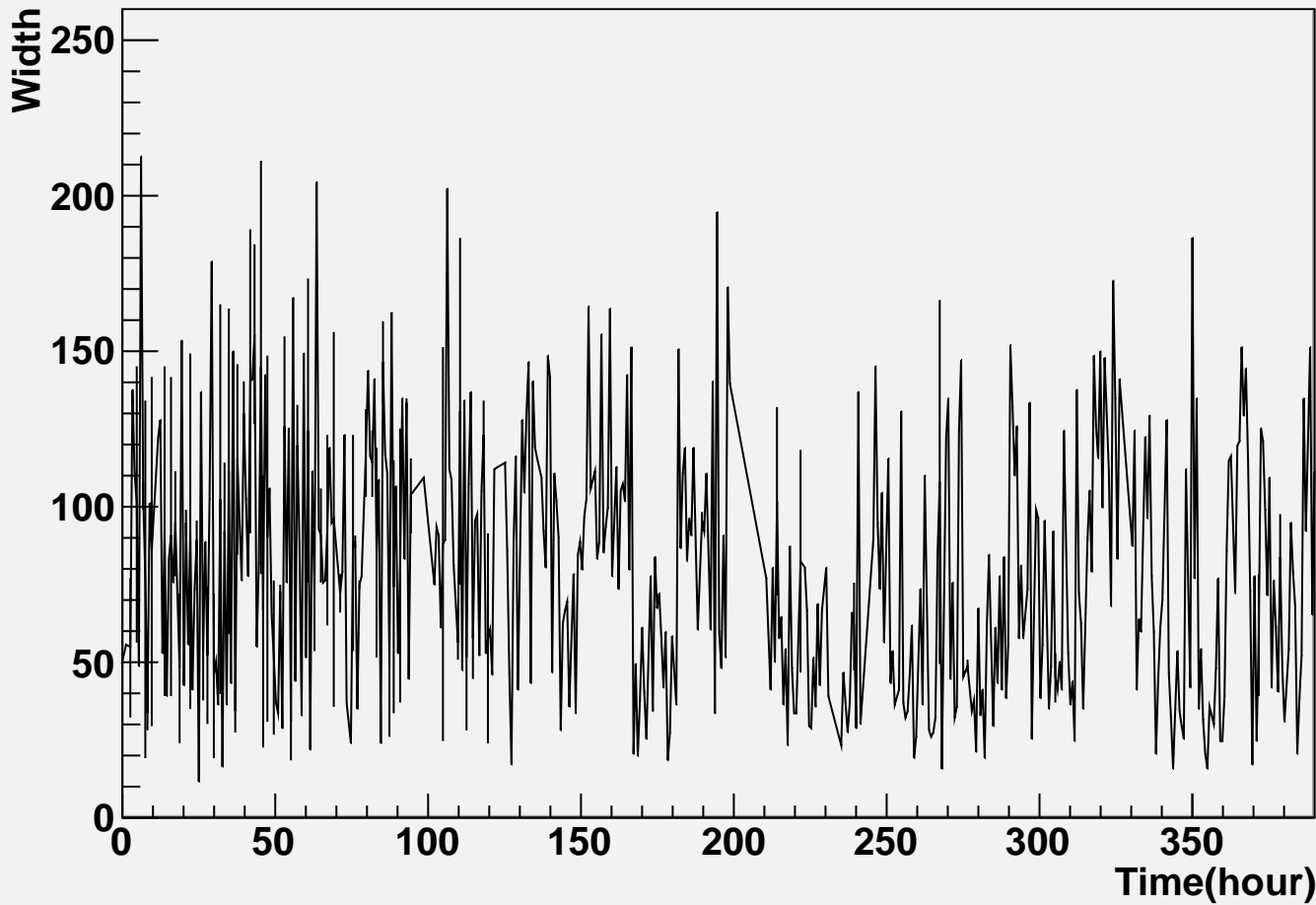
width-S3-SFET2D-Ch8



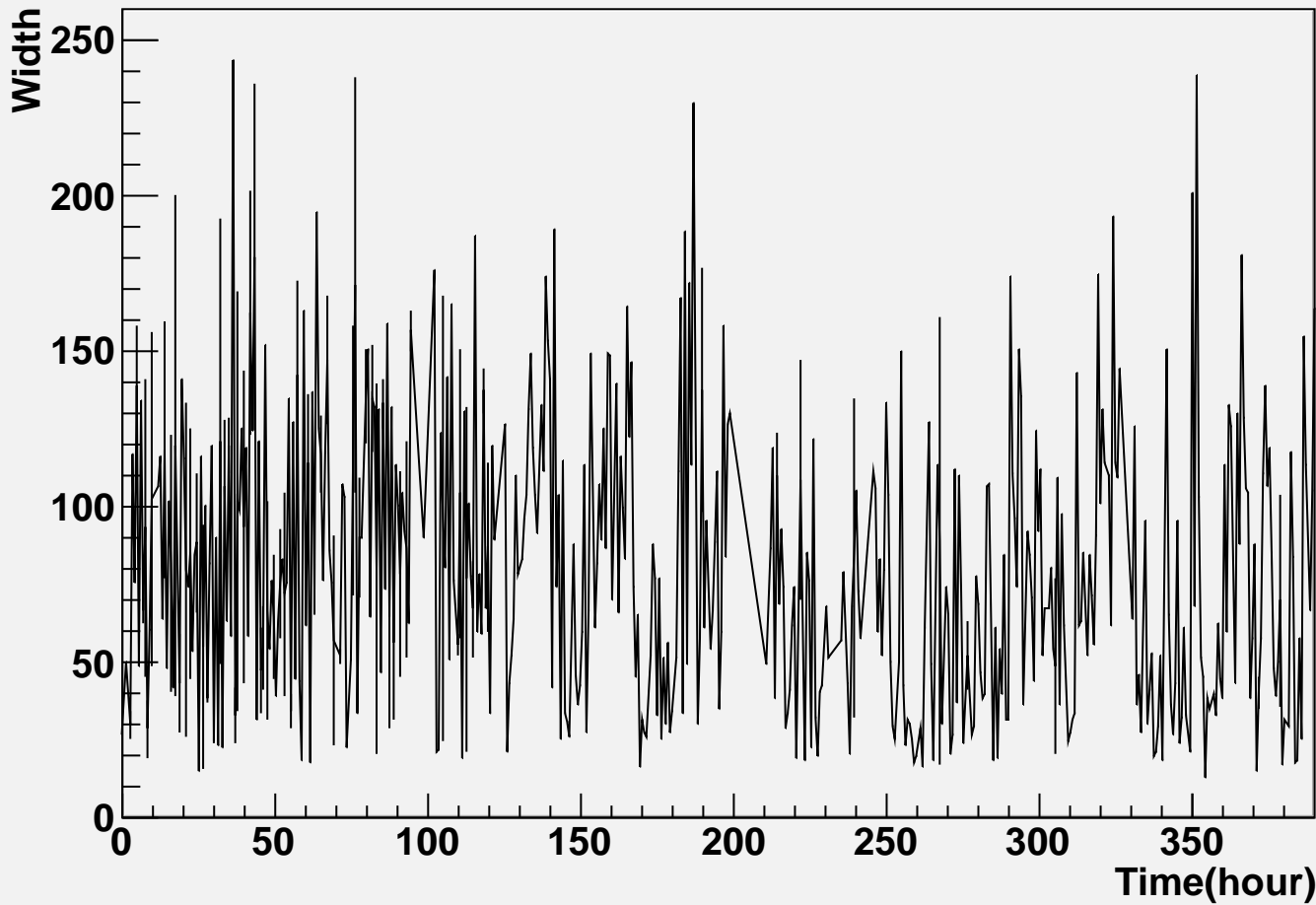
width-S3-SFET2D-Ch9



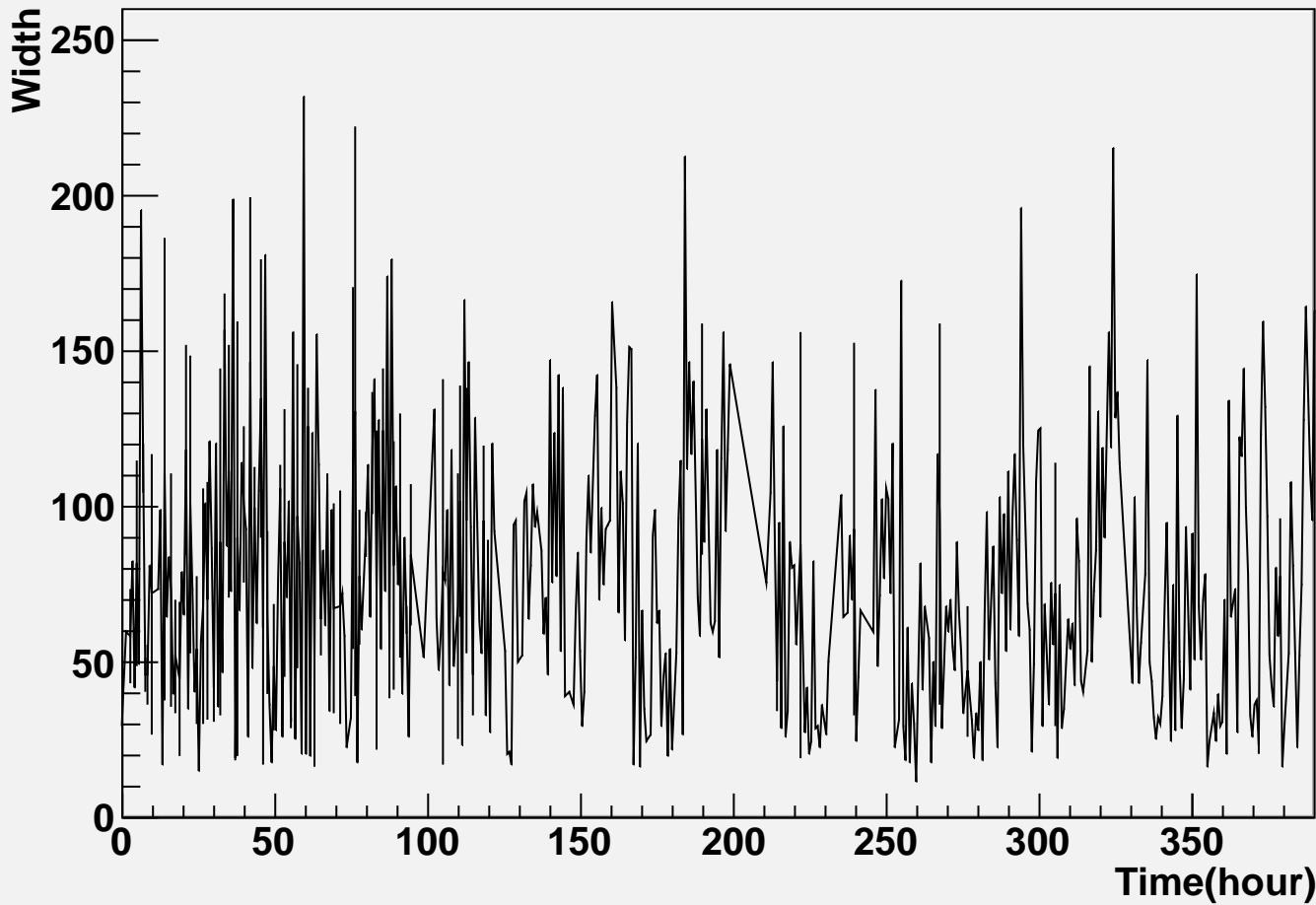
width-S3-SFEA2-Ch0



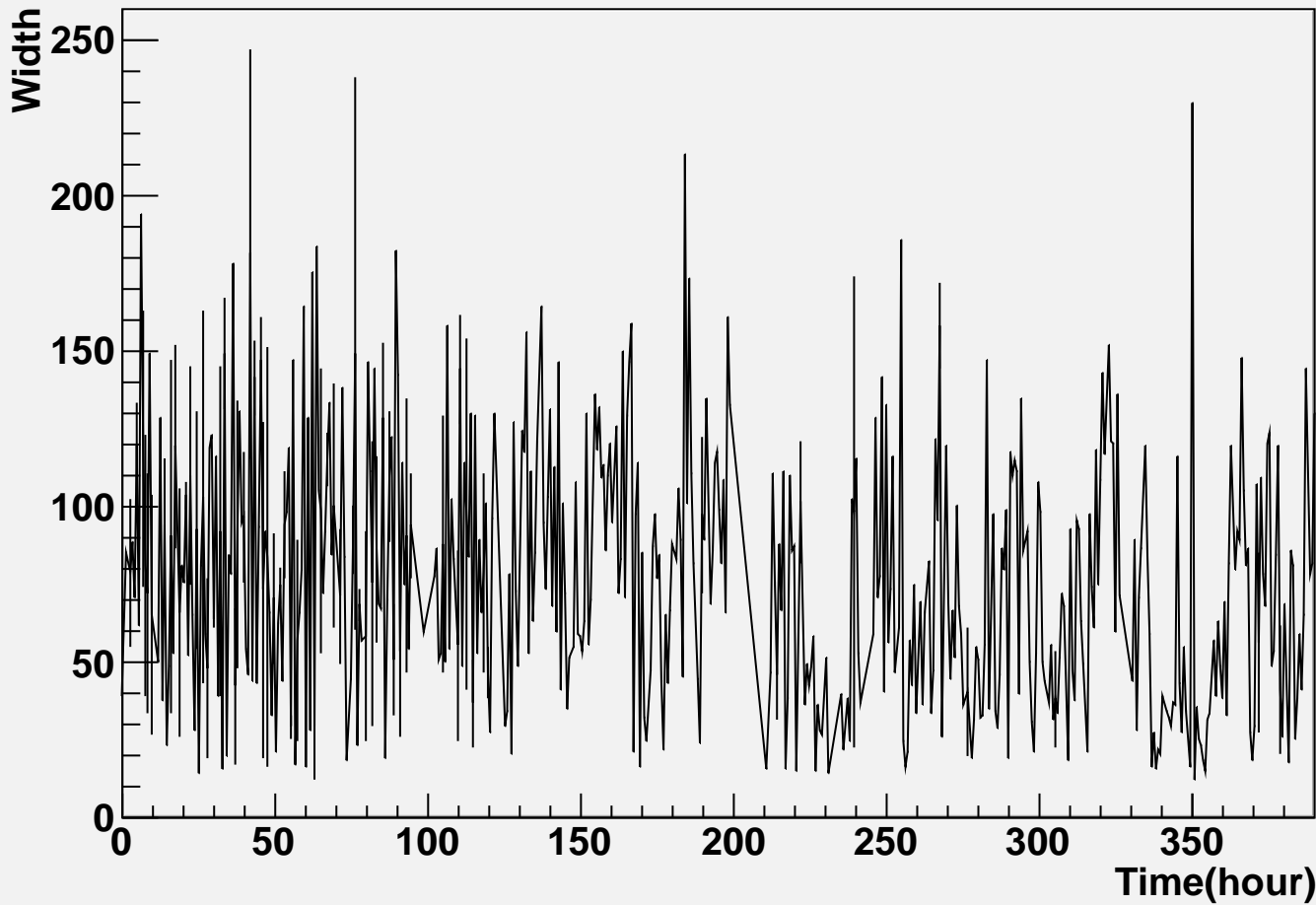
width-S3-SFEA2-Ch1



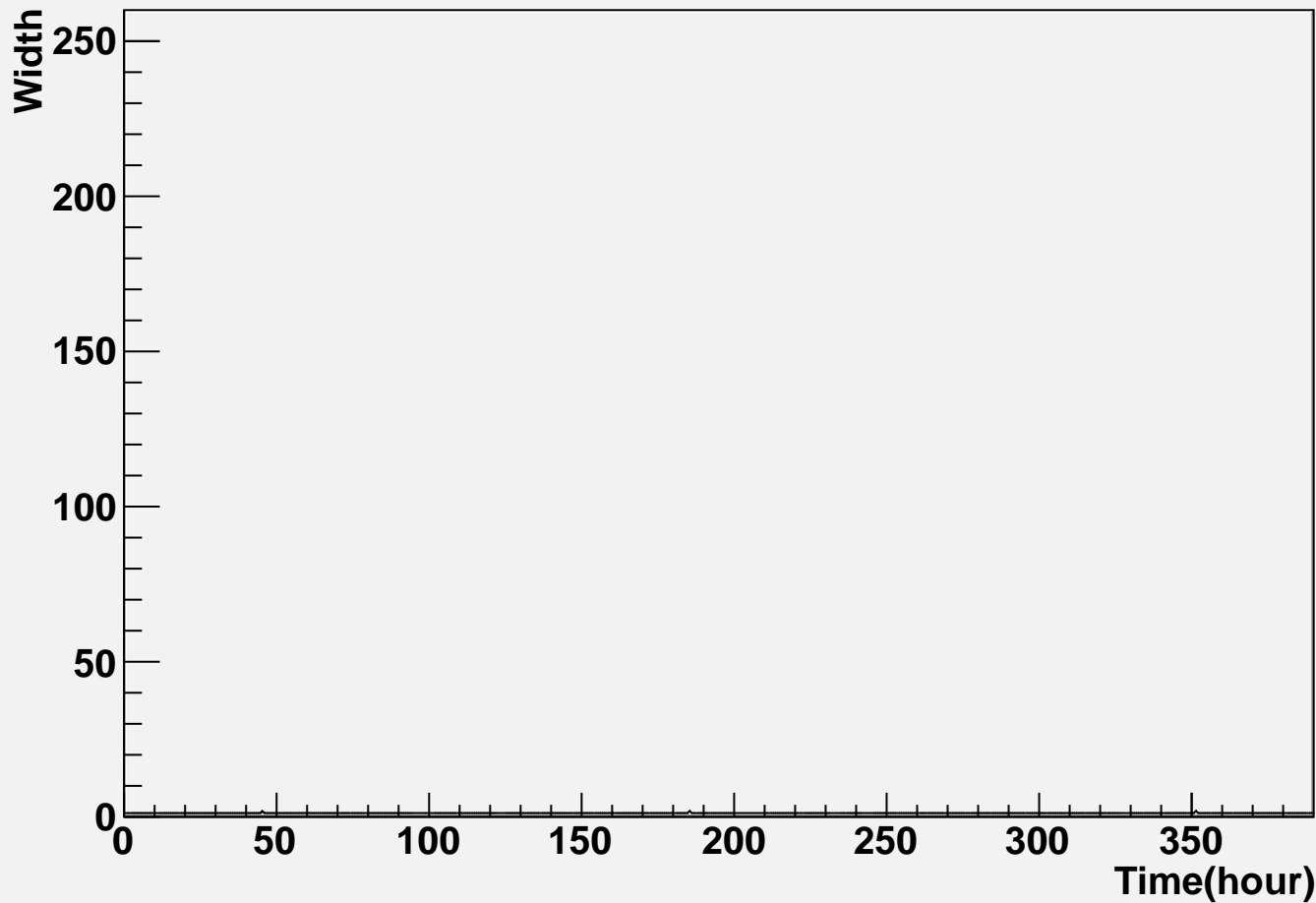
width-S3-SFEA2-Ch2



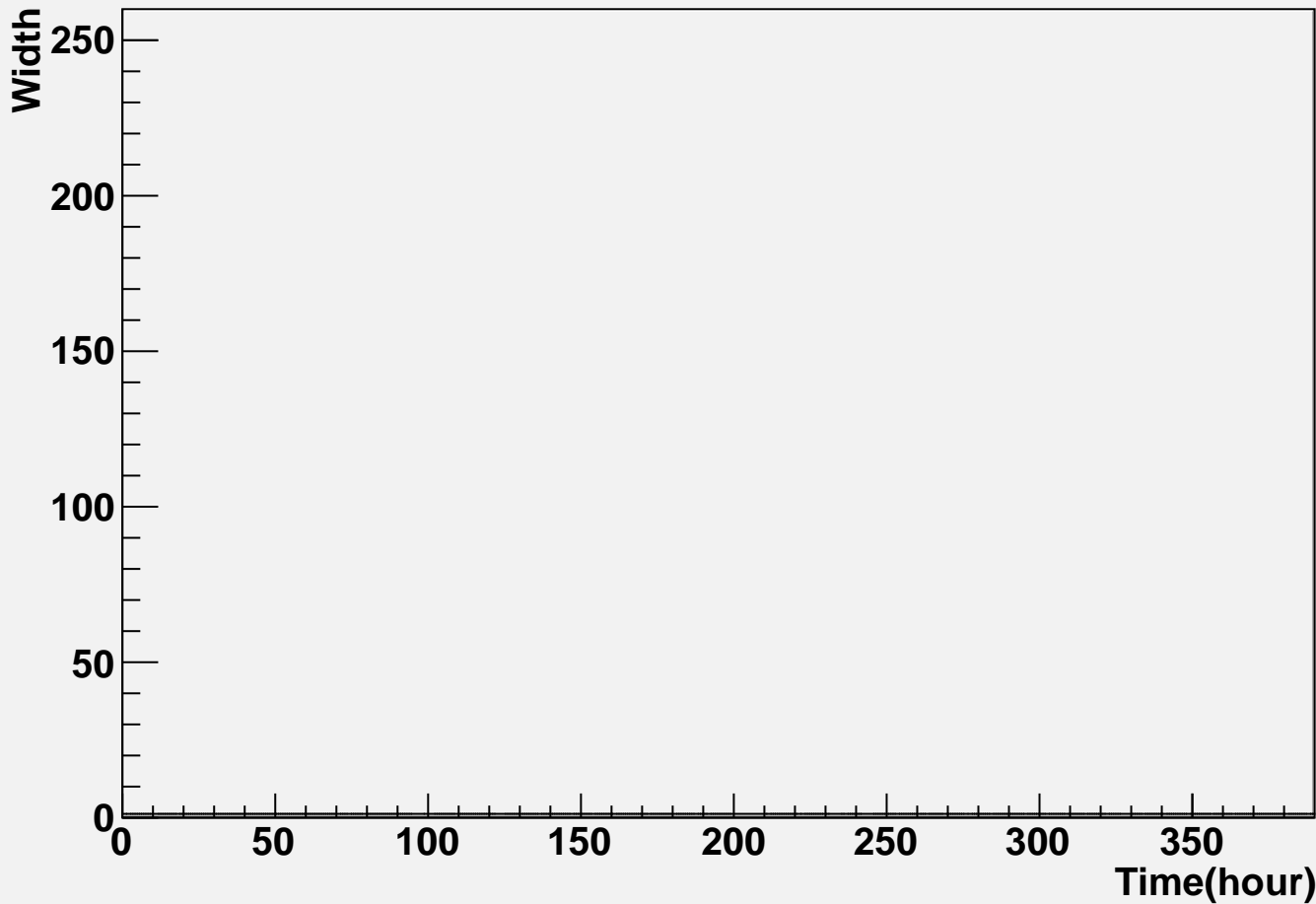
width-S3-SFEA2-Ch3



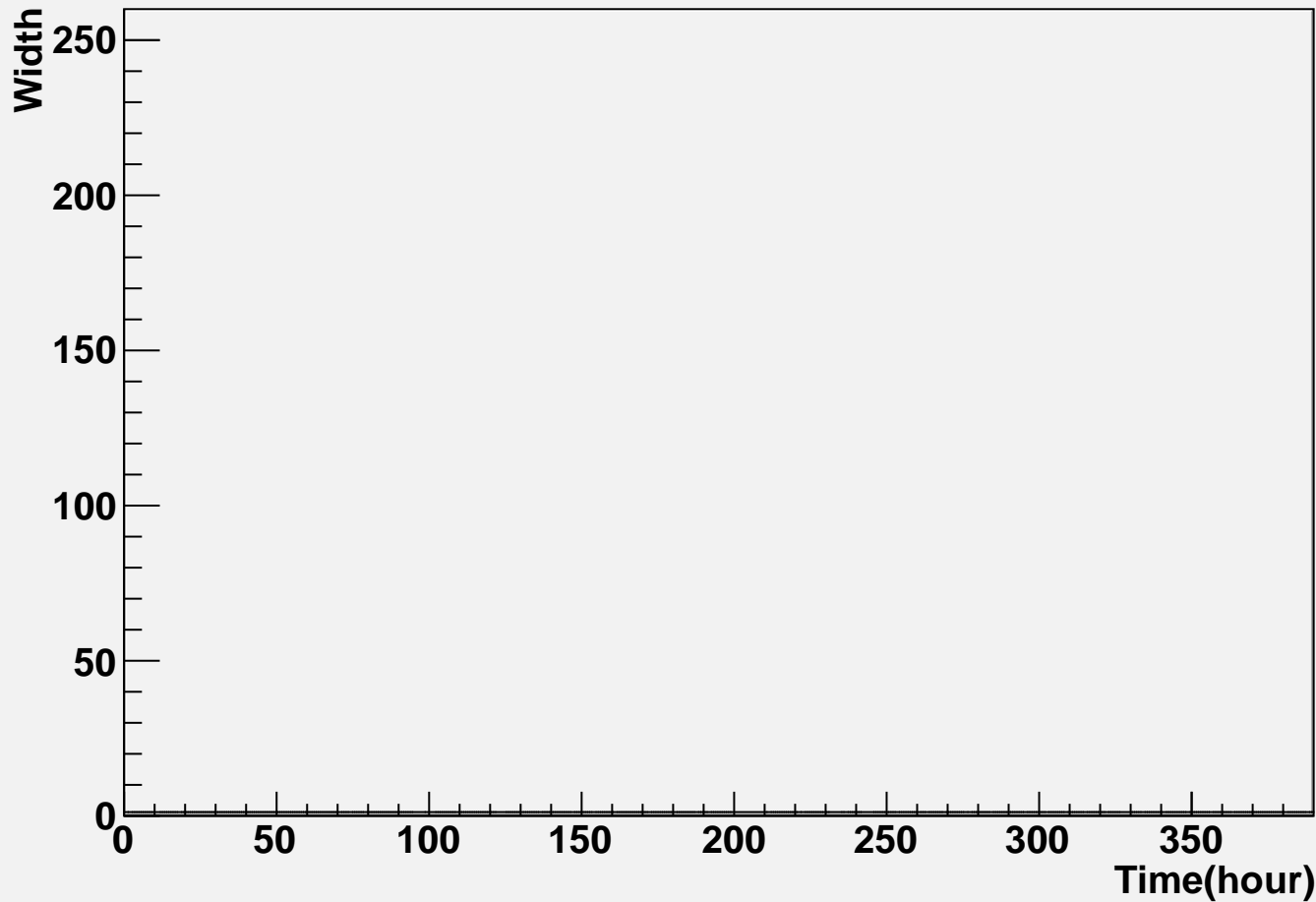
width-S3-SFEA2-Ch4



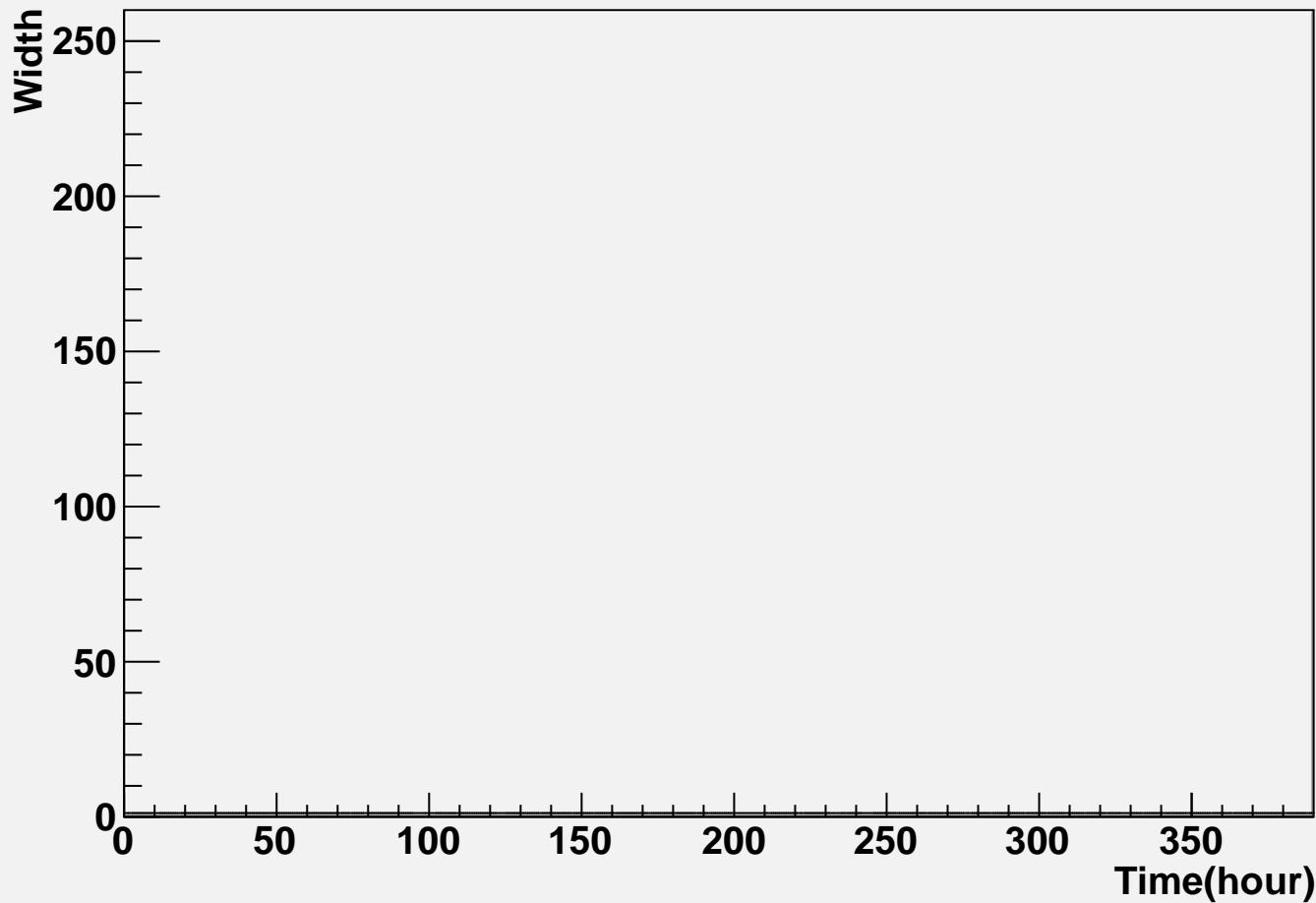
width-S3-SFEA2-Ch5



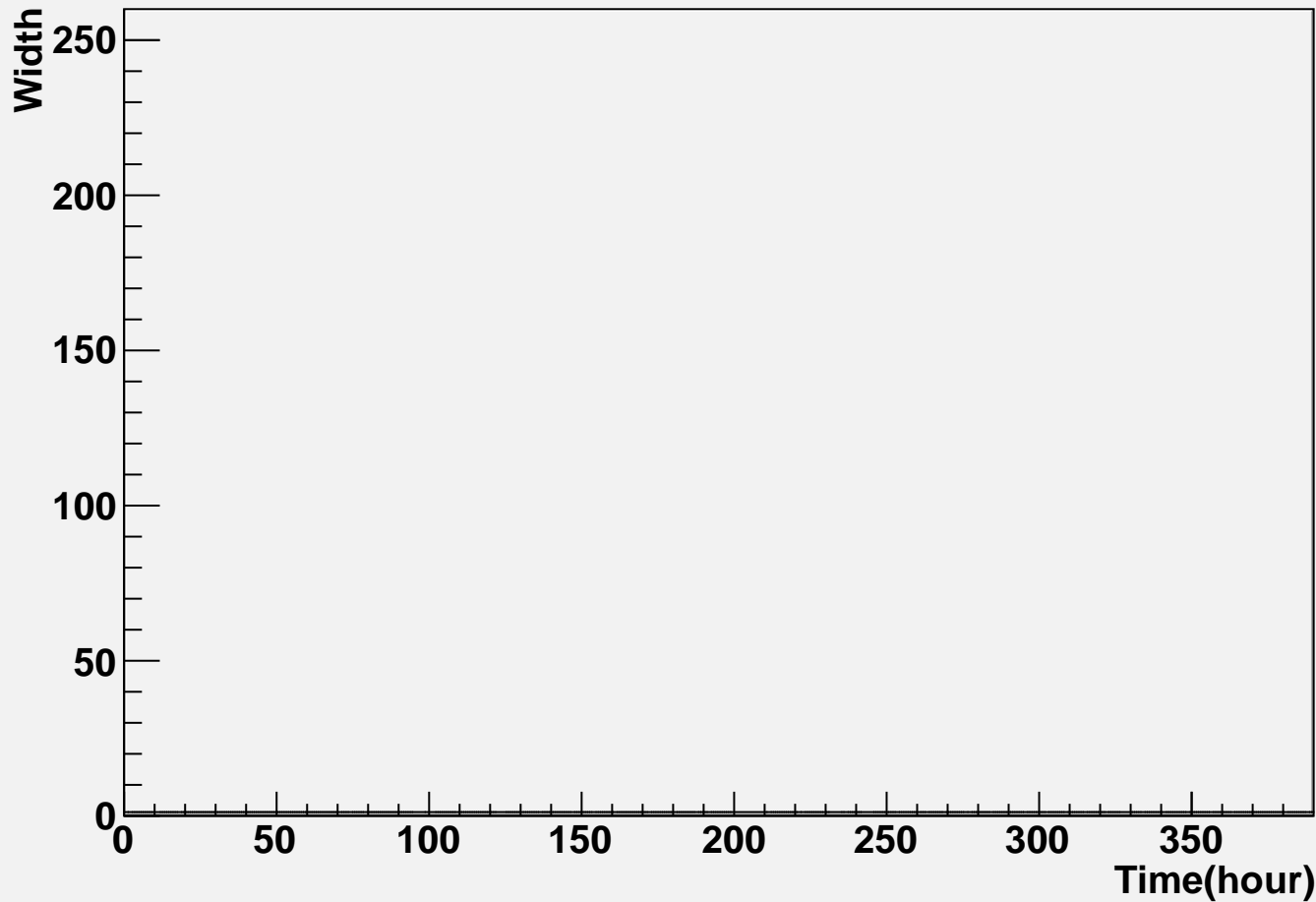
width-S3-SFEA2-Ch6



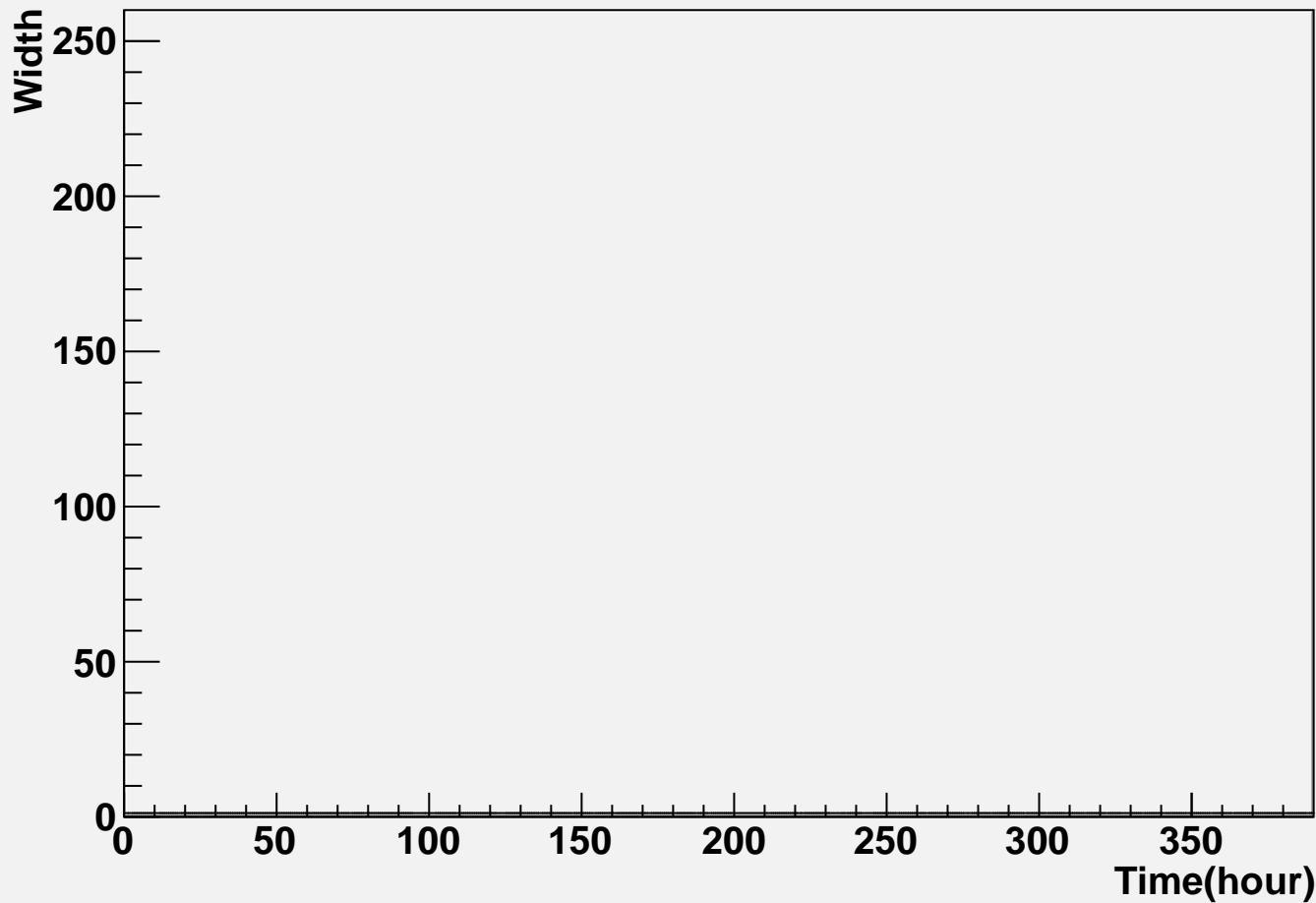
width-S3-SFEA2-Ch7



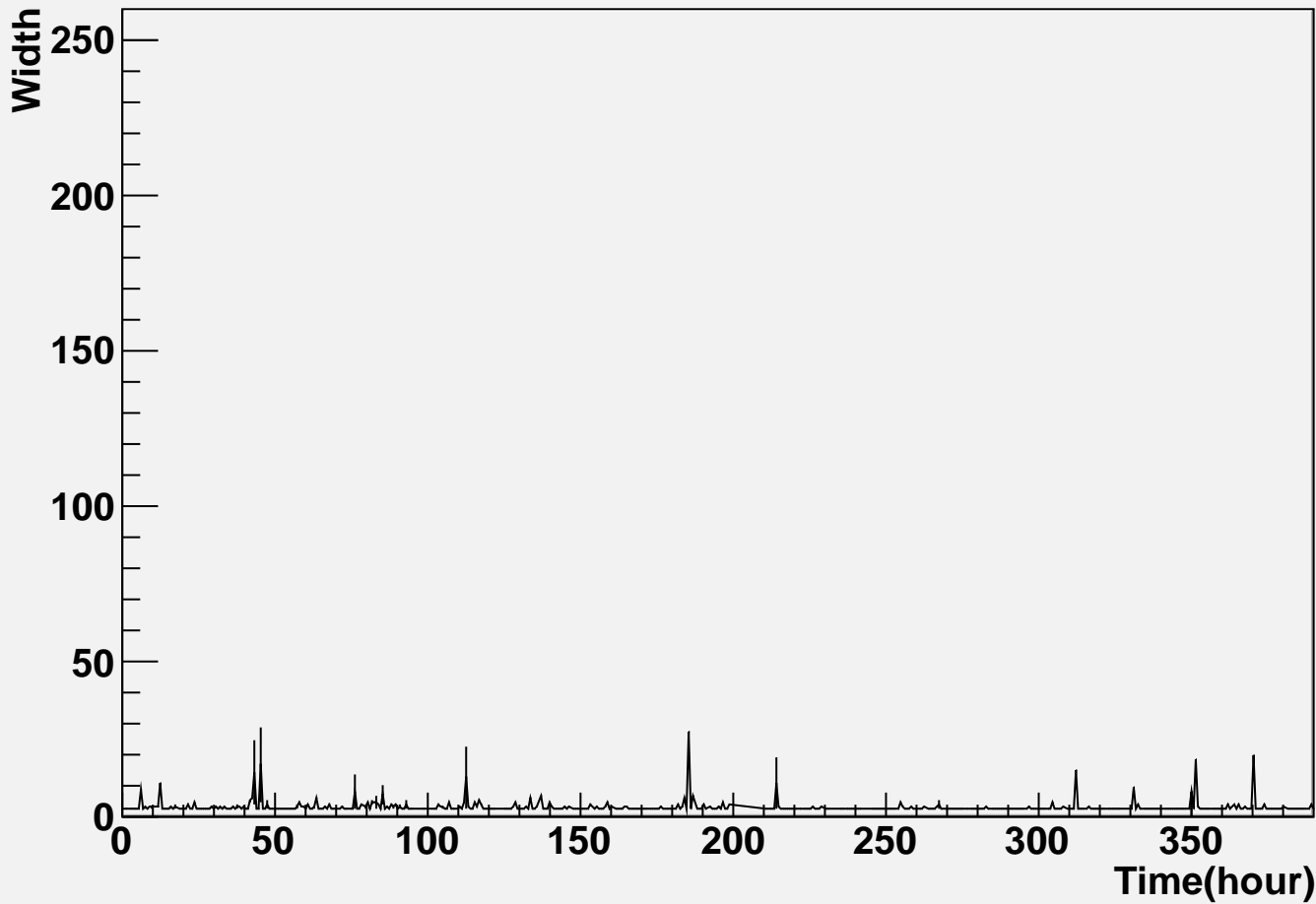
width-S3-SFEA2-Ch8



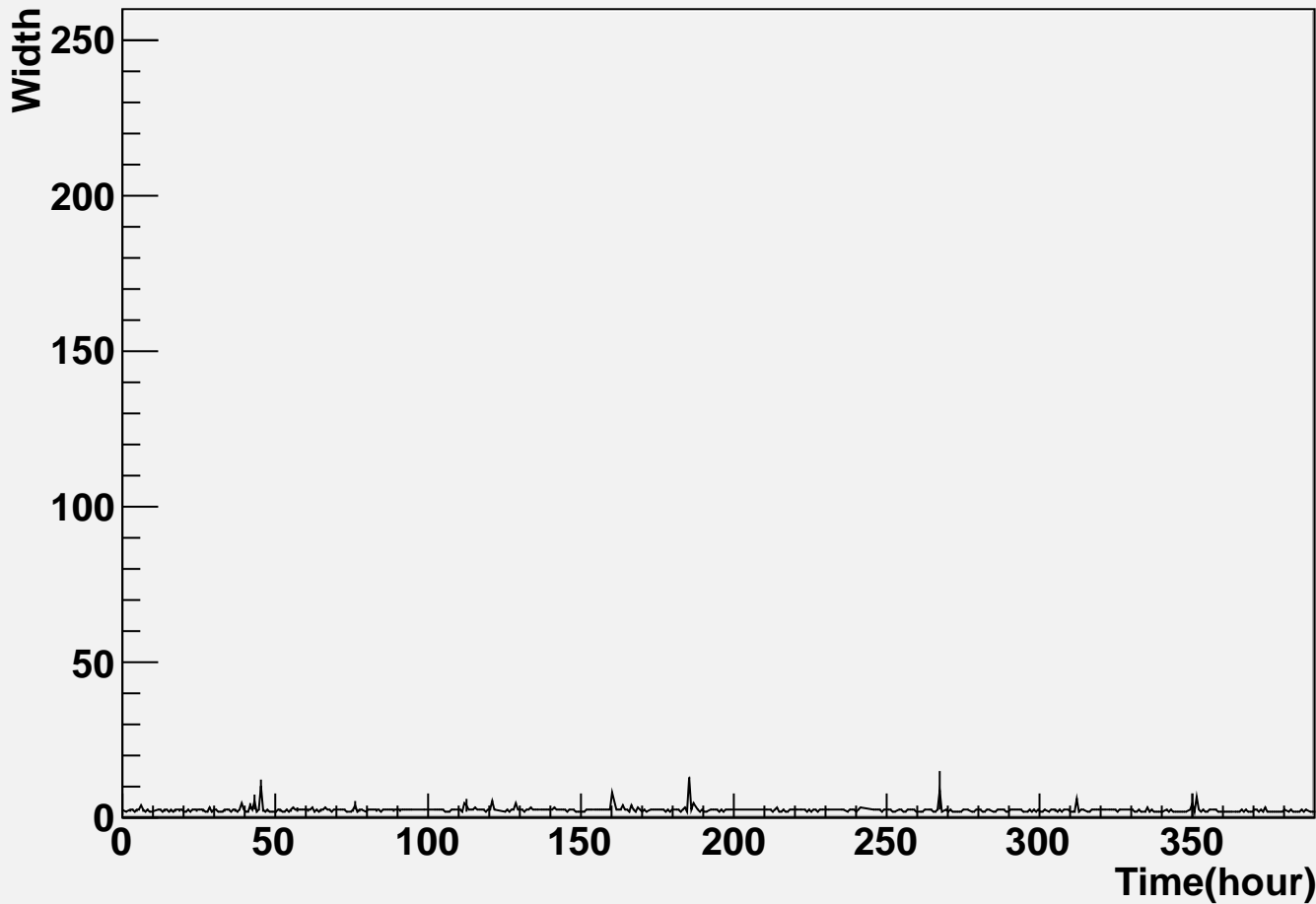
width-S3-SFEA2-Ch9



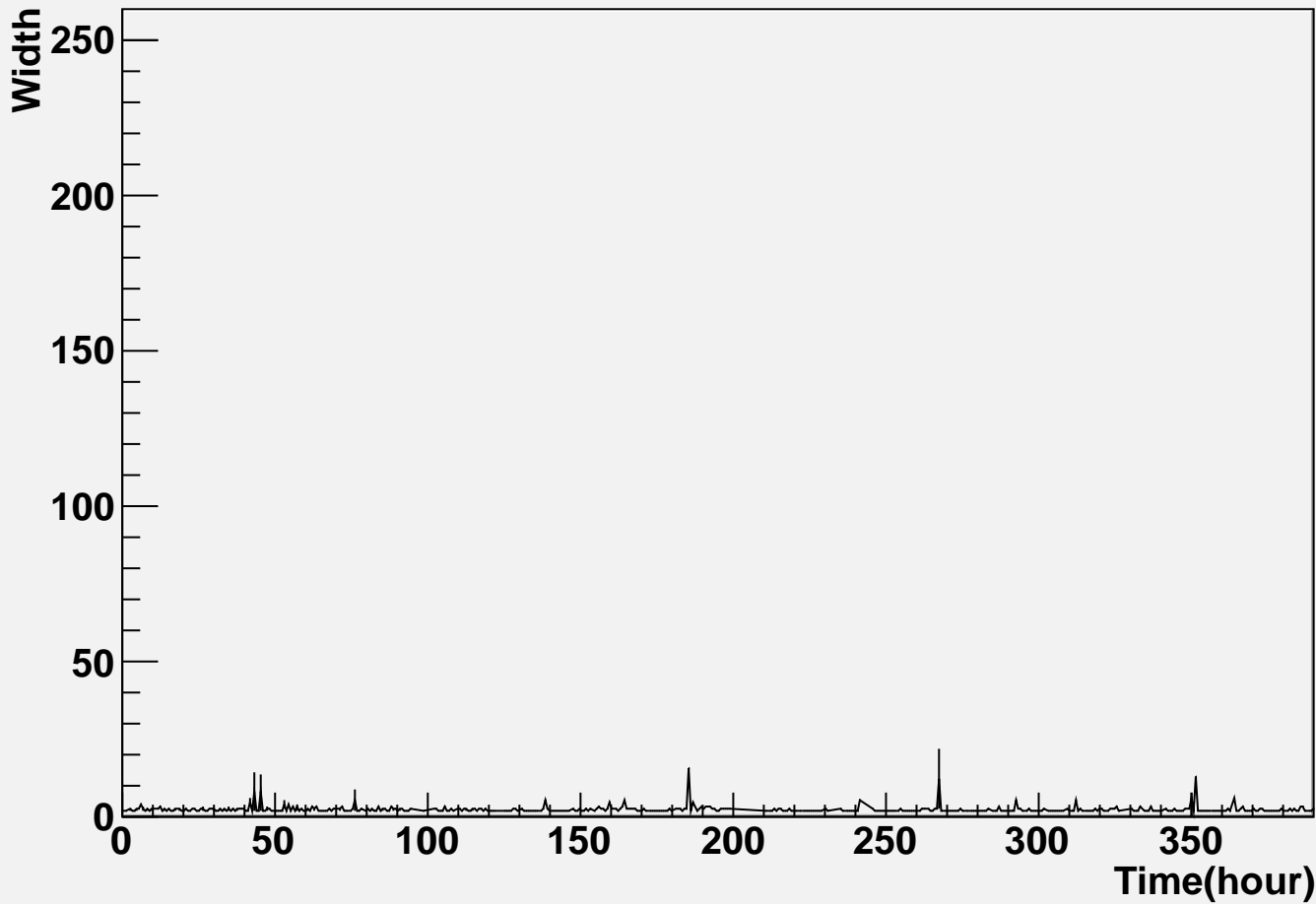
width-S3-SFEC00-Ch0



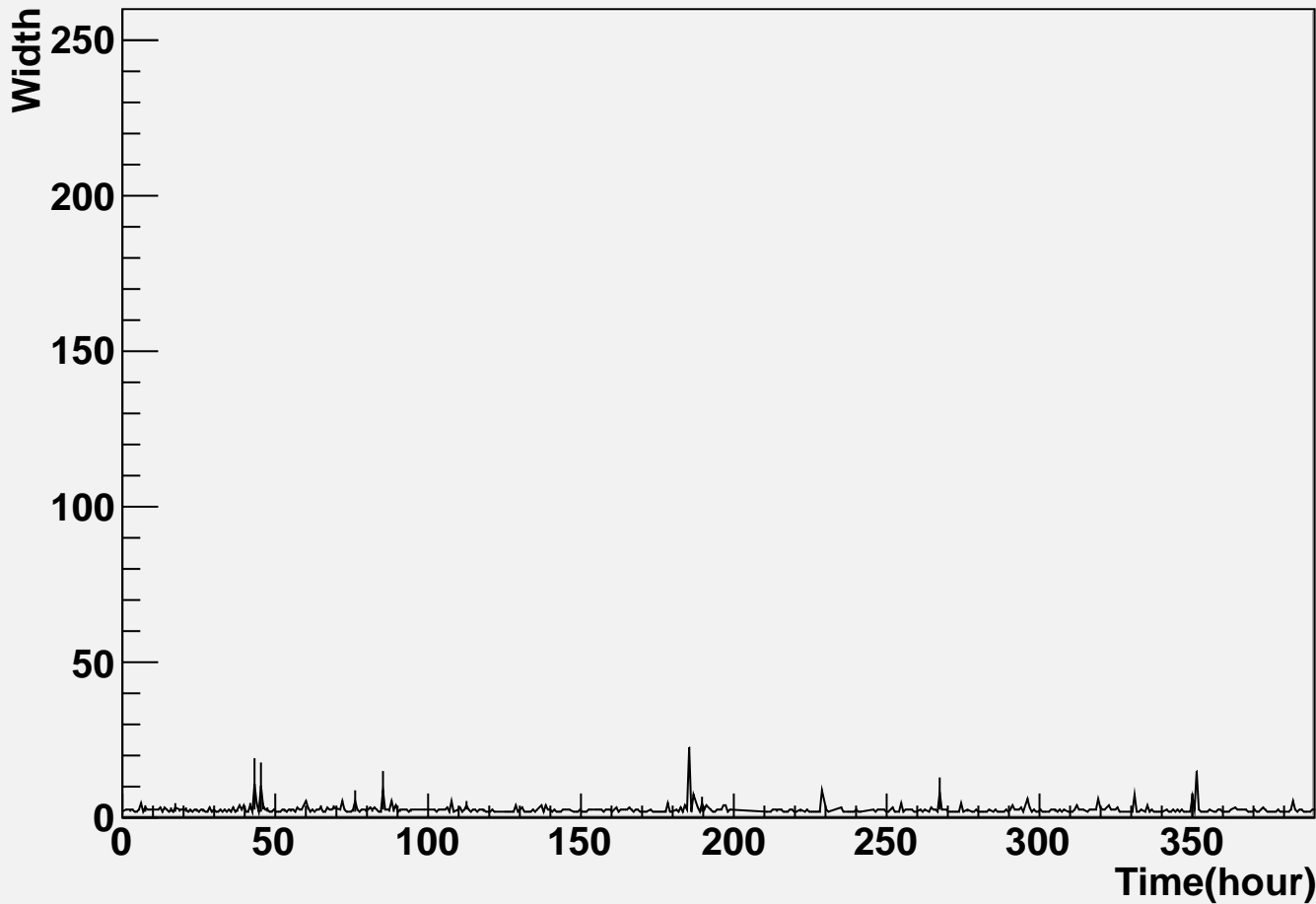
width-S3-SFEC00-Ch1



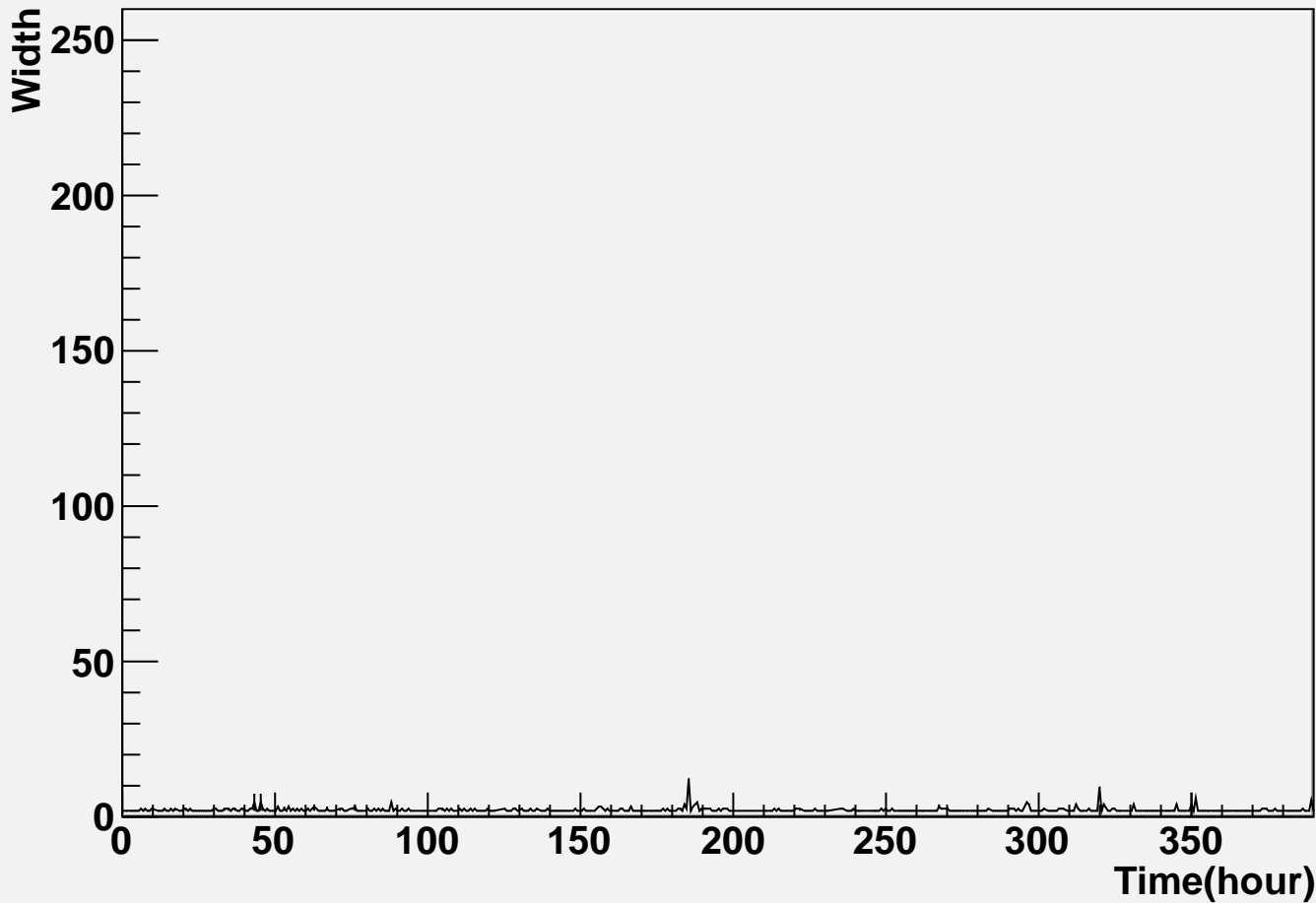
width-S3-SFEC00-Ch2



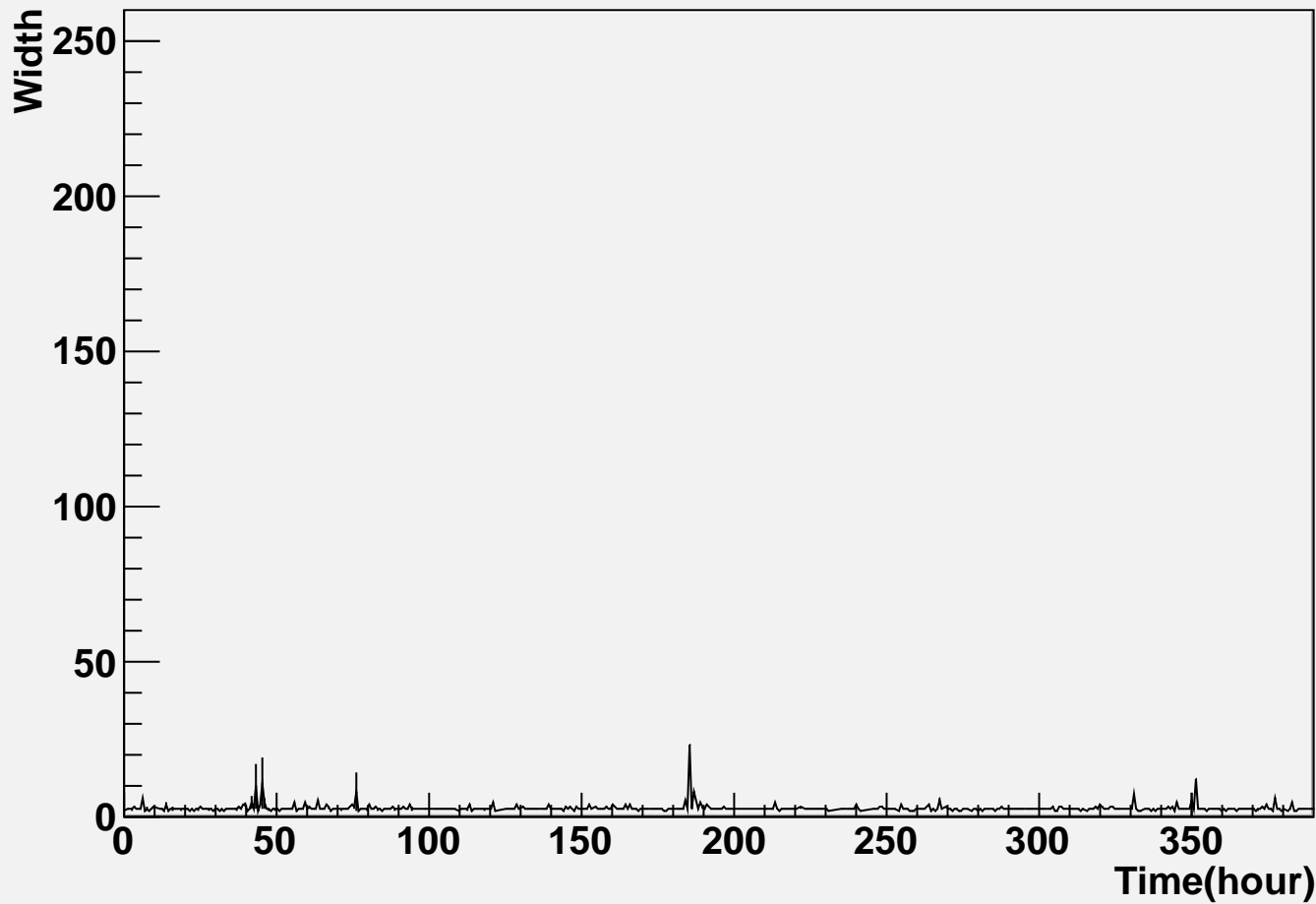
width-S3-SFEC00-Ch3



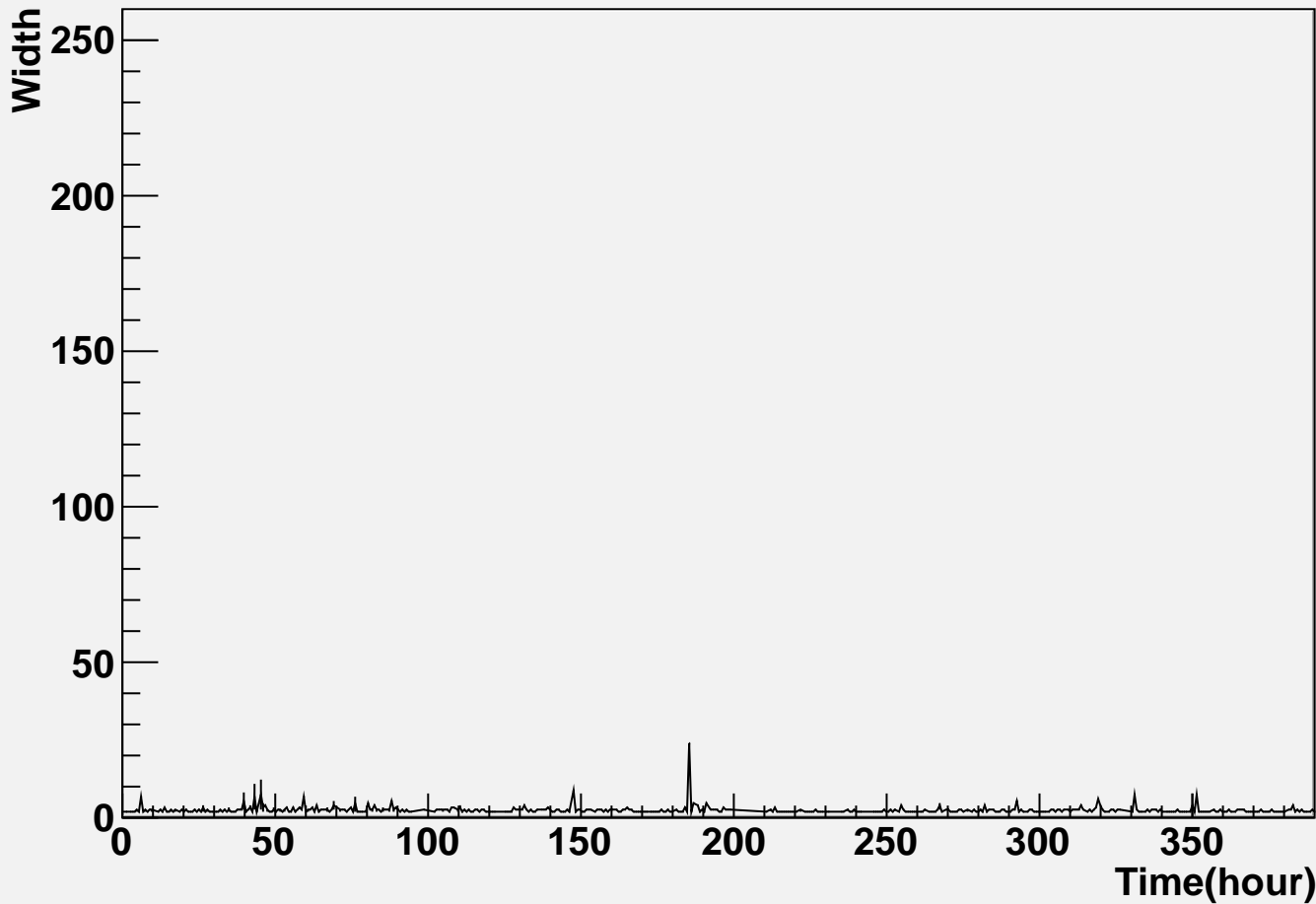
width-S3-SFEC00-Ch4



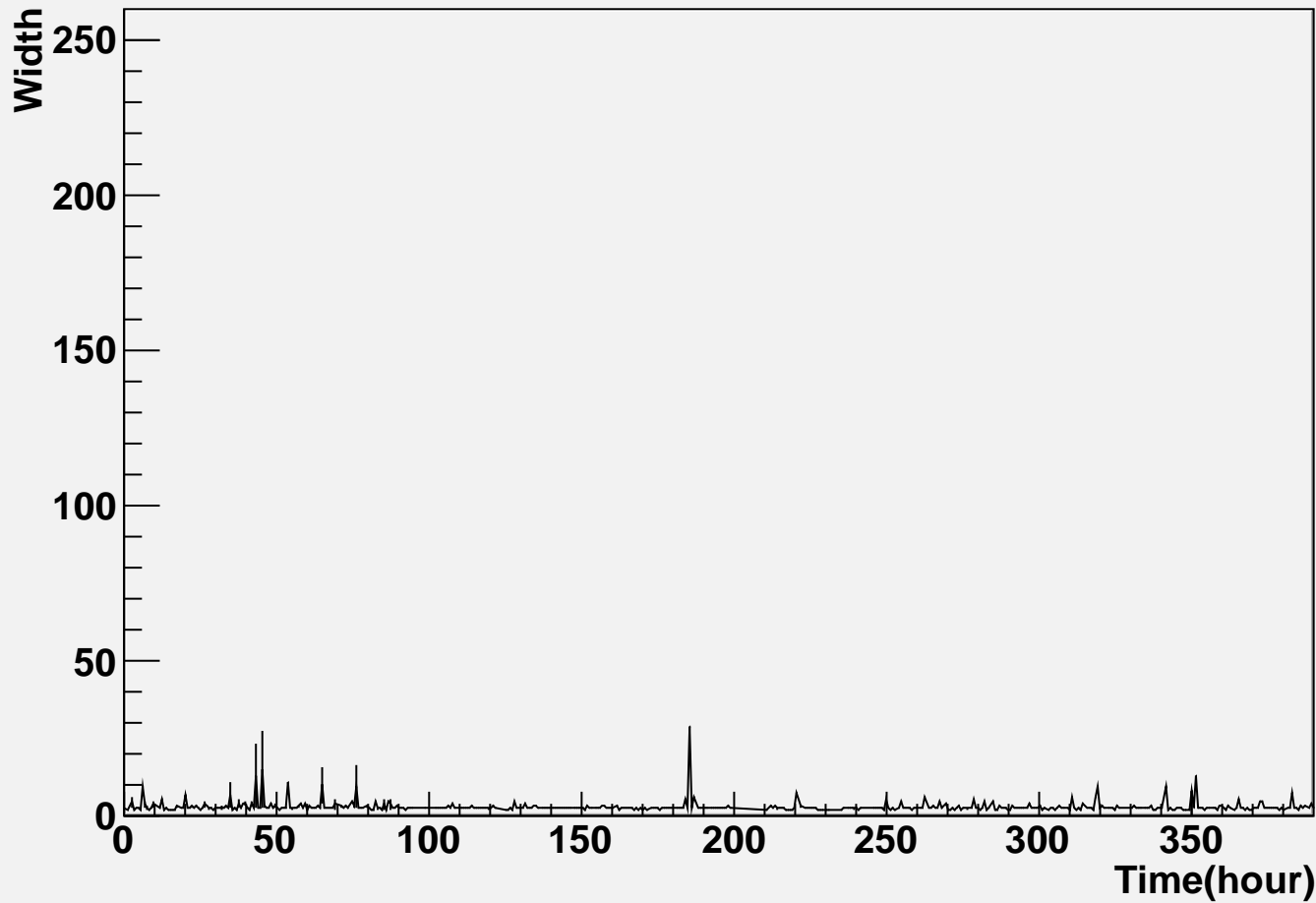
width-S3-SFEC00-Ch5



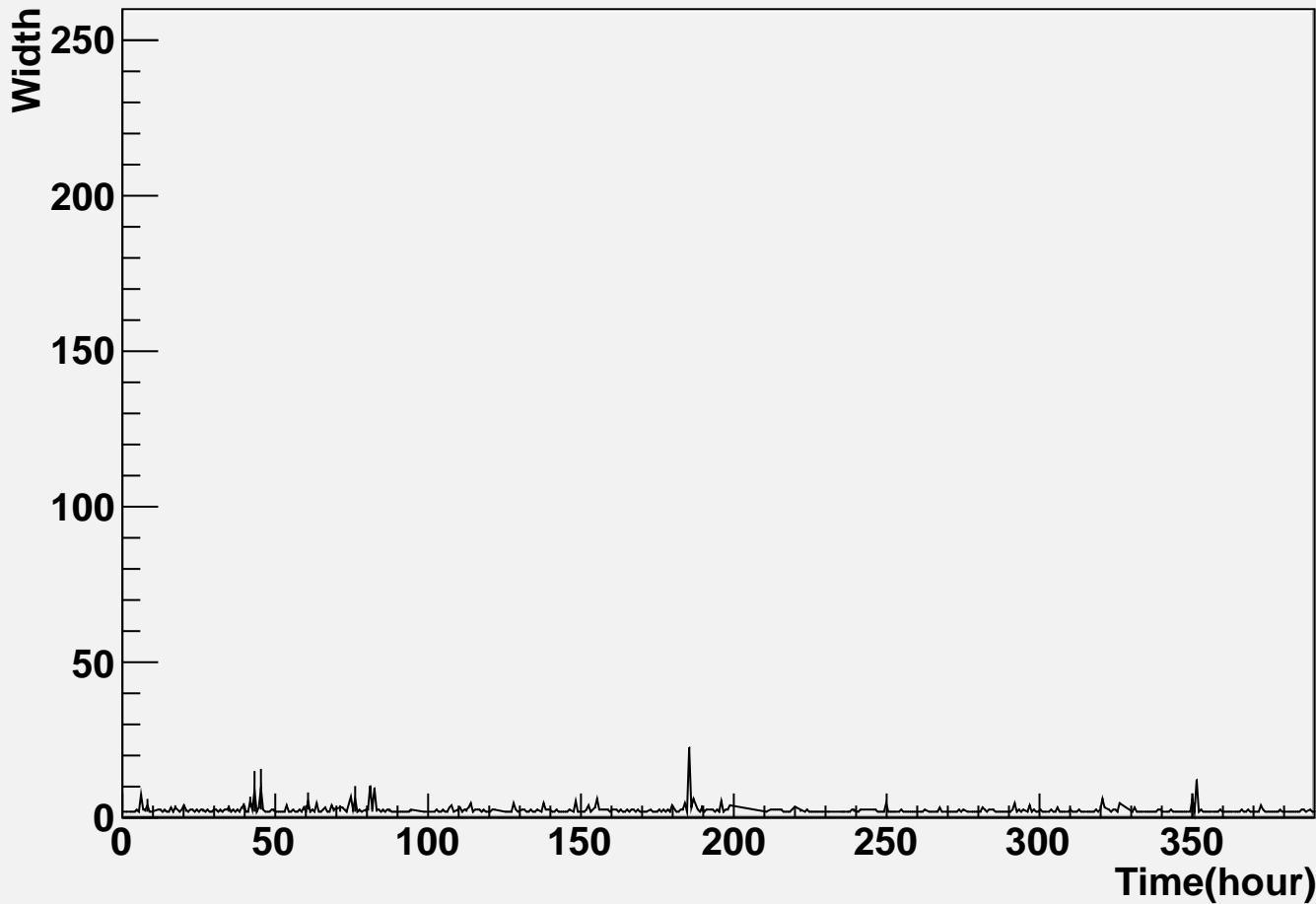
width-S3-SFEC00-Ch6



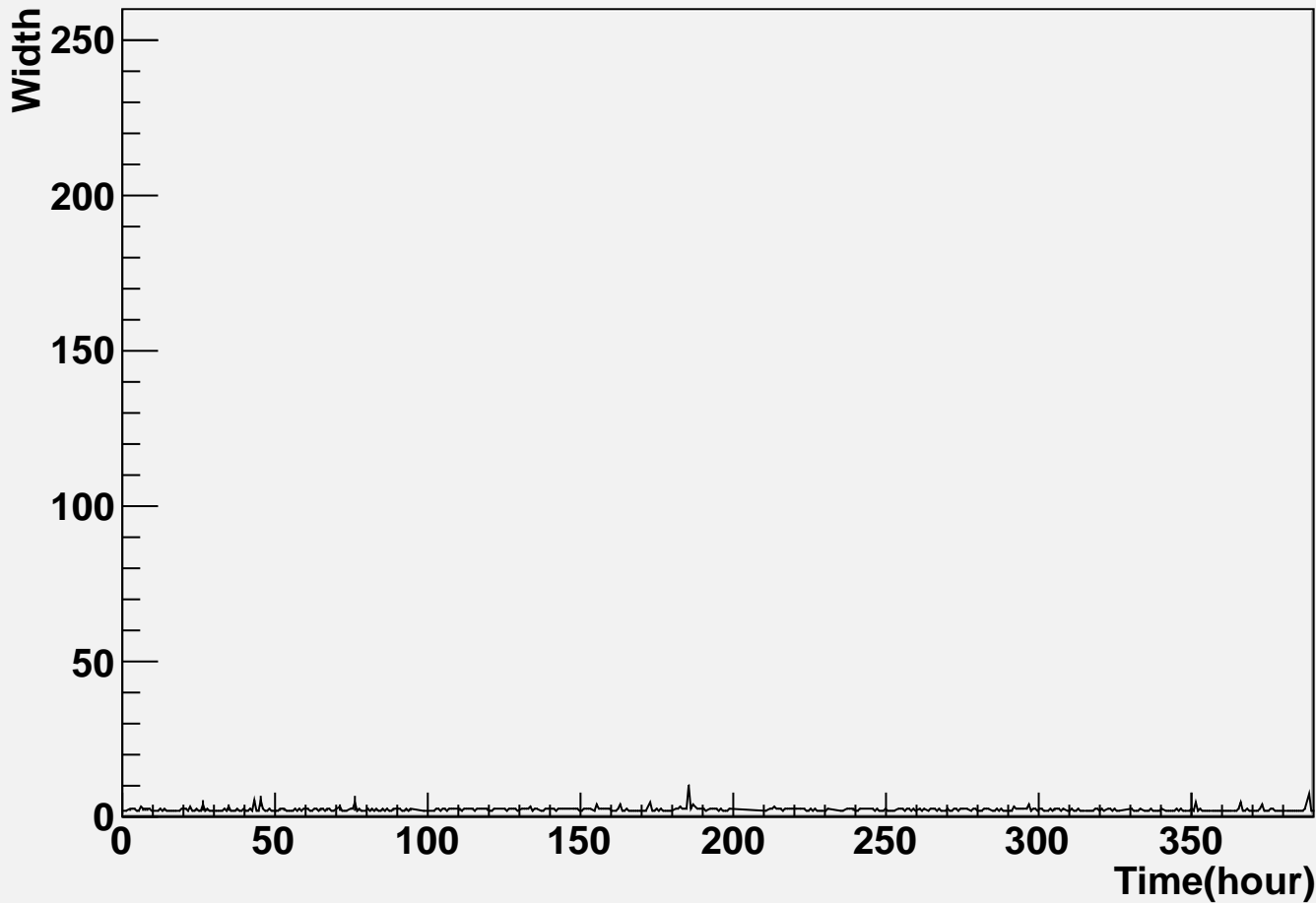
width-S3-SFEC00-Ch7



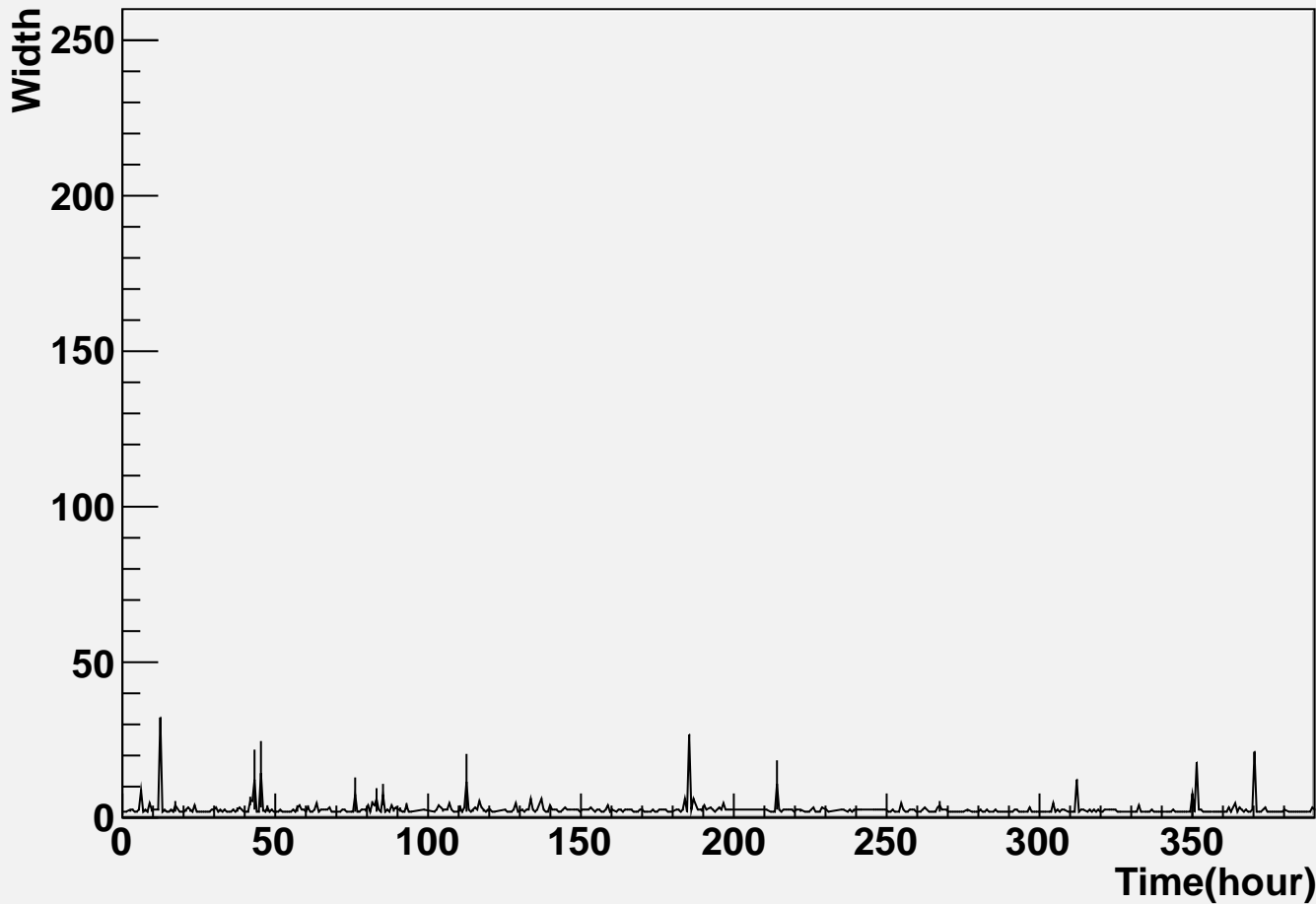
width-S3-SFEC00-Ch8



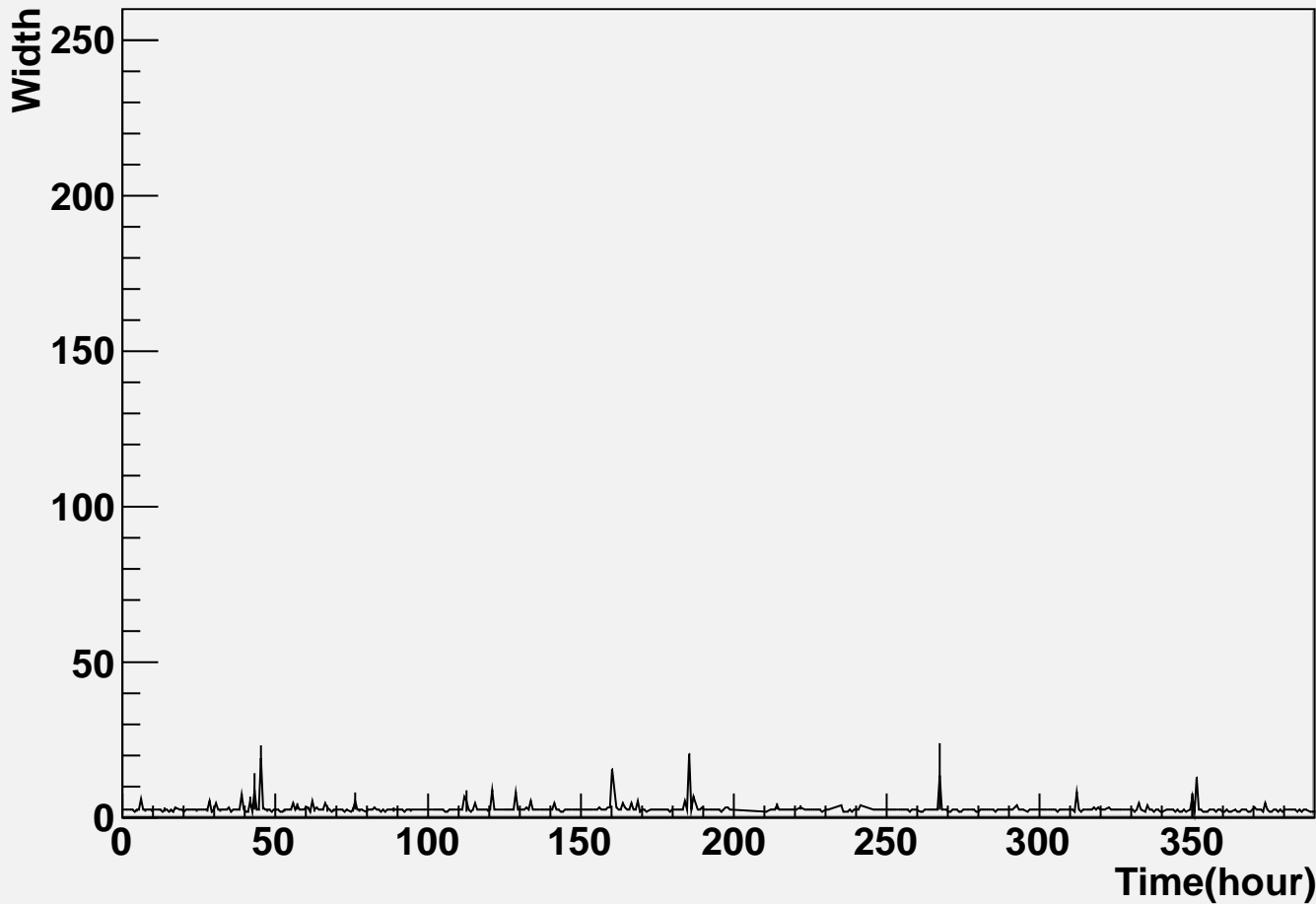
width-S3-SFEC00-Ch9



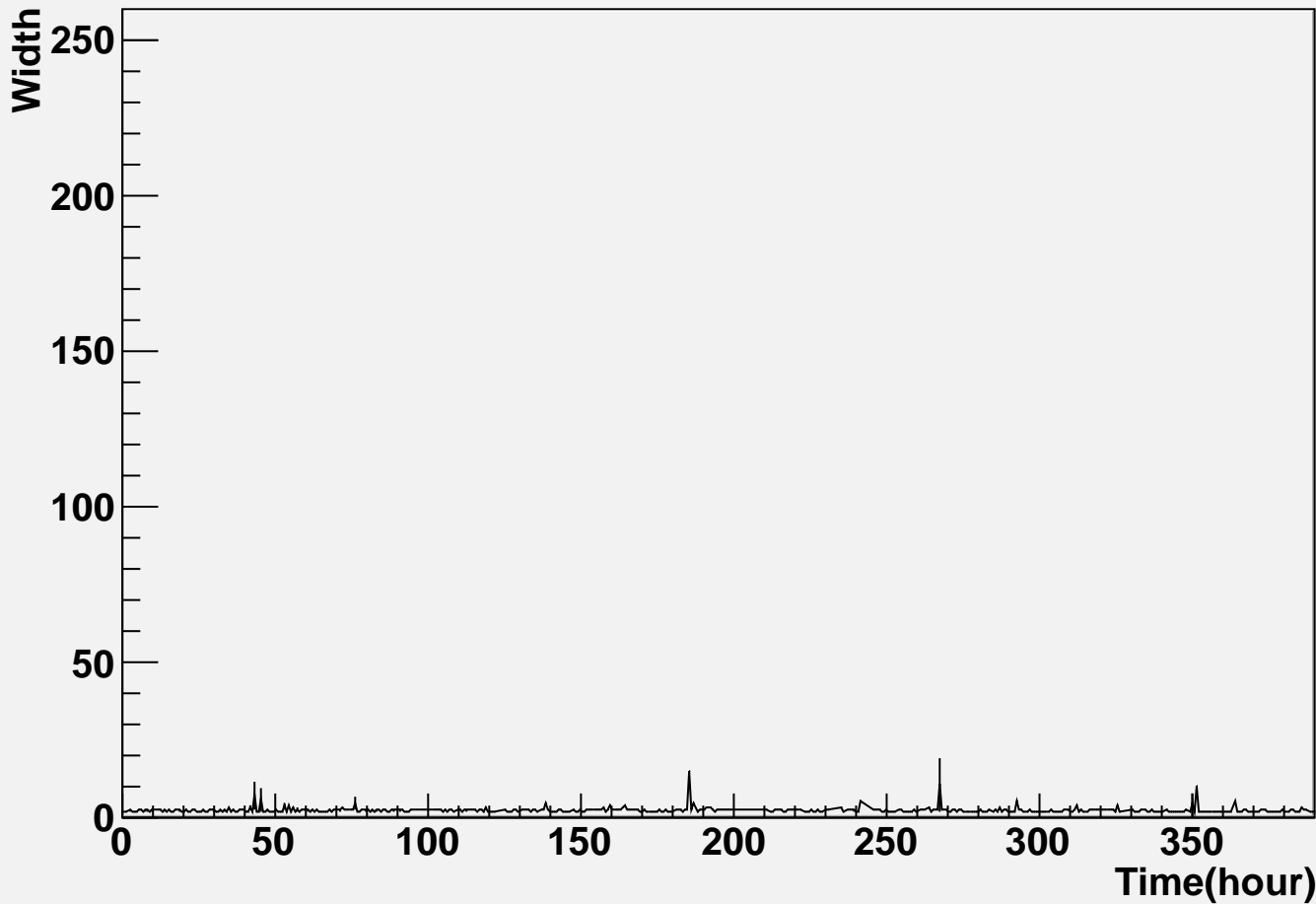
width-S3-SFEC01-Ch0



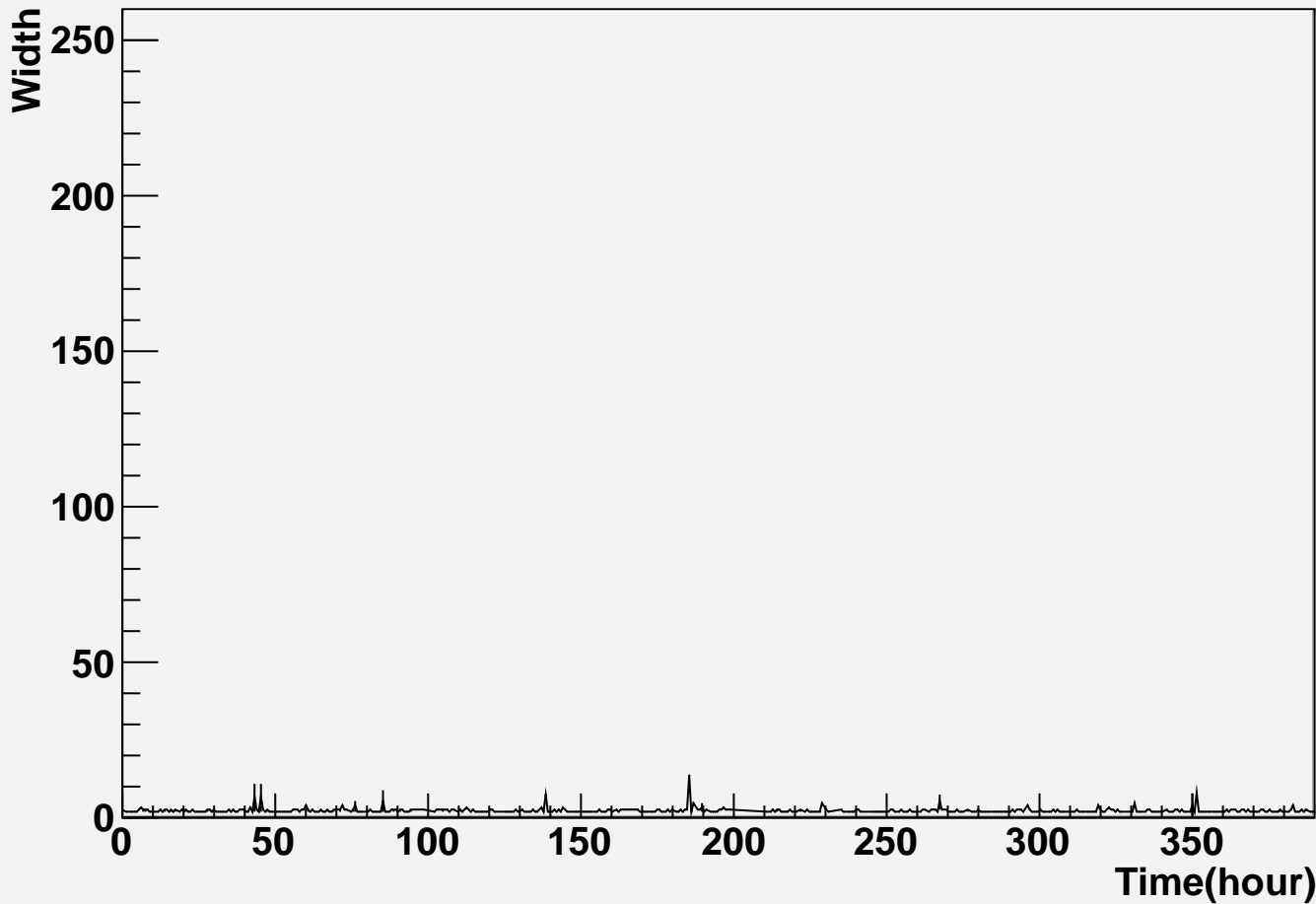
width-S3-SFEC01-Ch1



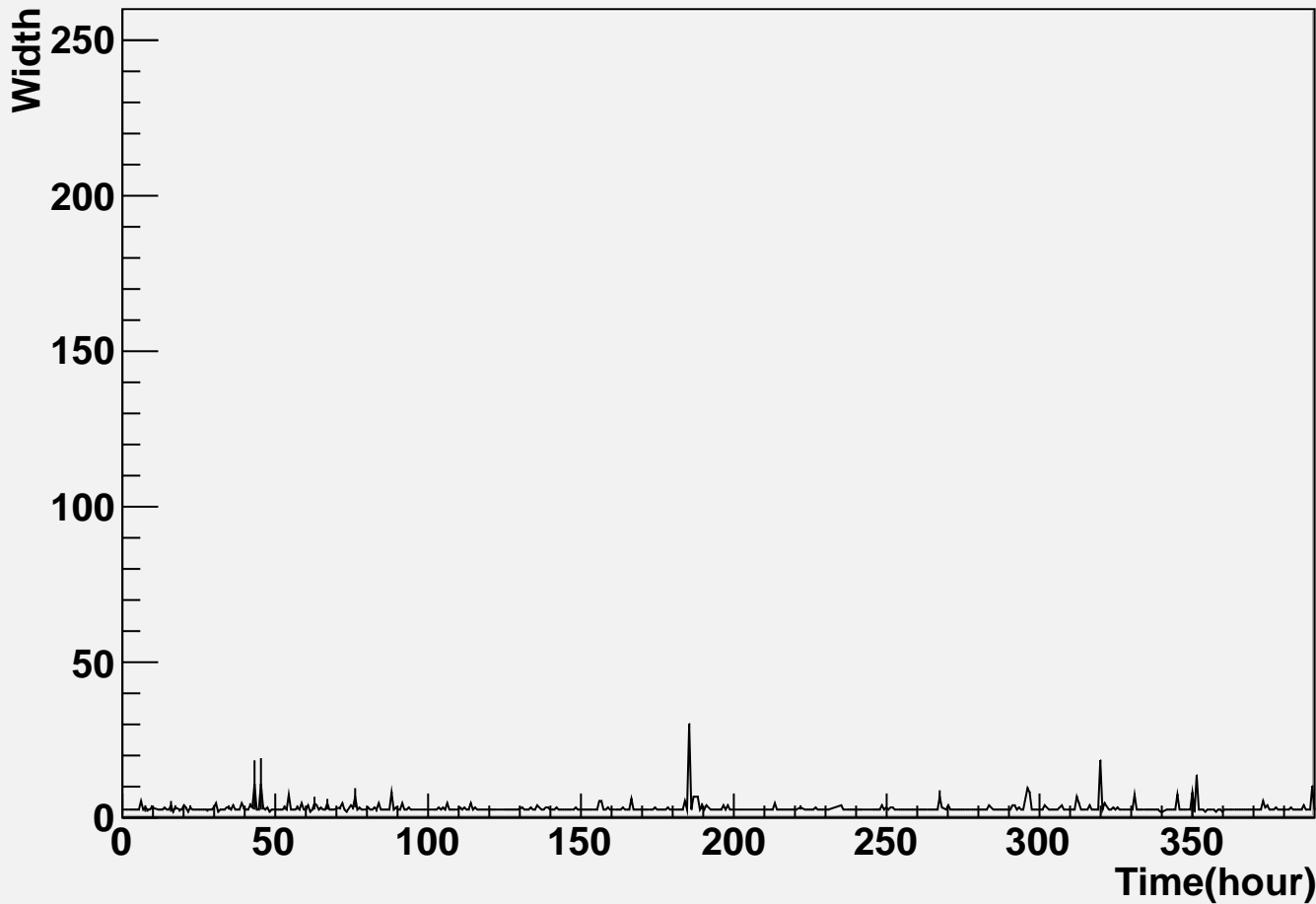
width-S3-SFEC01-Ch2



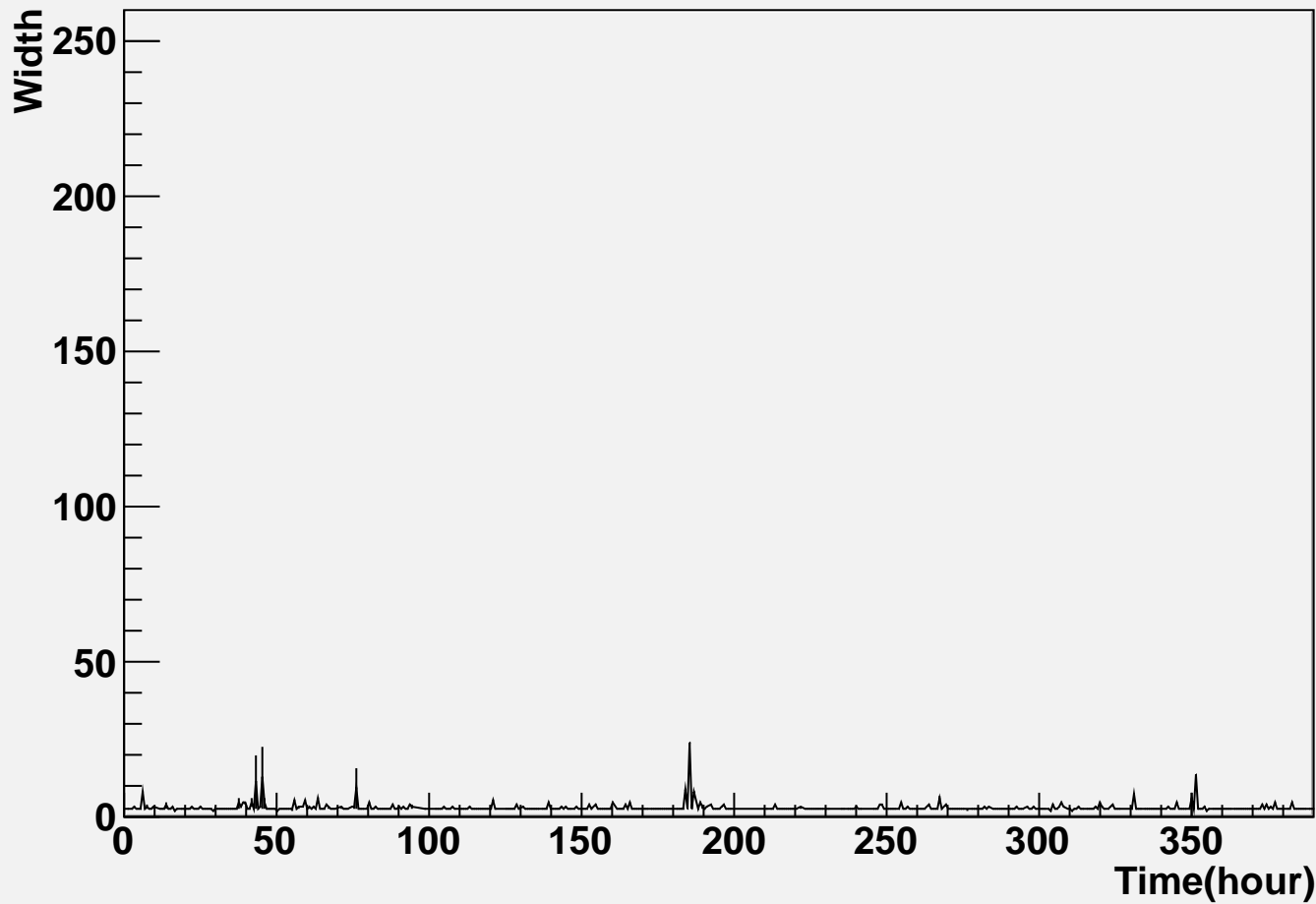
width-S3-SFEC01-Ch3



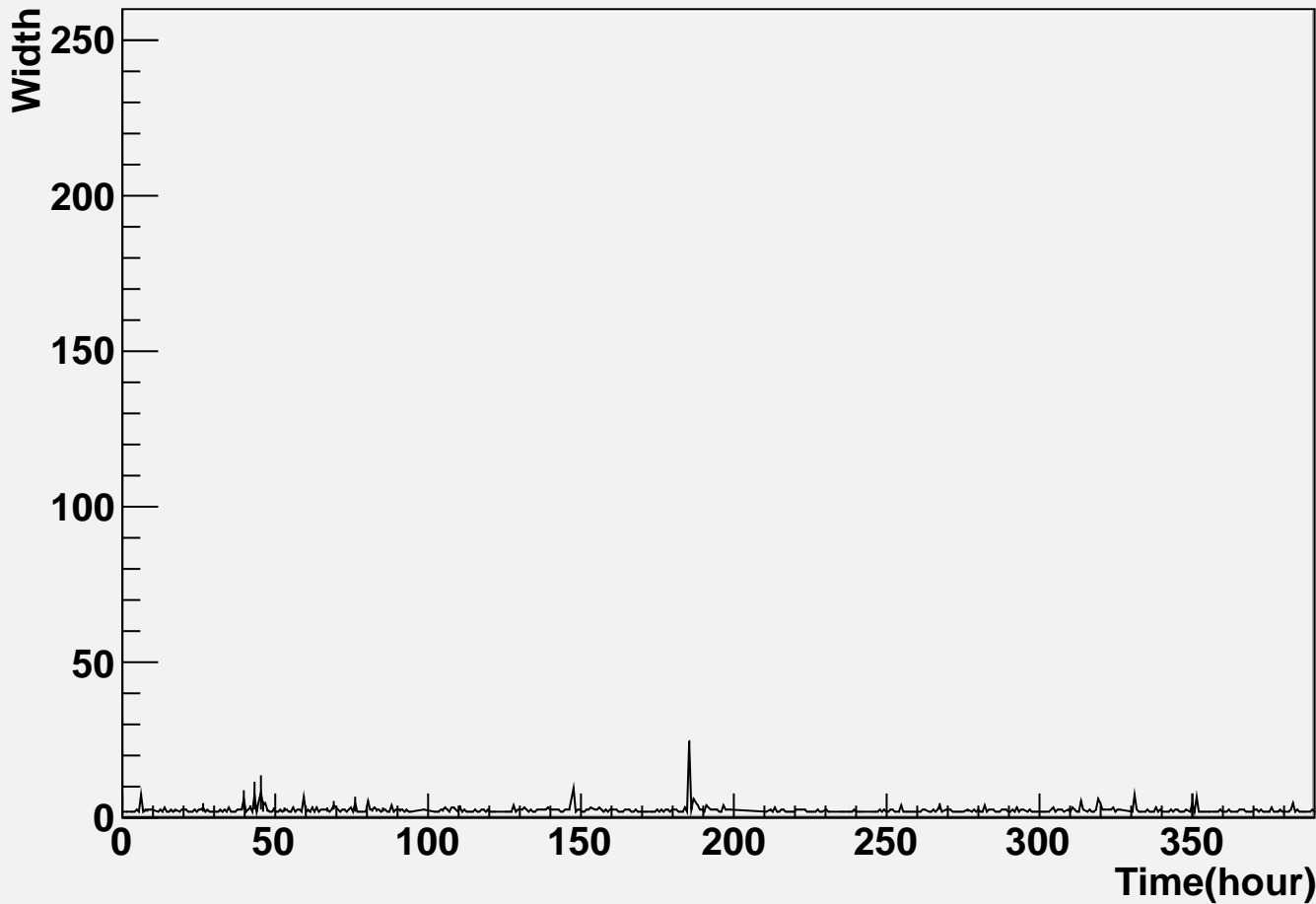
width-S3-SFEC01-Ch4



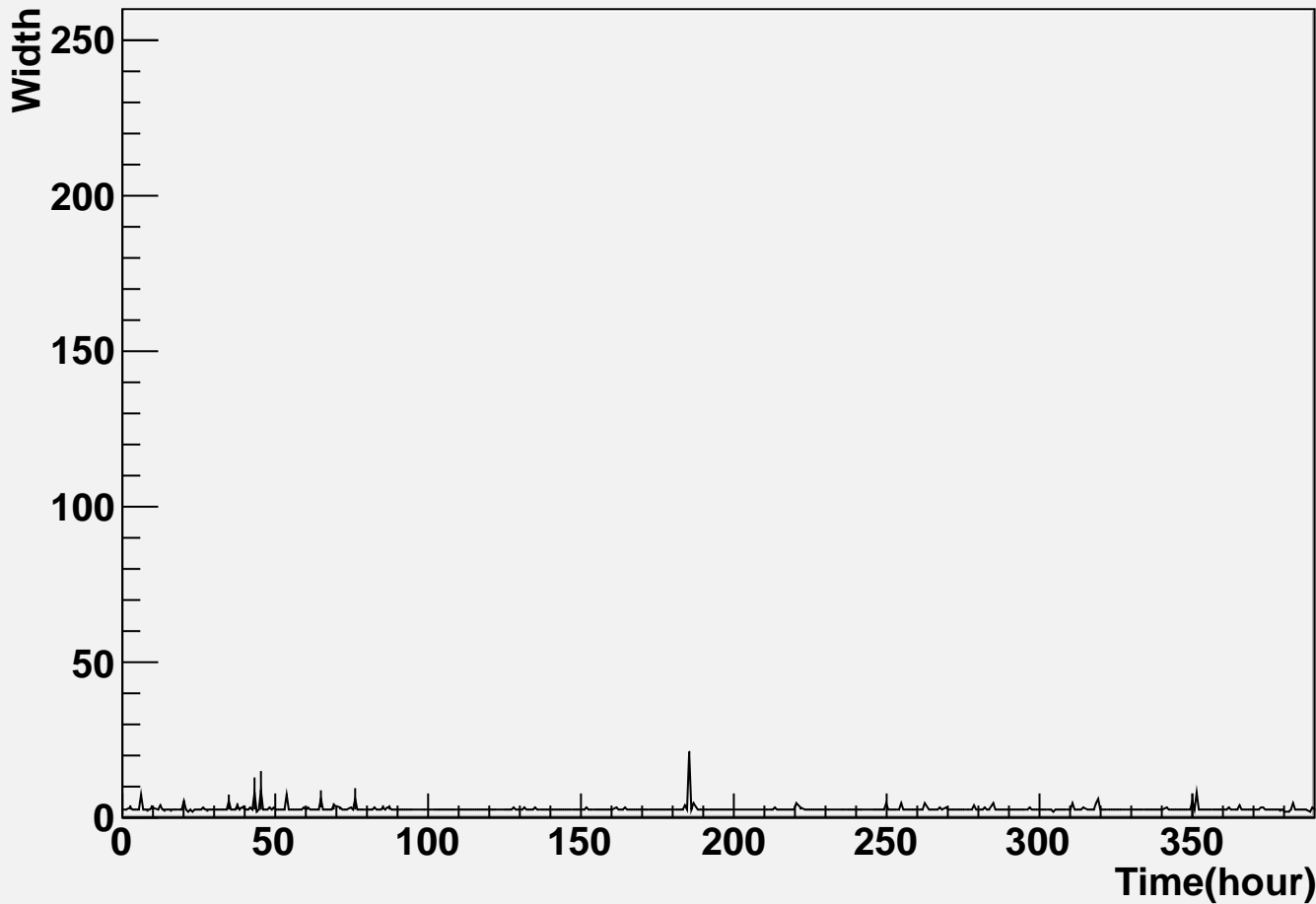
width-S3-SFEC01-Ch5



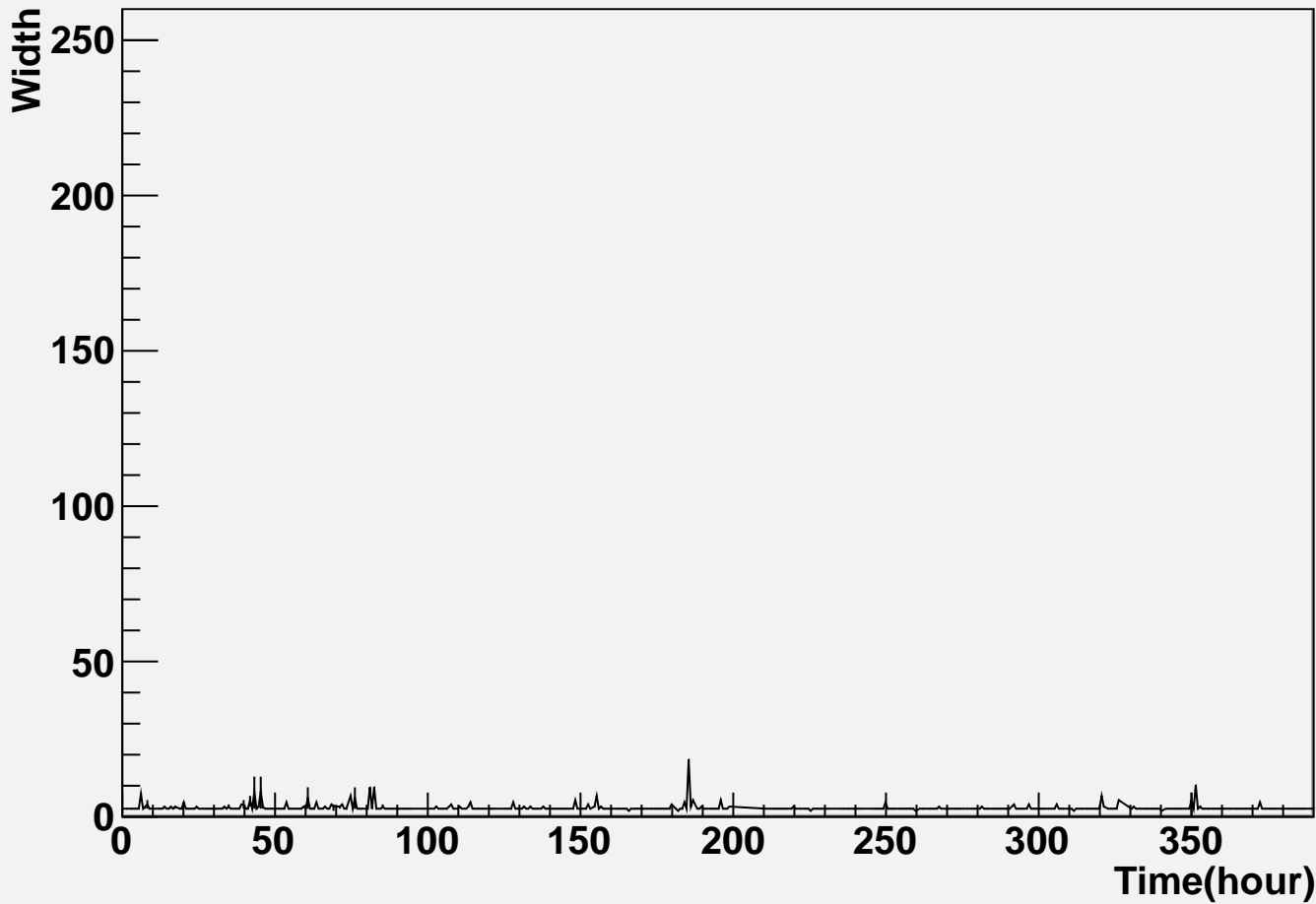
width-S3-SFEC01-Ch6



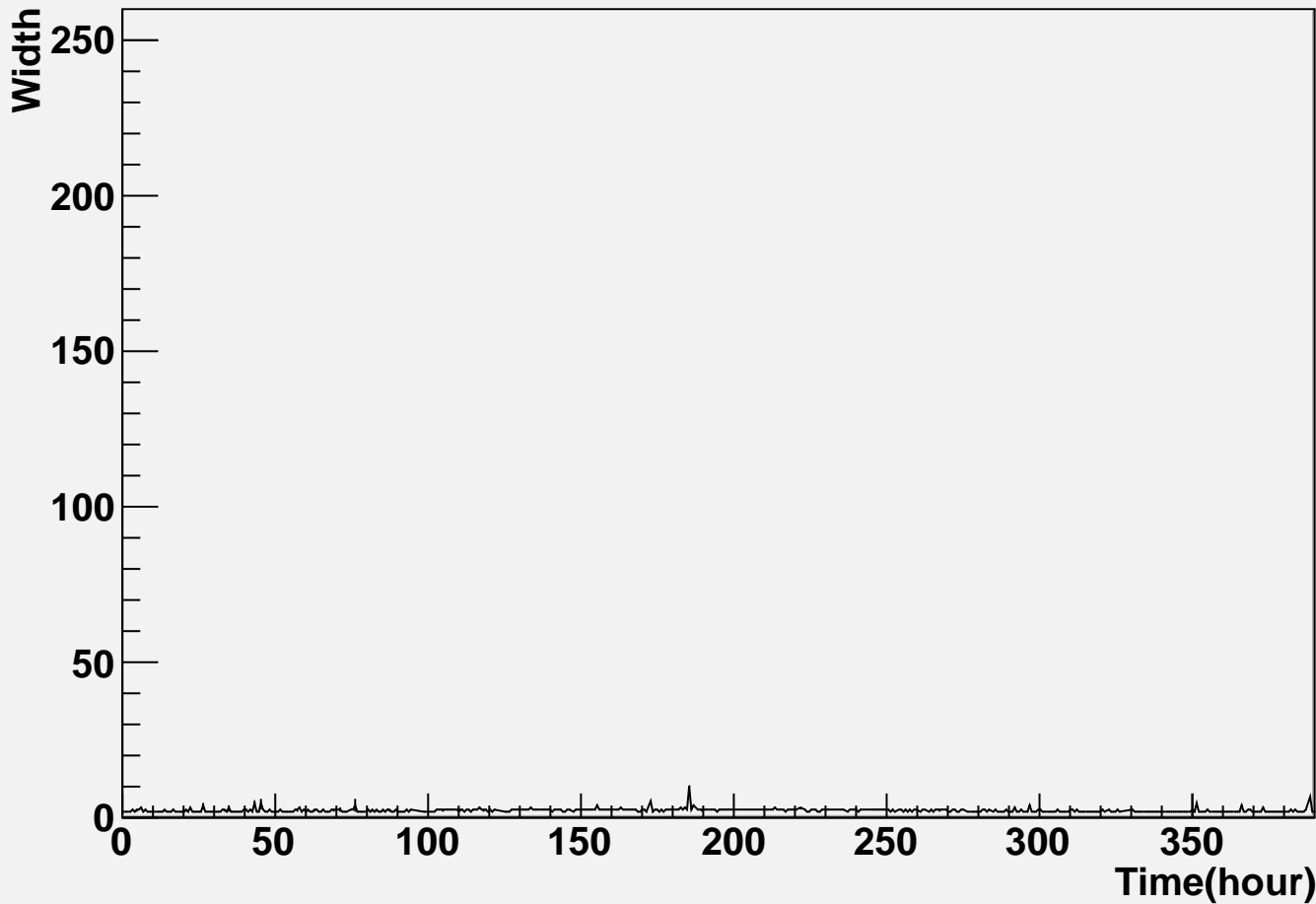
width-S3-SFEC01-Ch7



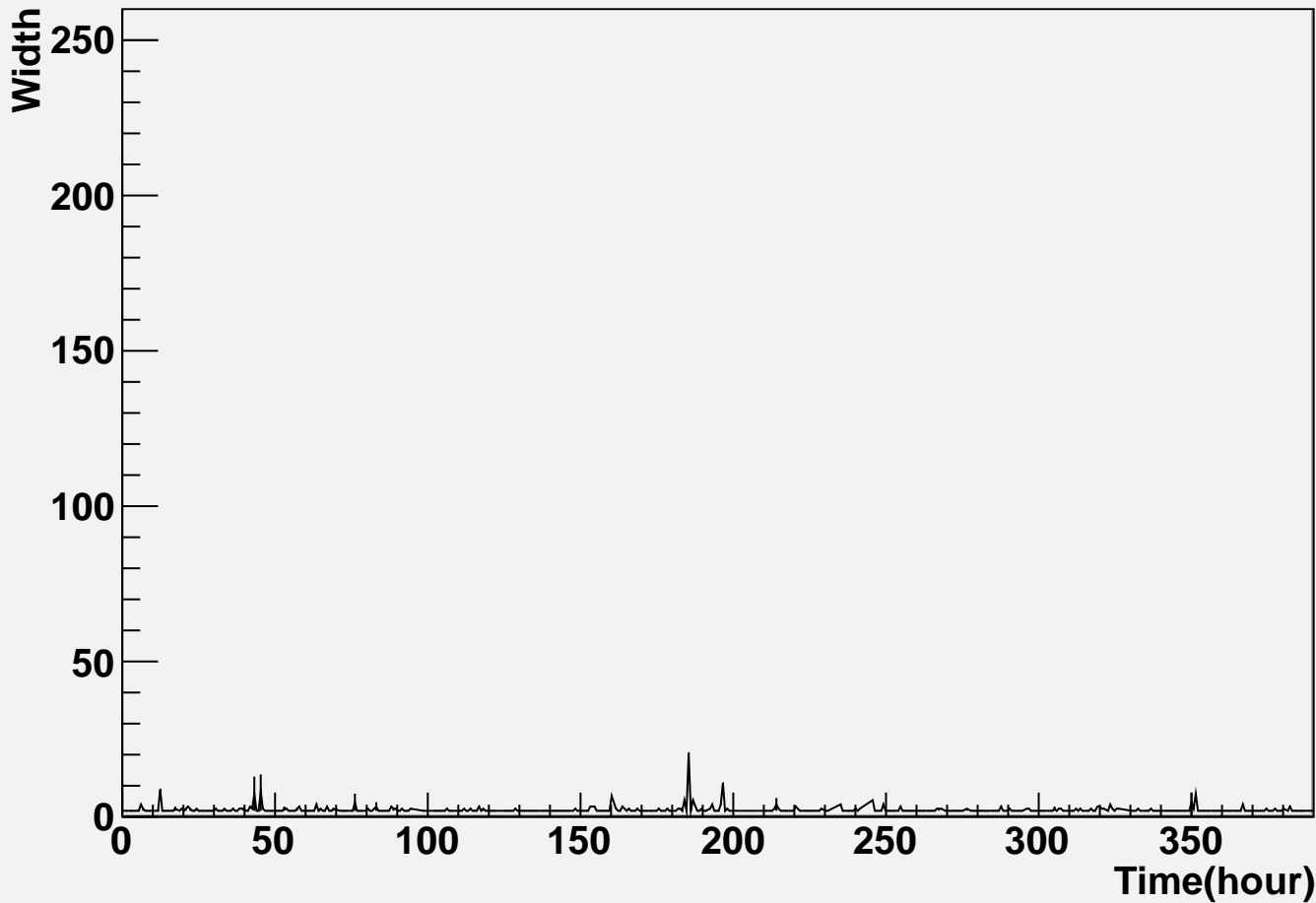
width-S3-SFEC01-Ch8



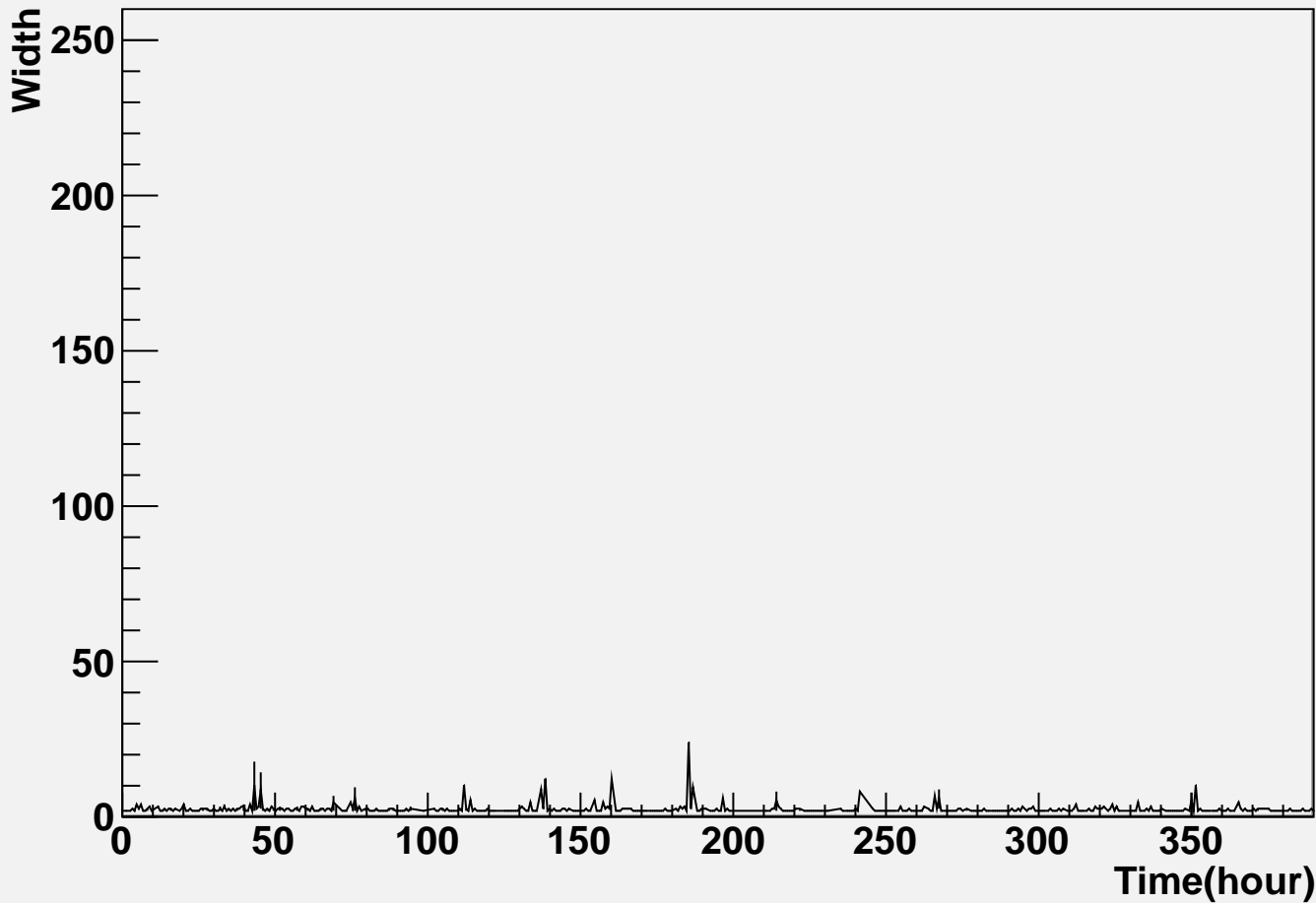
width-S3-SFEC01-Ch9



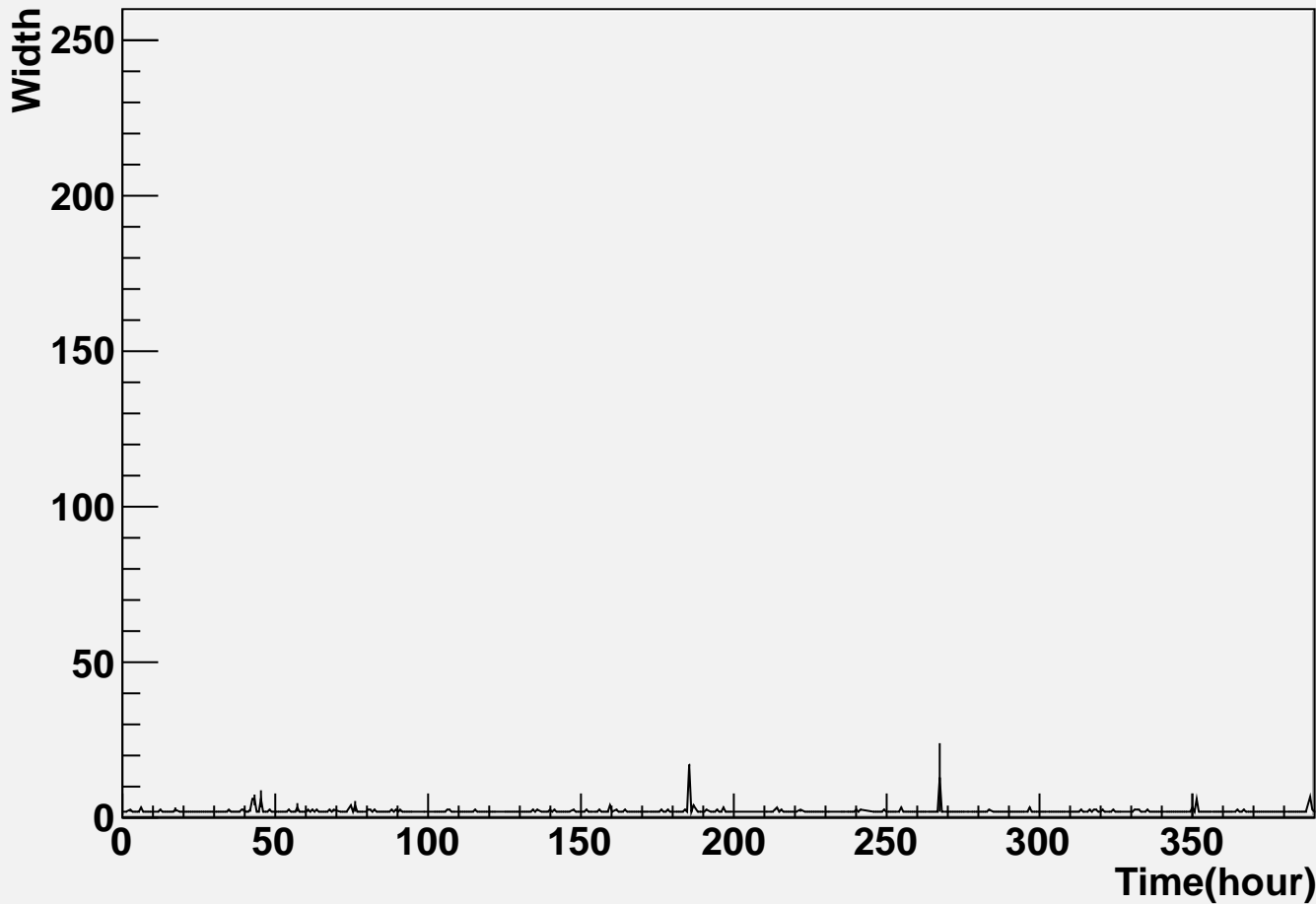
width-S3-SFEC10-Ch0



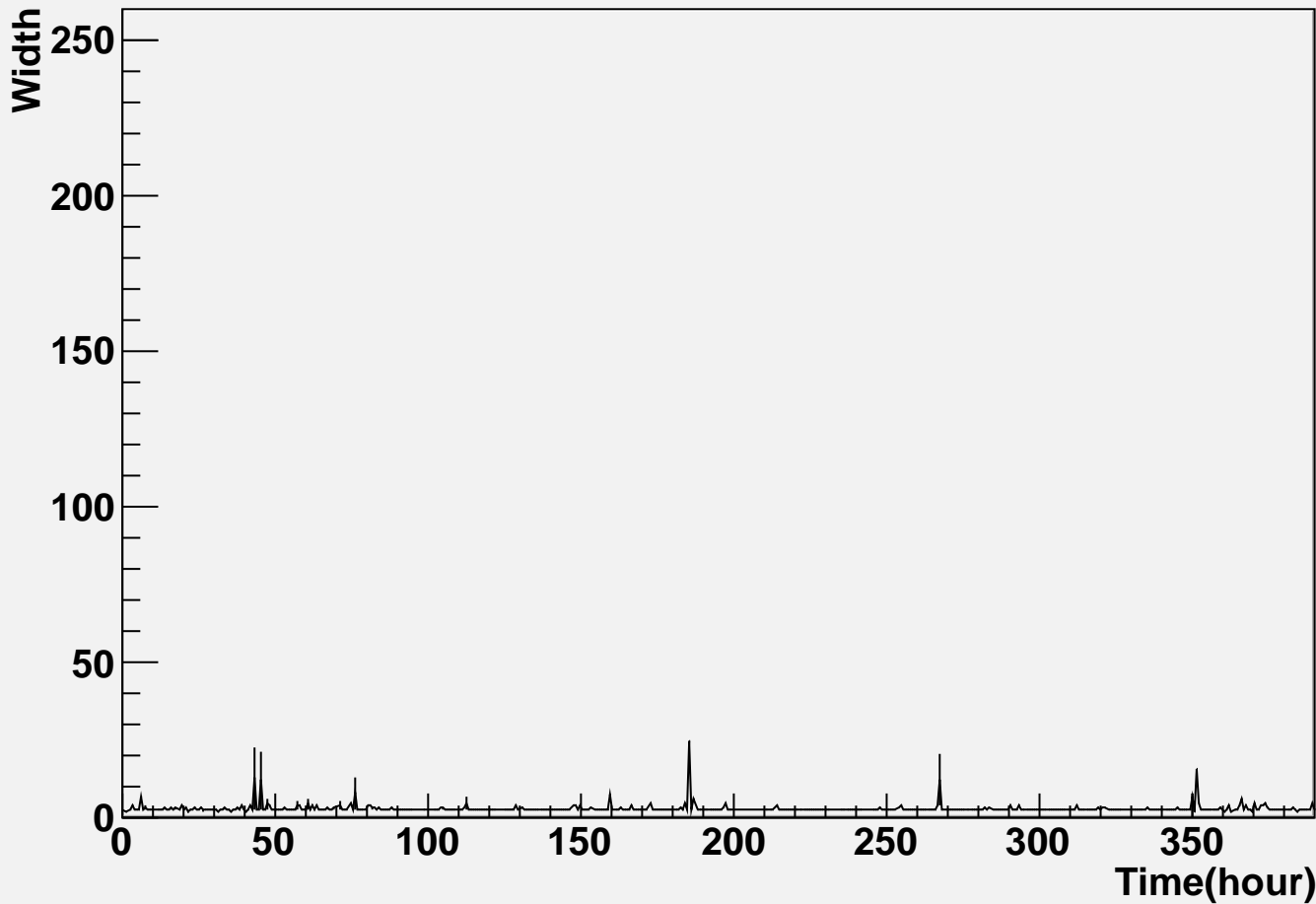
width-S3-SFEC10-Ch1



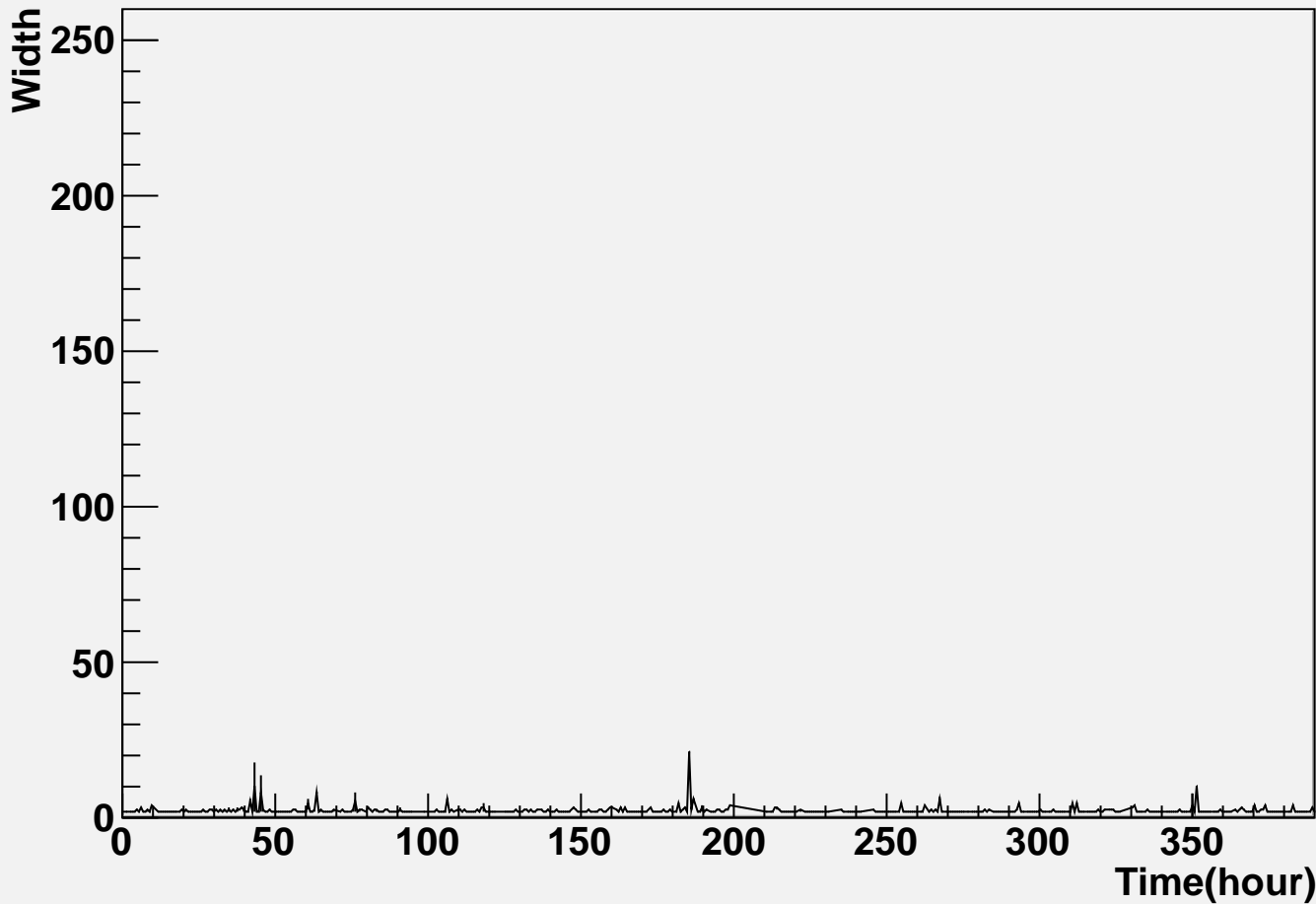
width-S3-SFEC10-Ch2



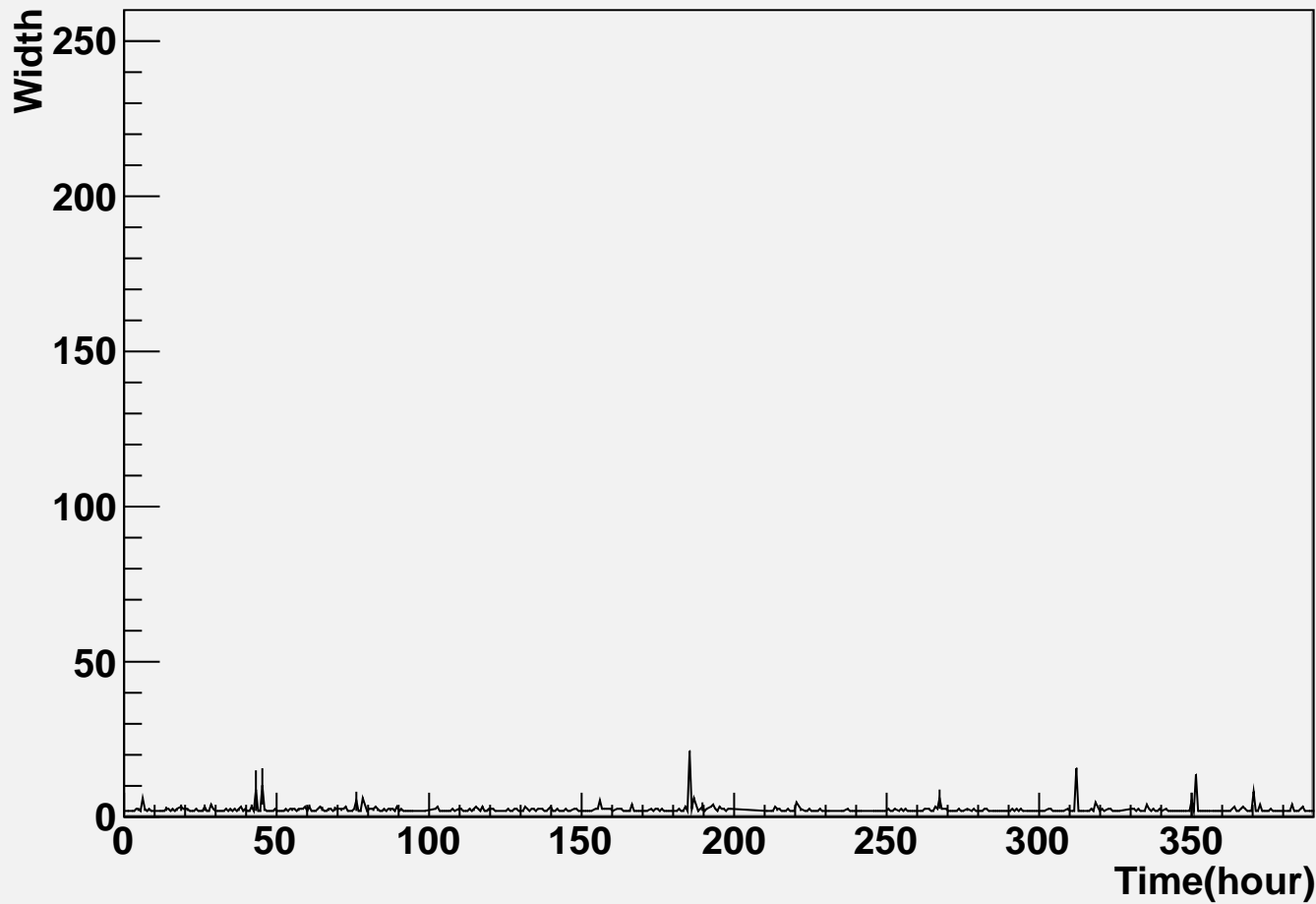
width-S3-SFEC10-Ch3



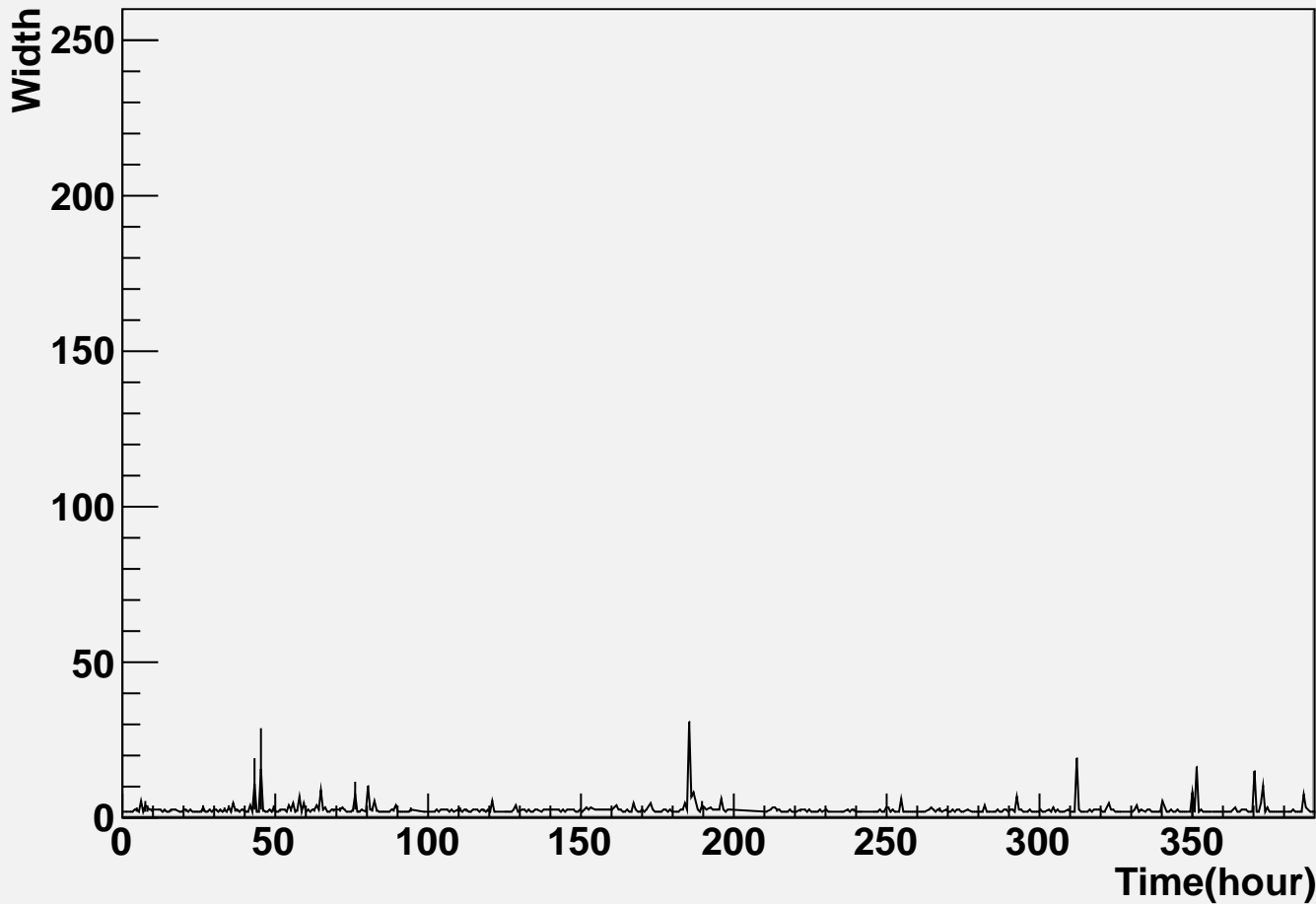
width-S3-SFEC10-Ch4



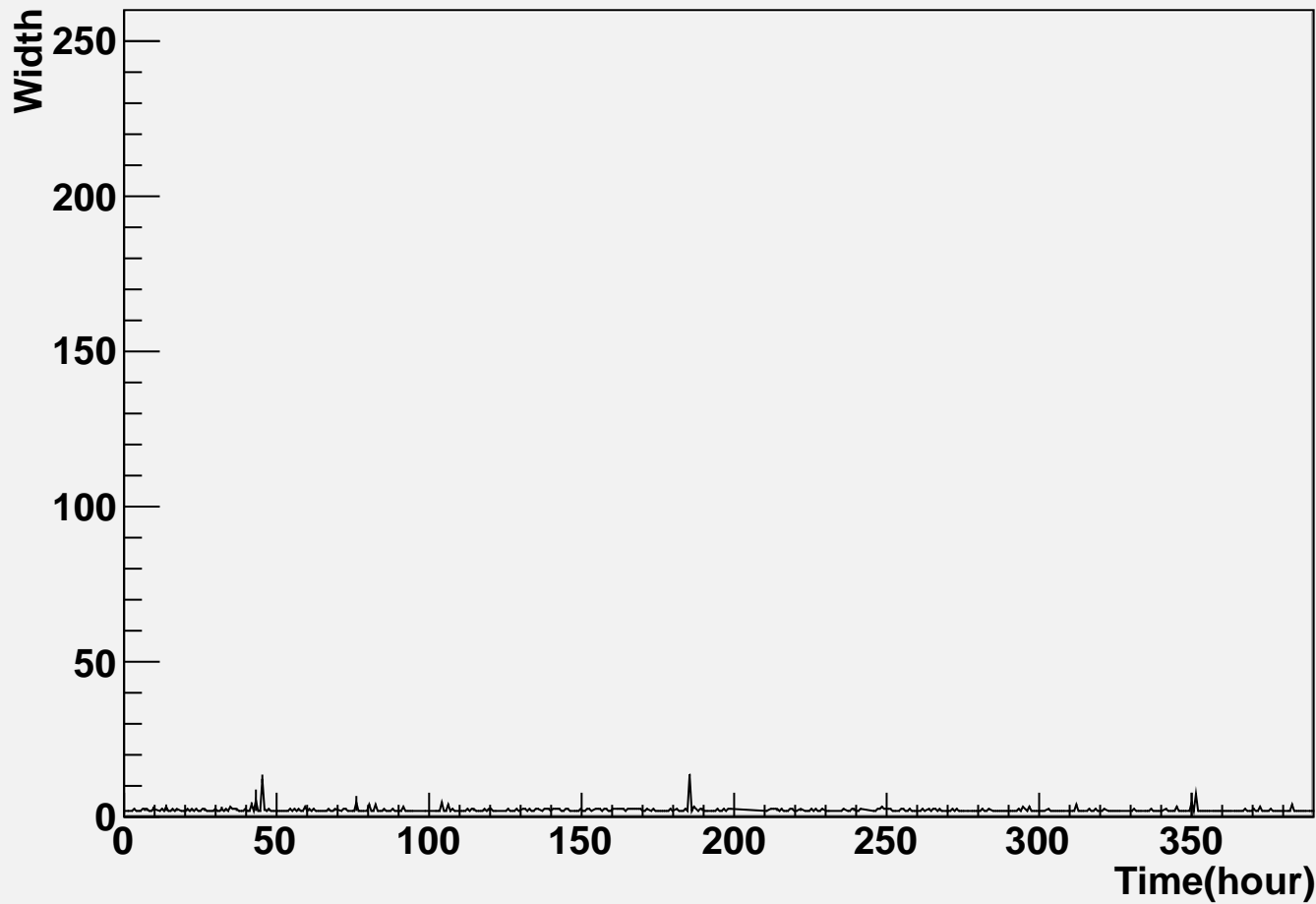
width-S3-SFEC10-Ch5



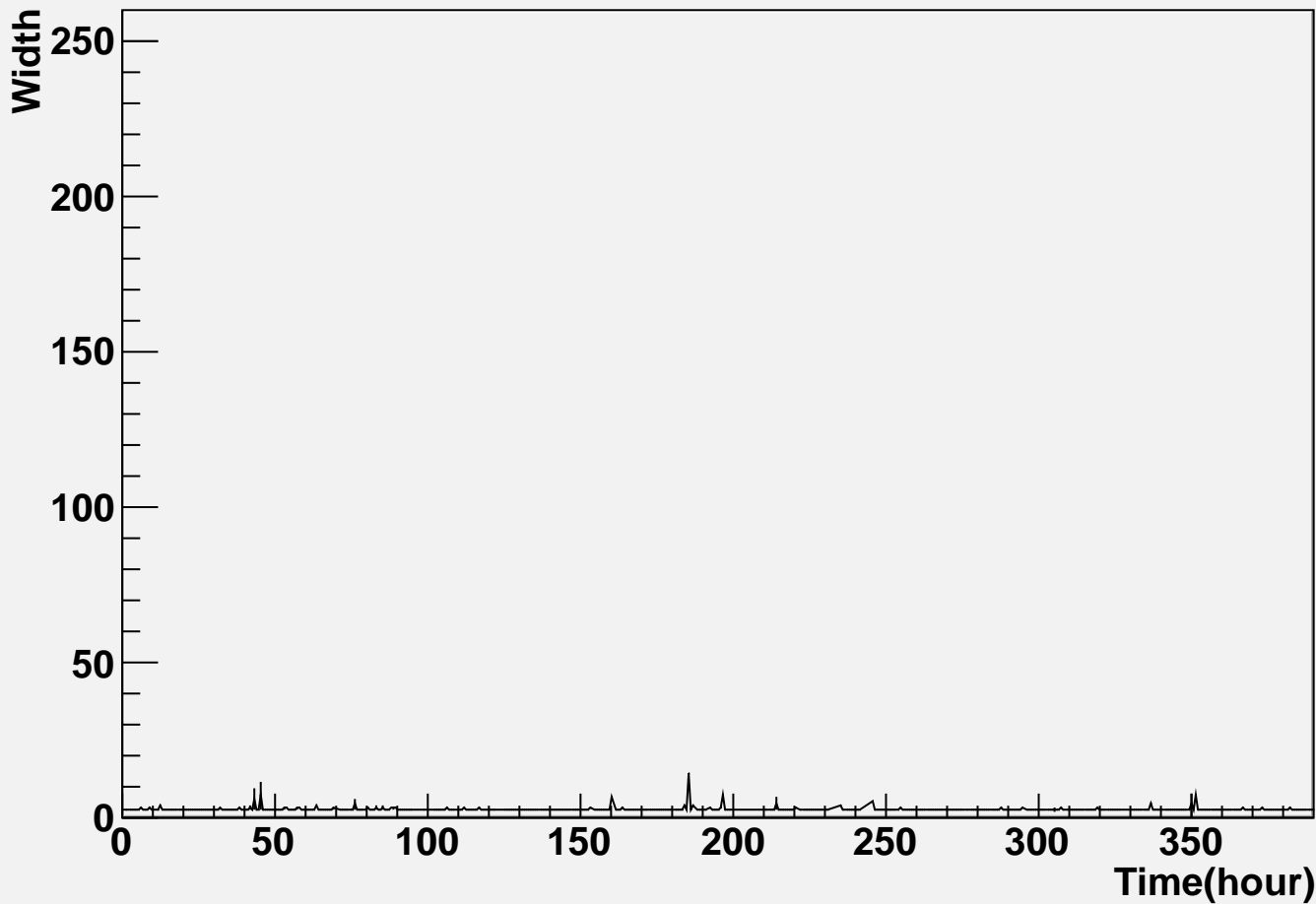
width-S3-SFEC10-Ch6



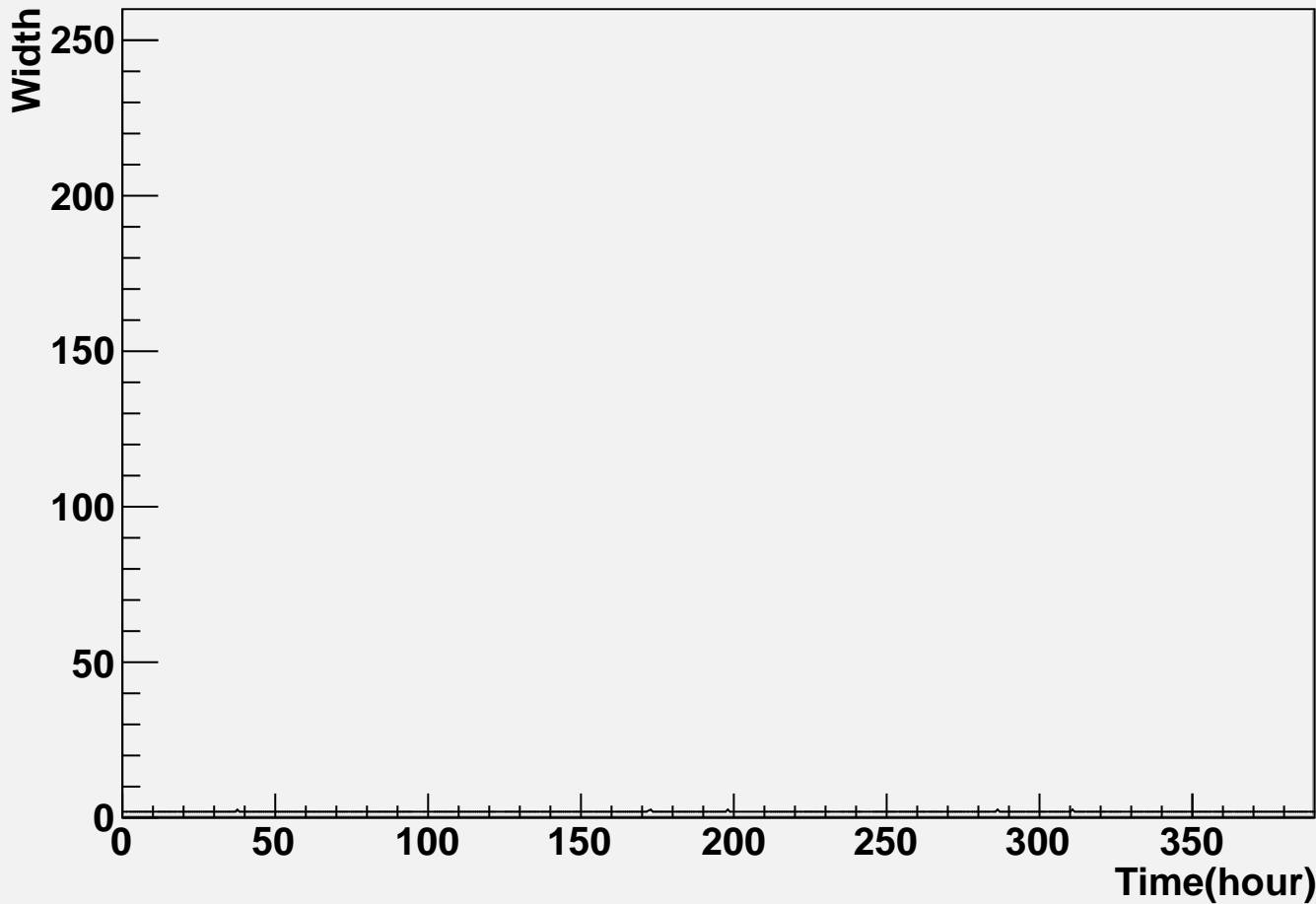
width-S3-SFEC10-Ch7



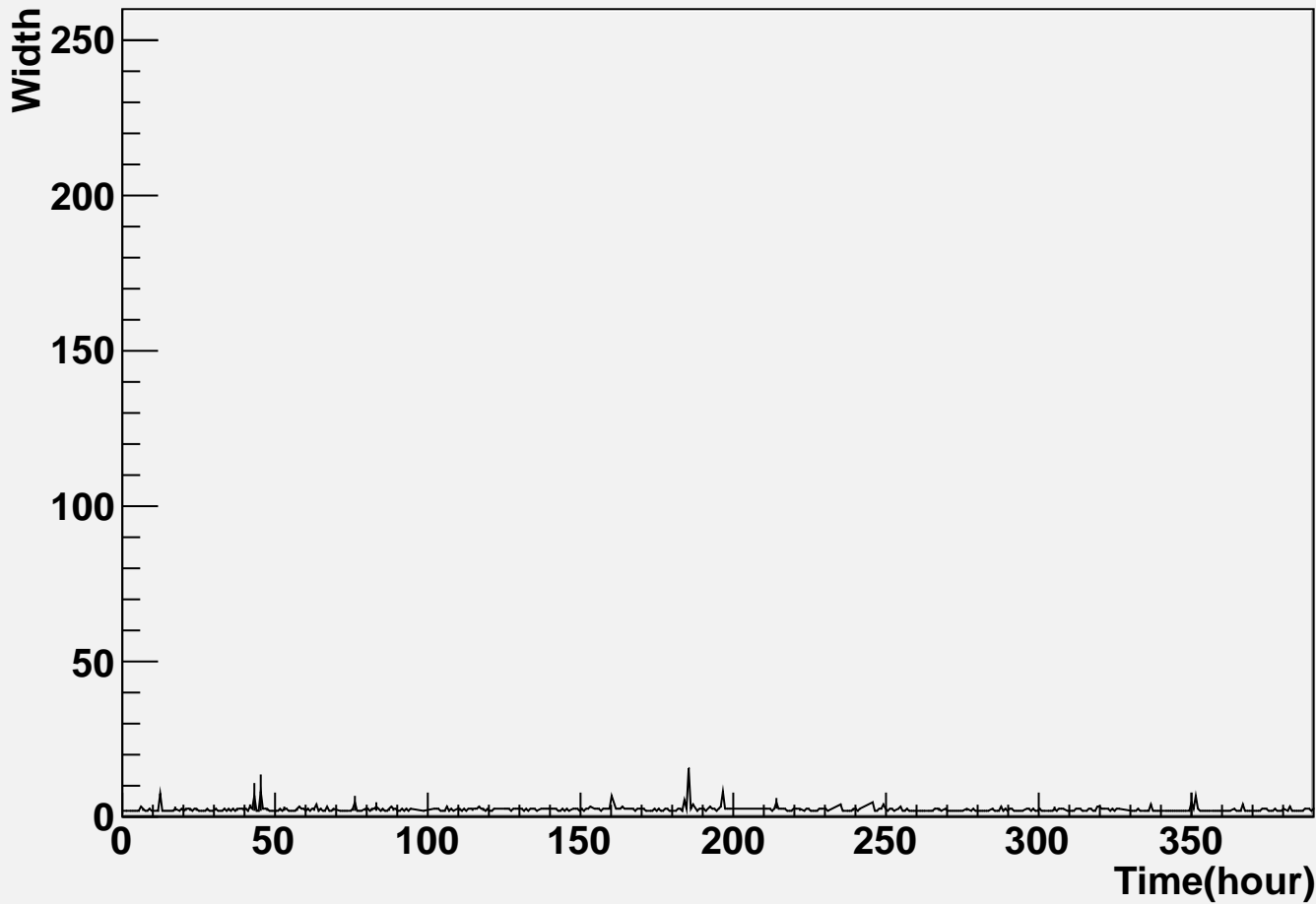
width-S3-SFEC10-Ch8



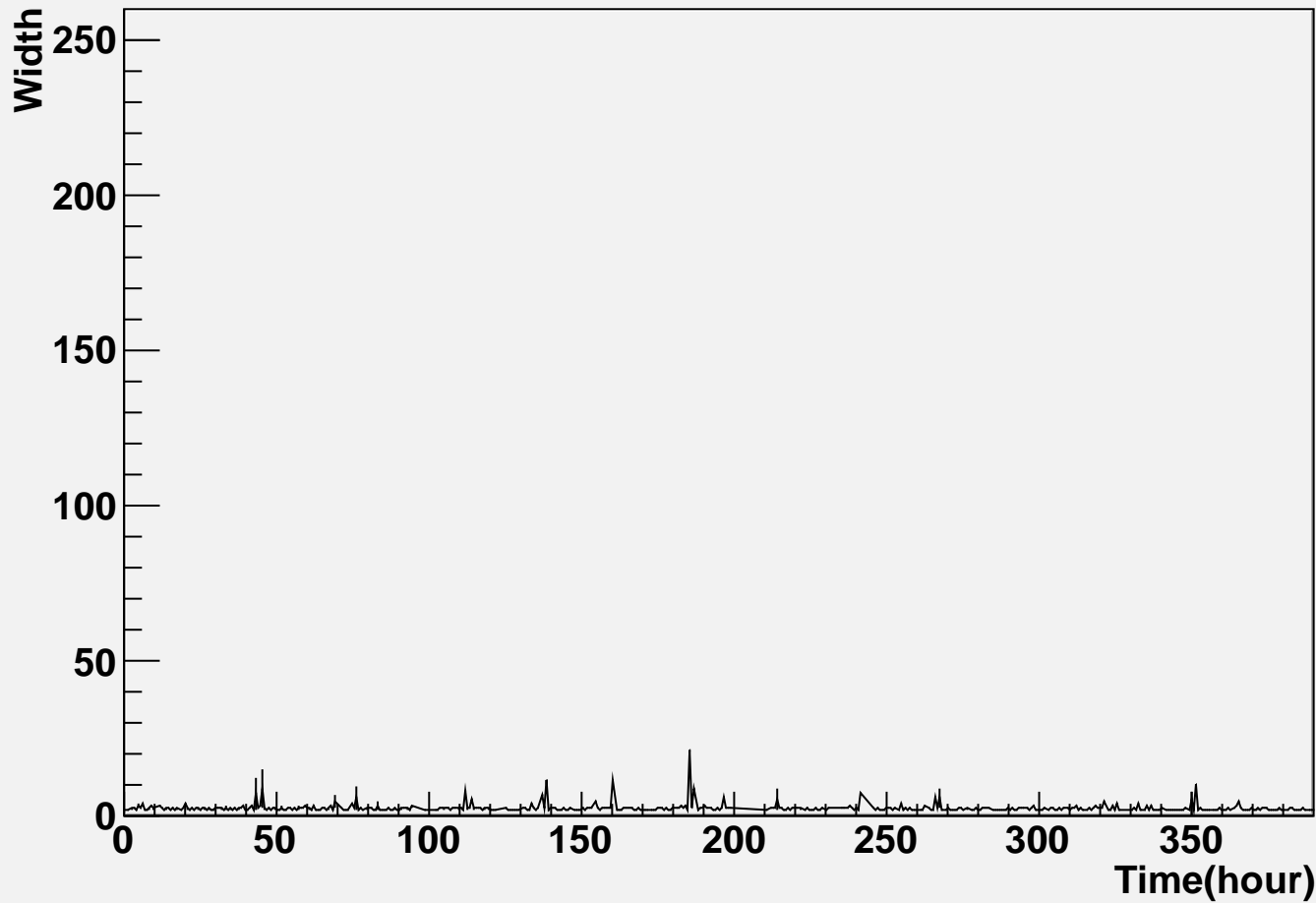
width-S3-SFEC10-Ch9



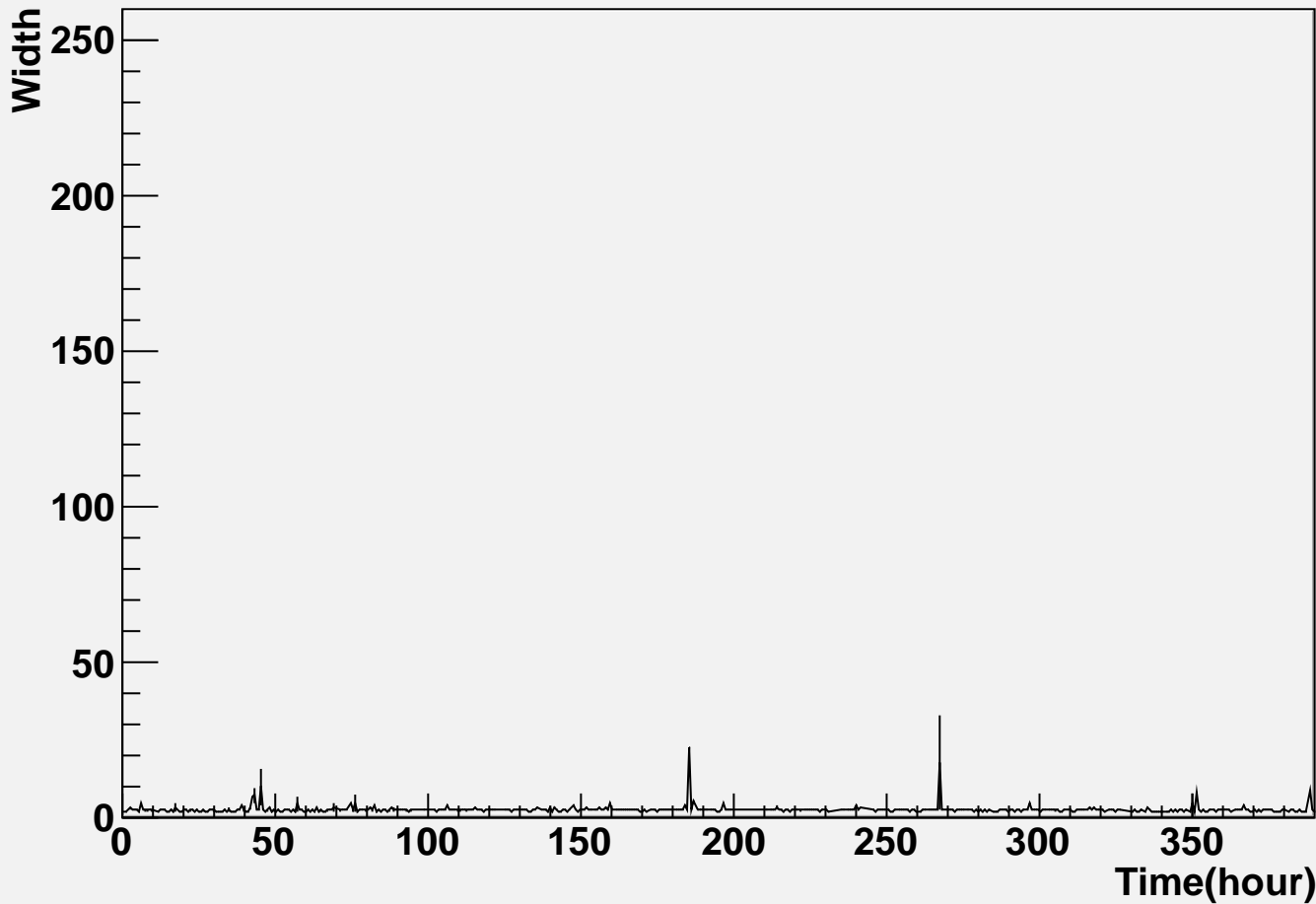
width-S3-SFEC11-Ch0



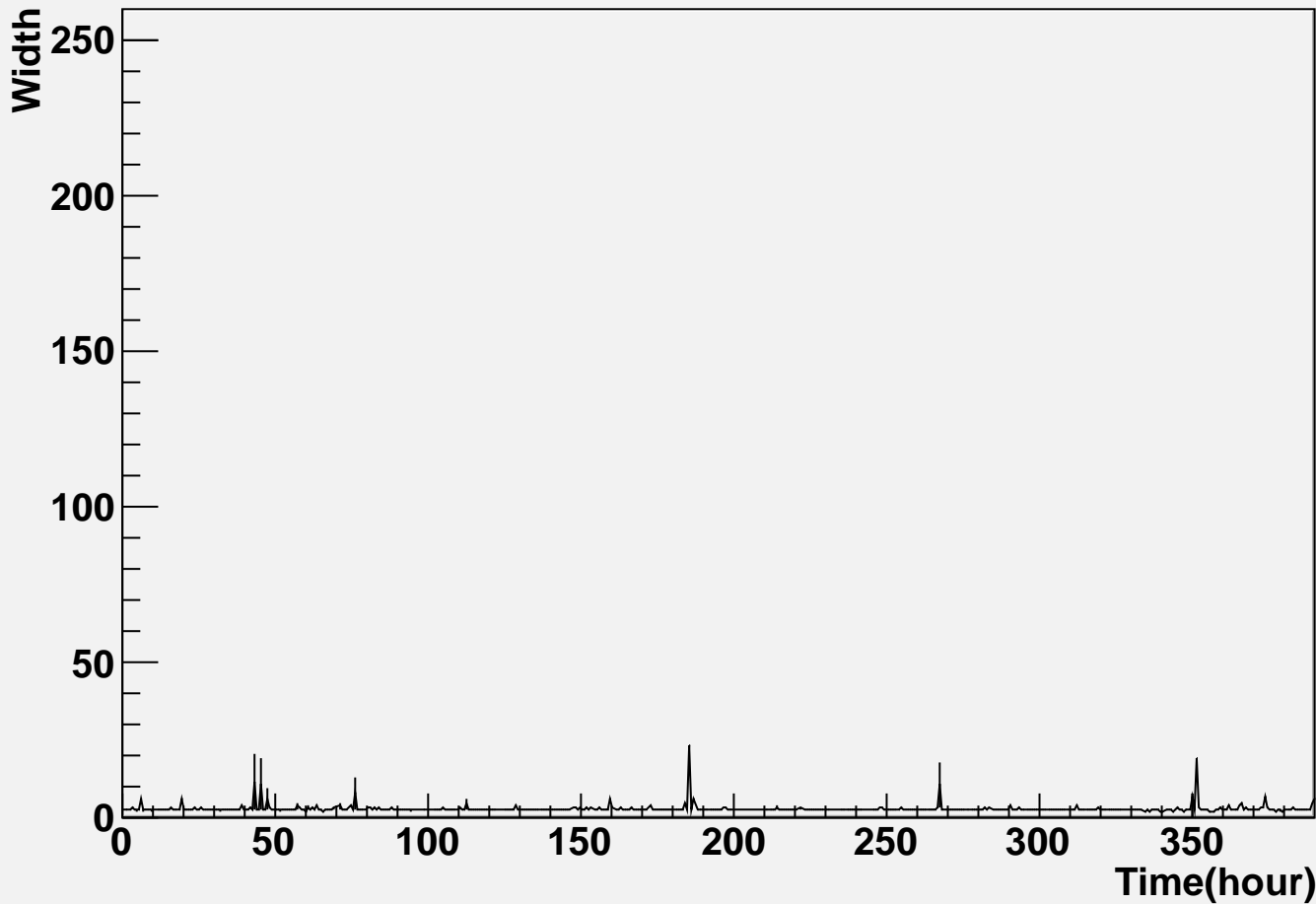
width-S3-SFEC11-Ch1



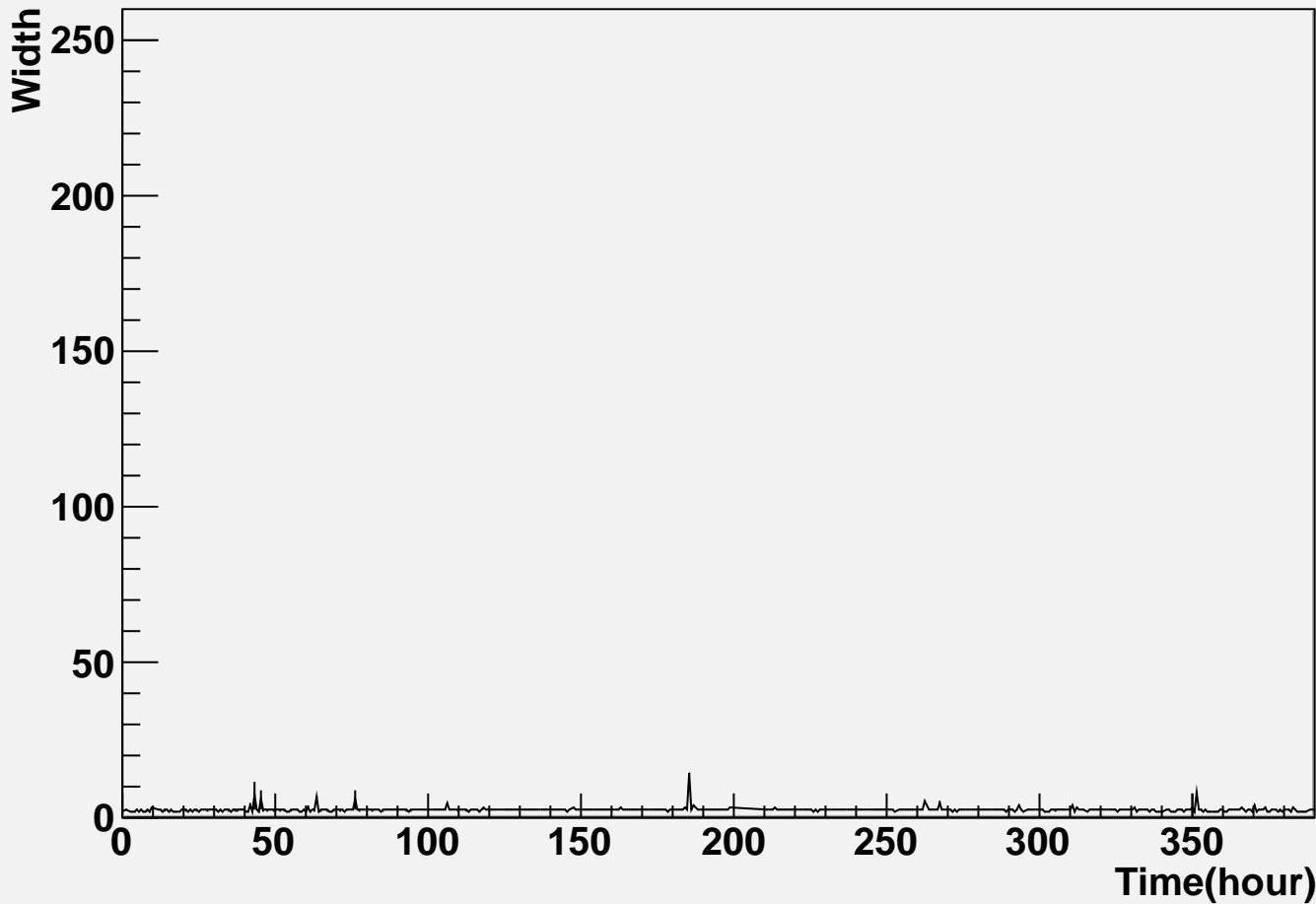
width-S3-SFEC11-Ch2



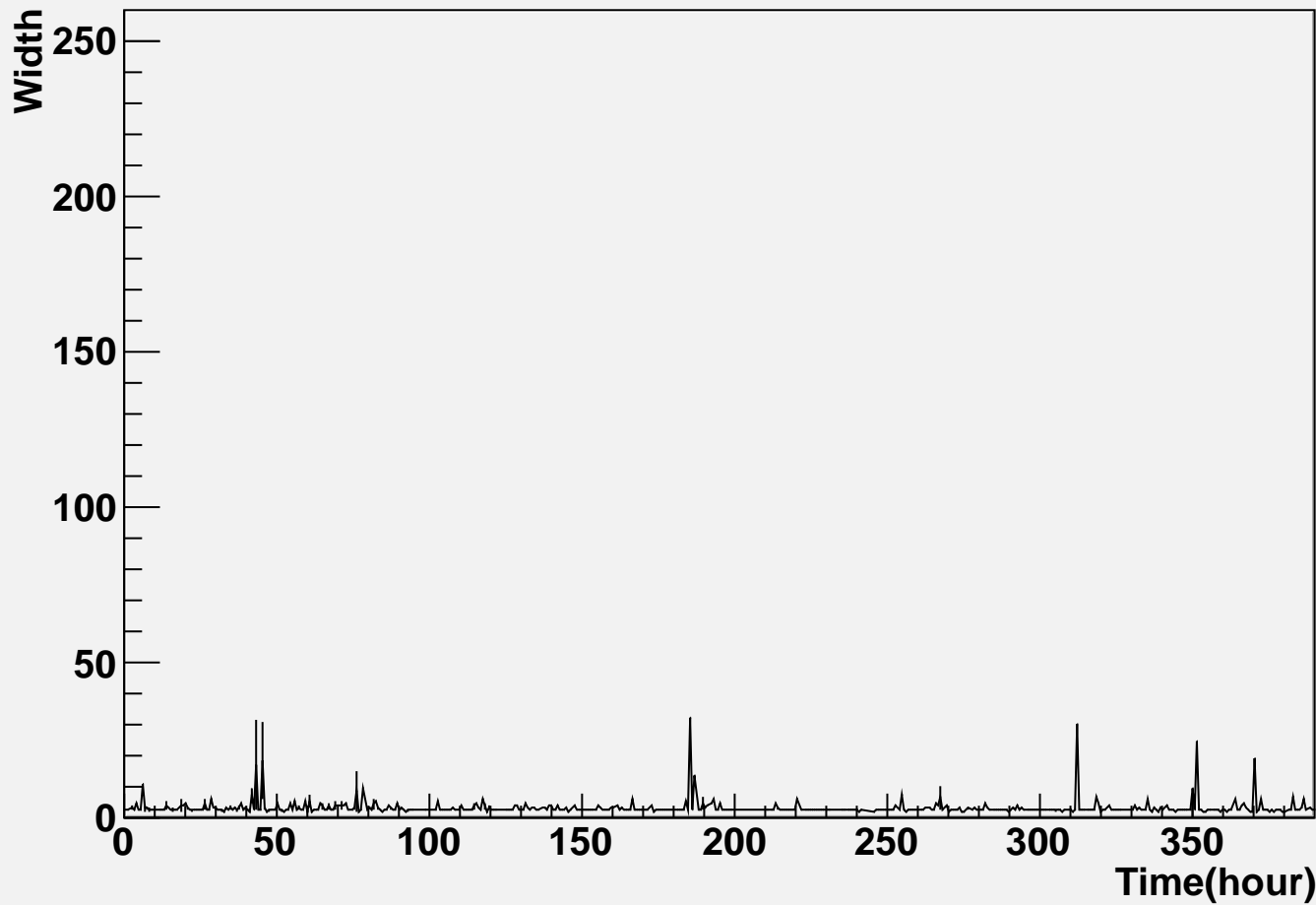
width-S3-SFEC11-Ch3



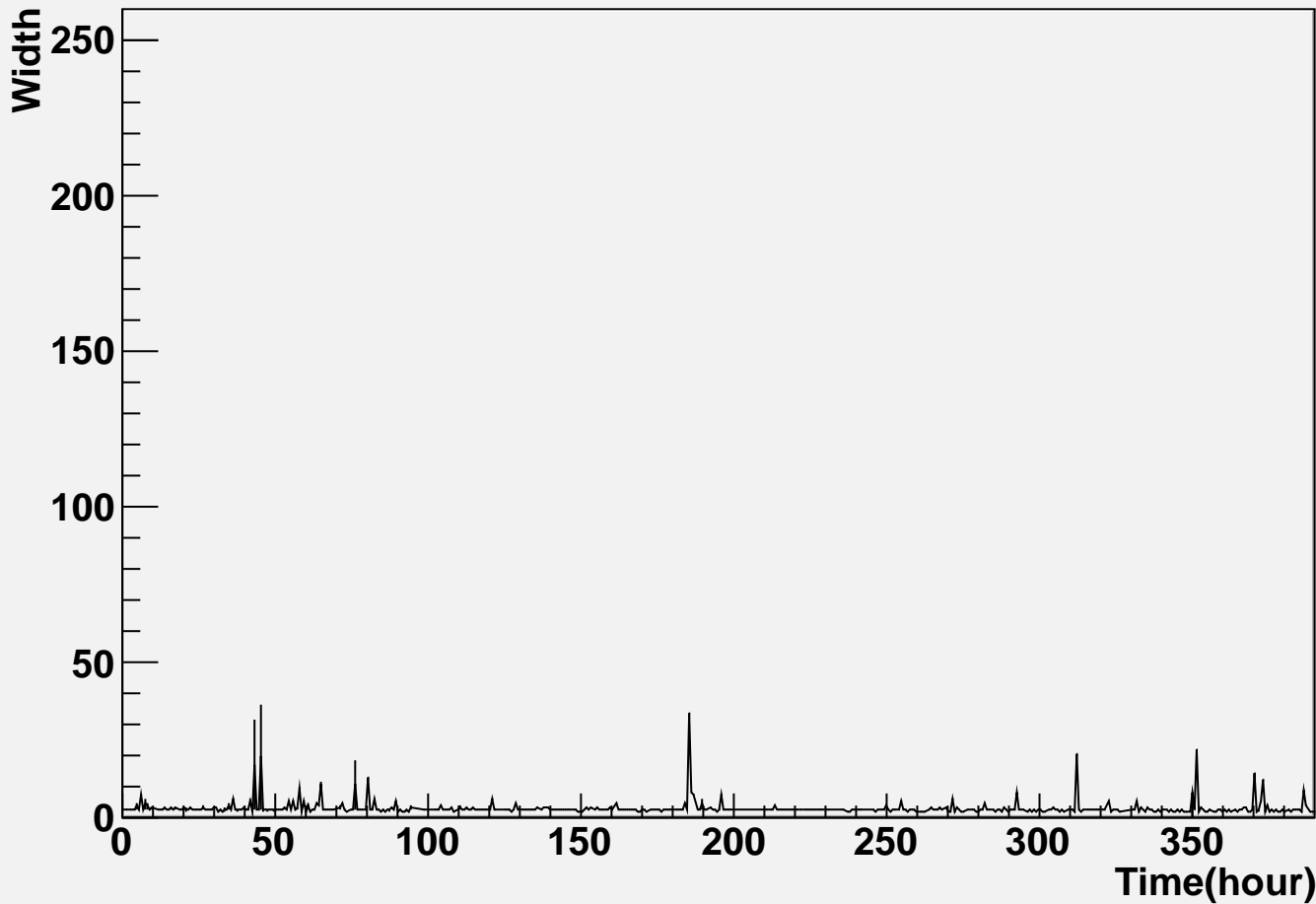
width-S3-SFEC11-Ch4



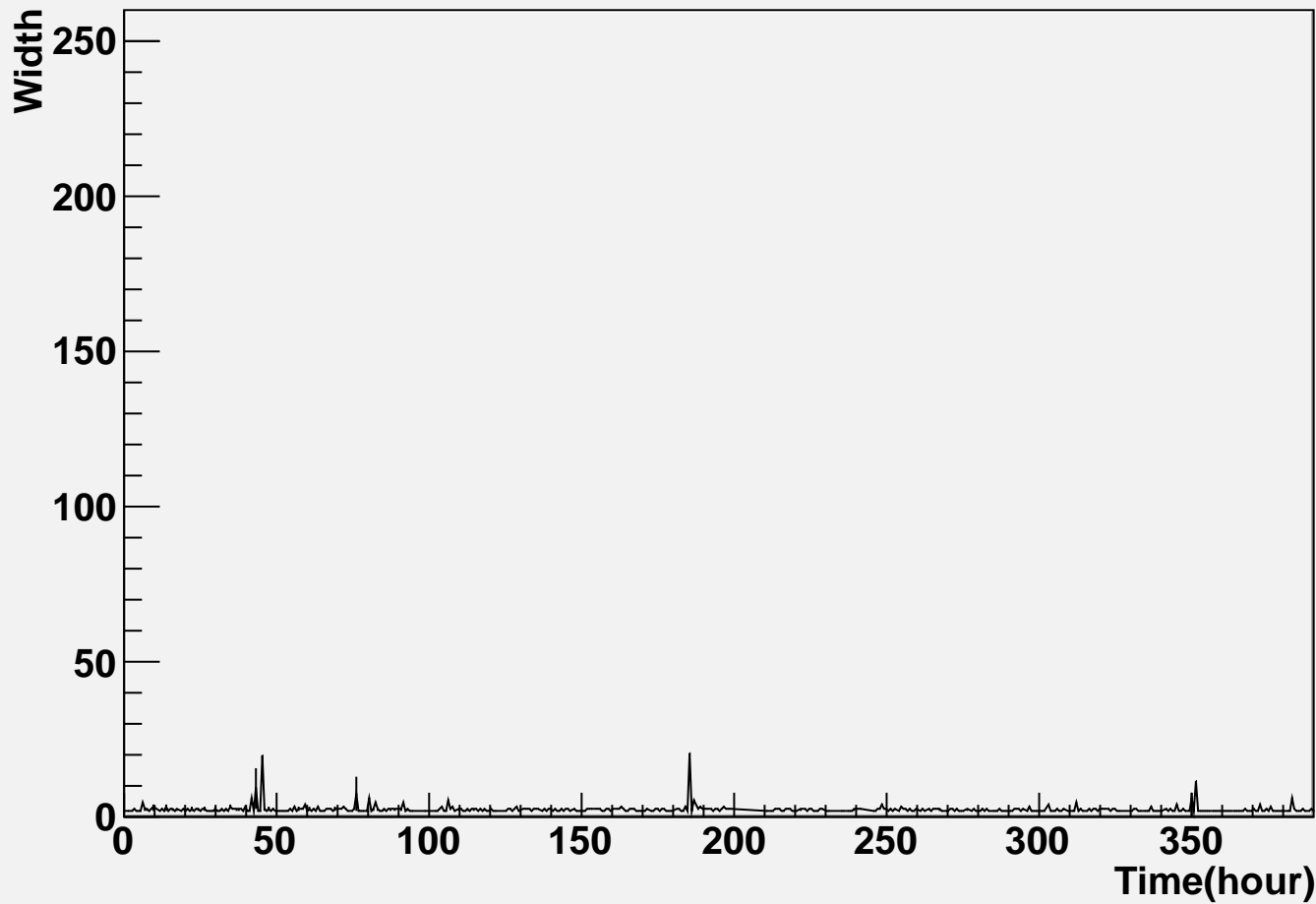
width-S3-SFEC11-Ch5



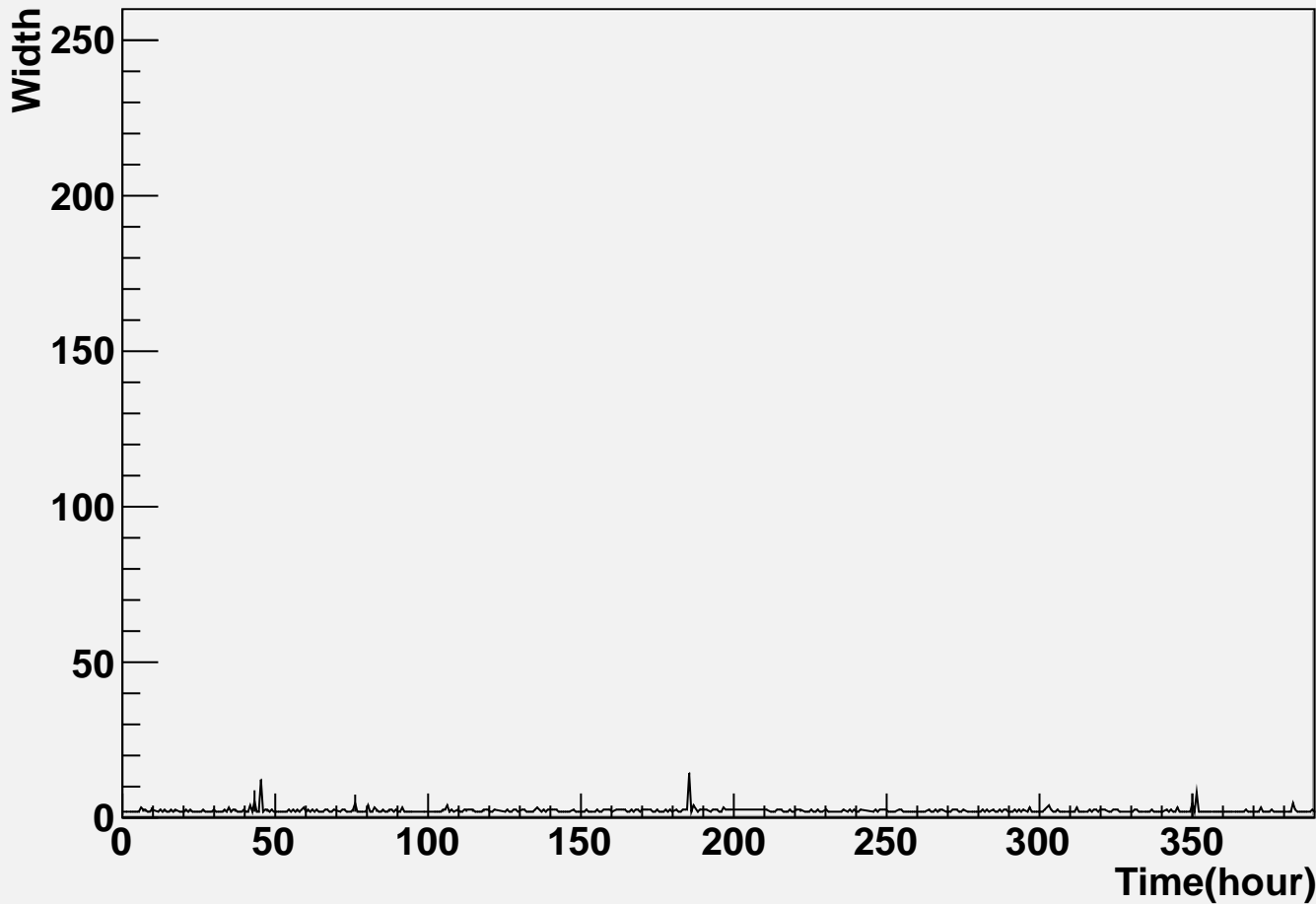
width-S3-SFEC11-Ch6



width-S3-SFEC11-Ch7



width-S3-SFEC11-Ch8



width-S3-SFEC11-Ch9

